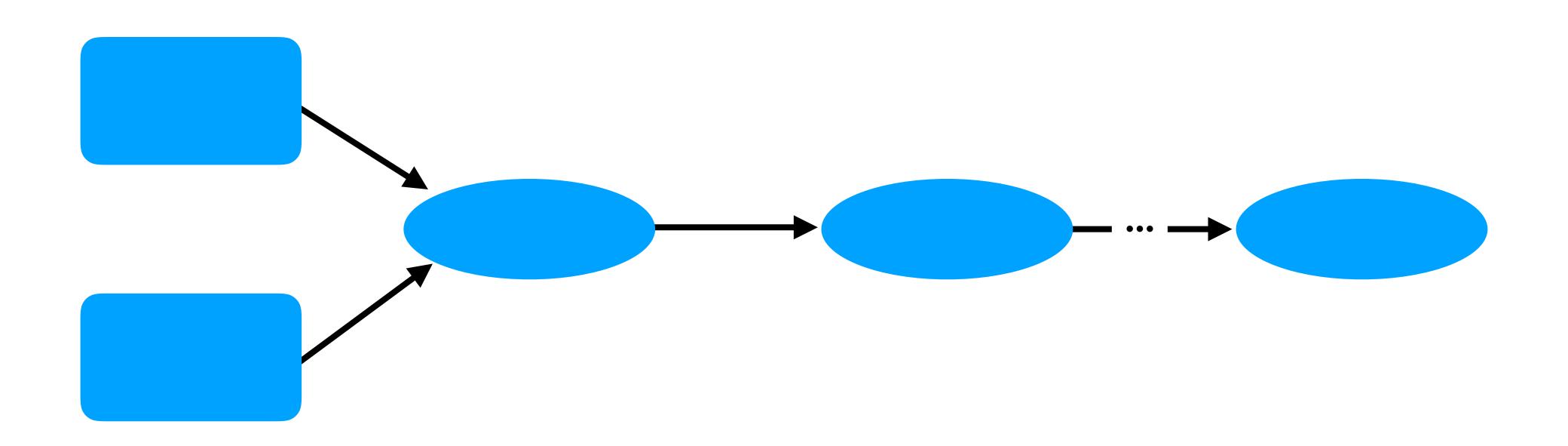
Program Analysis in Relay

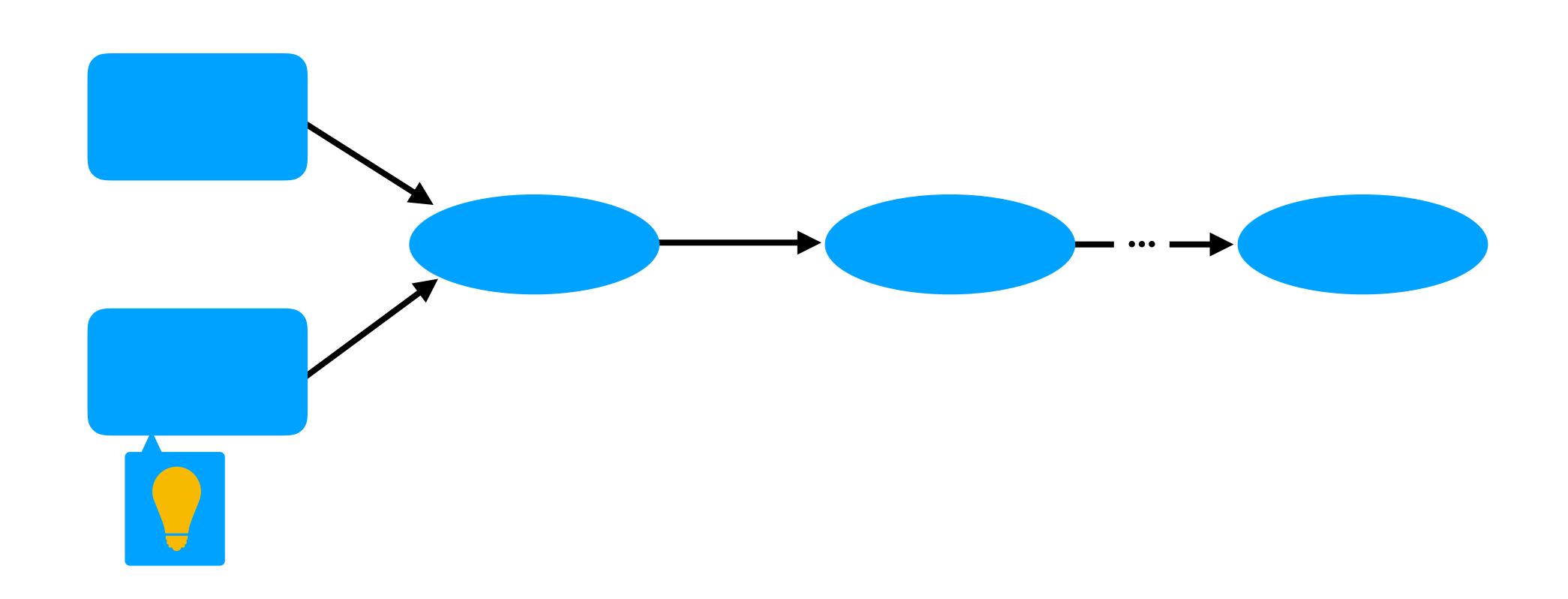
Gus Smith
December 5th, 2019

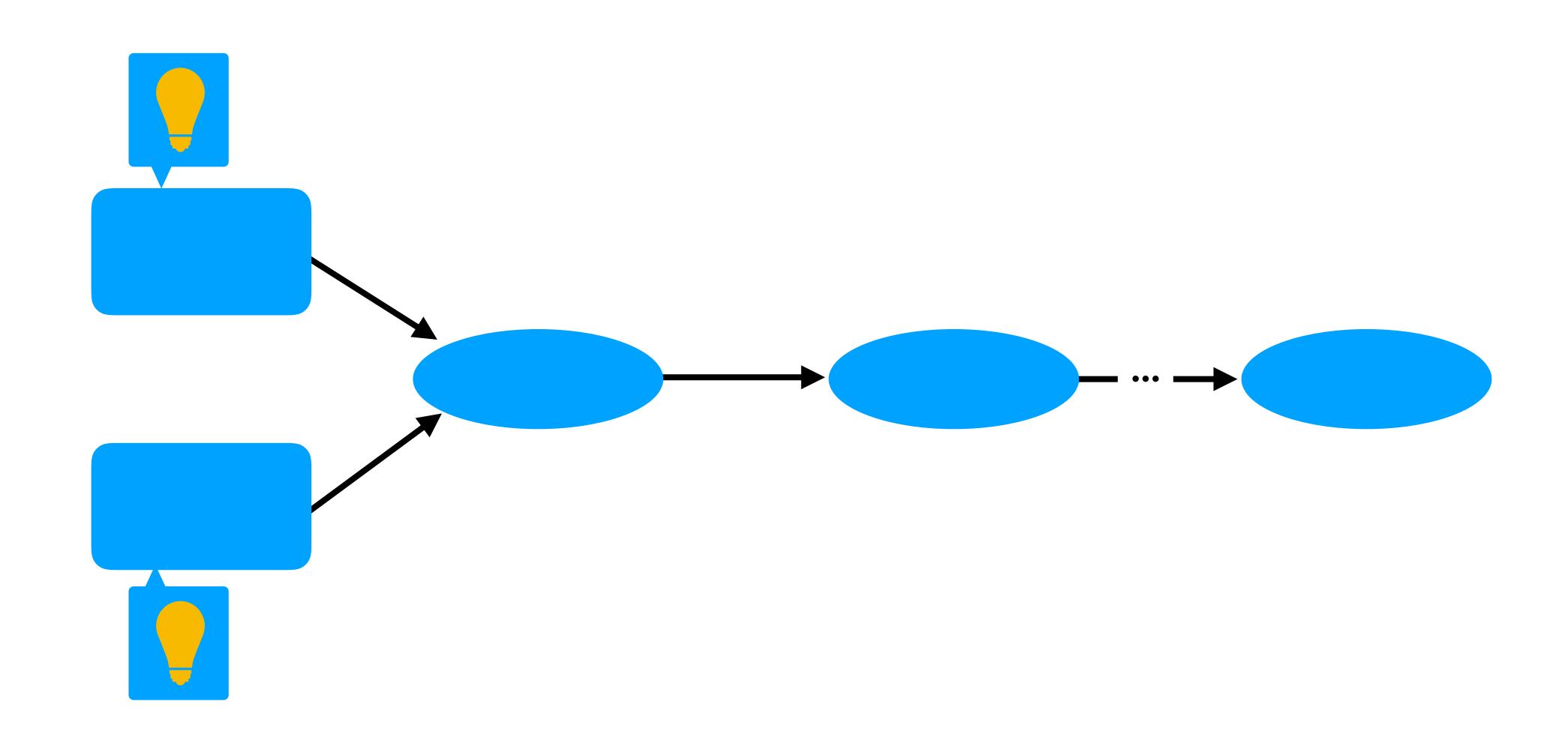


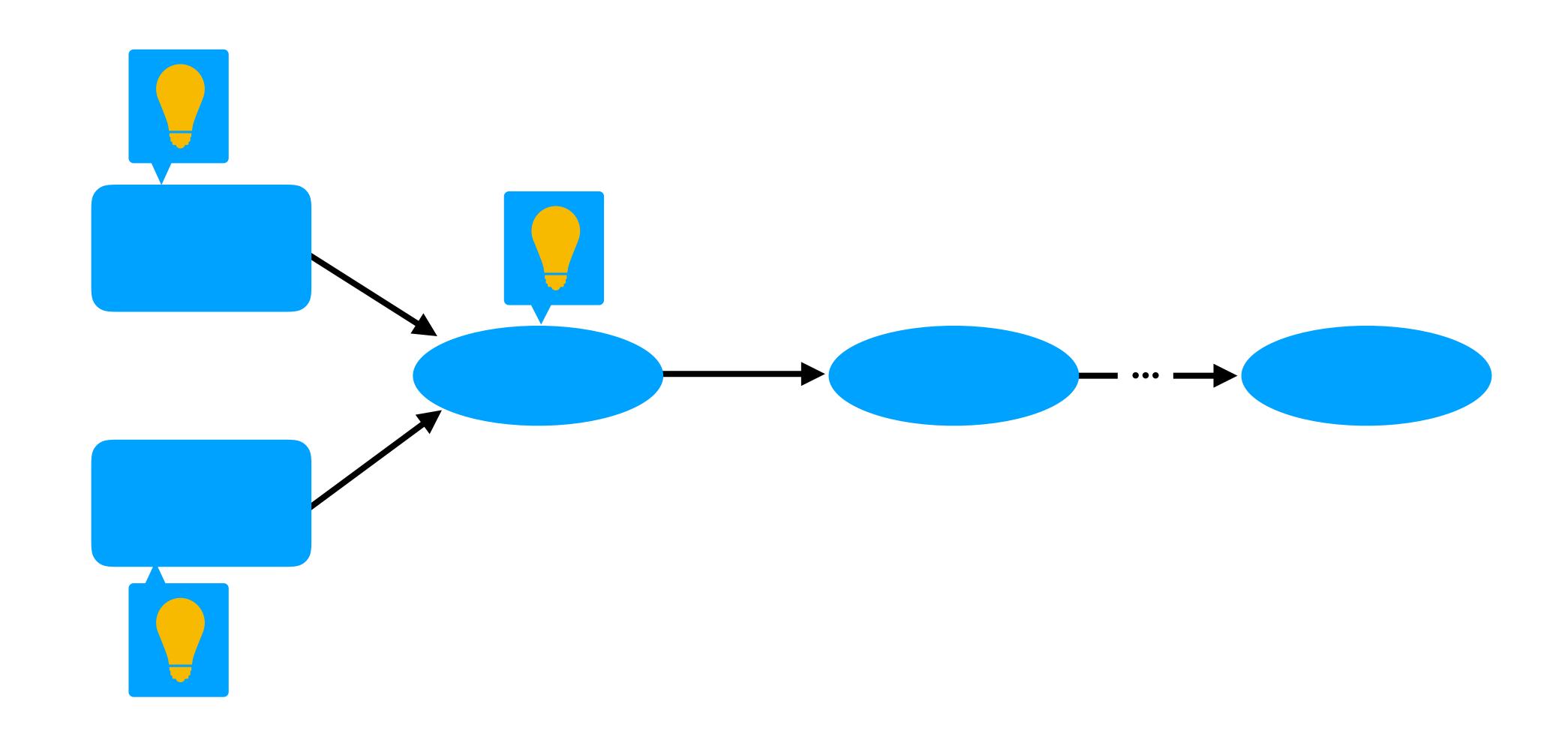


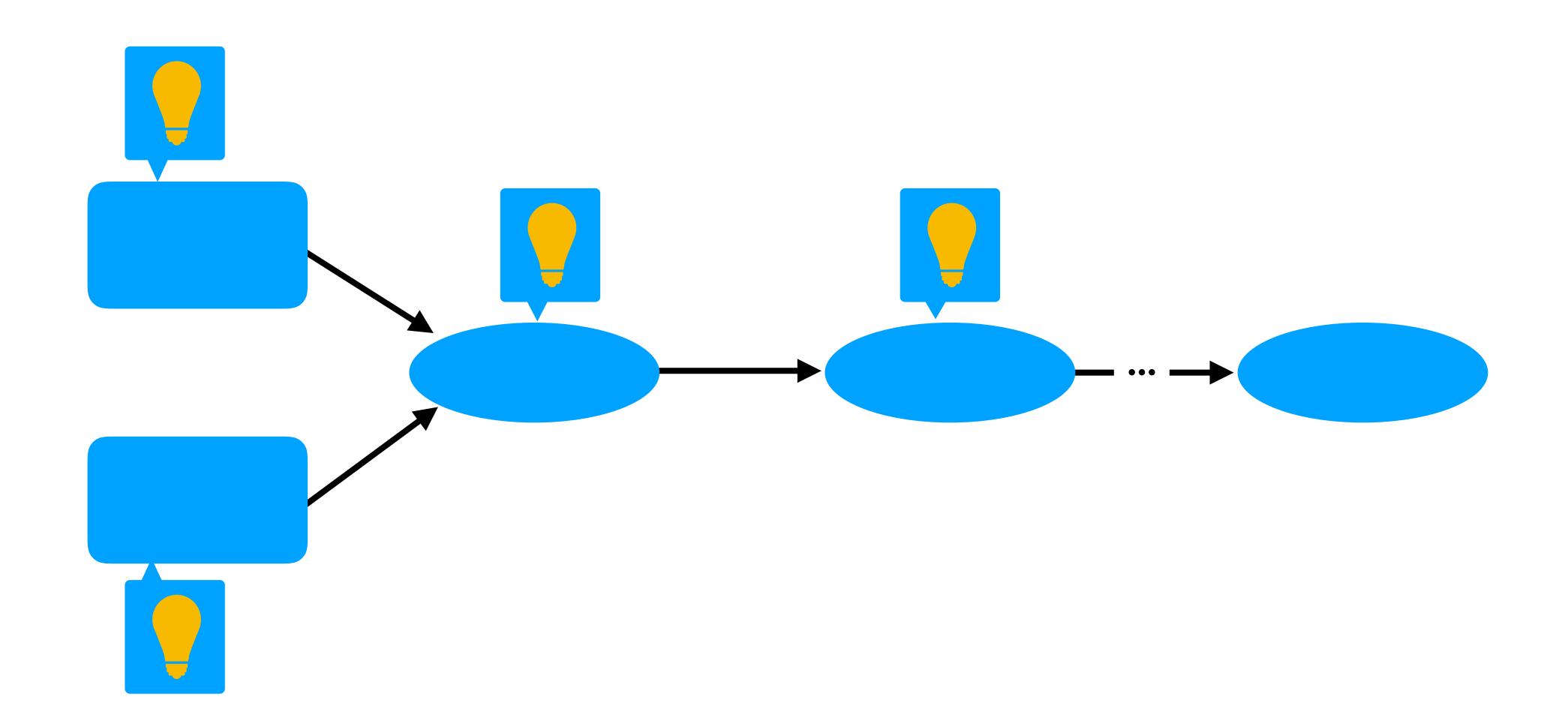


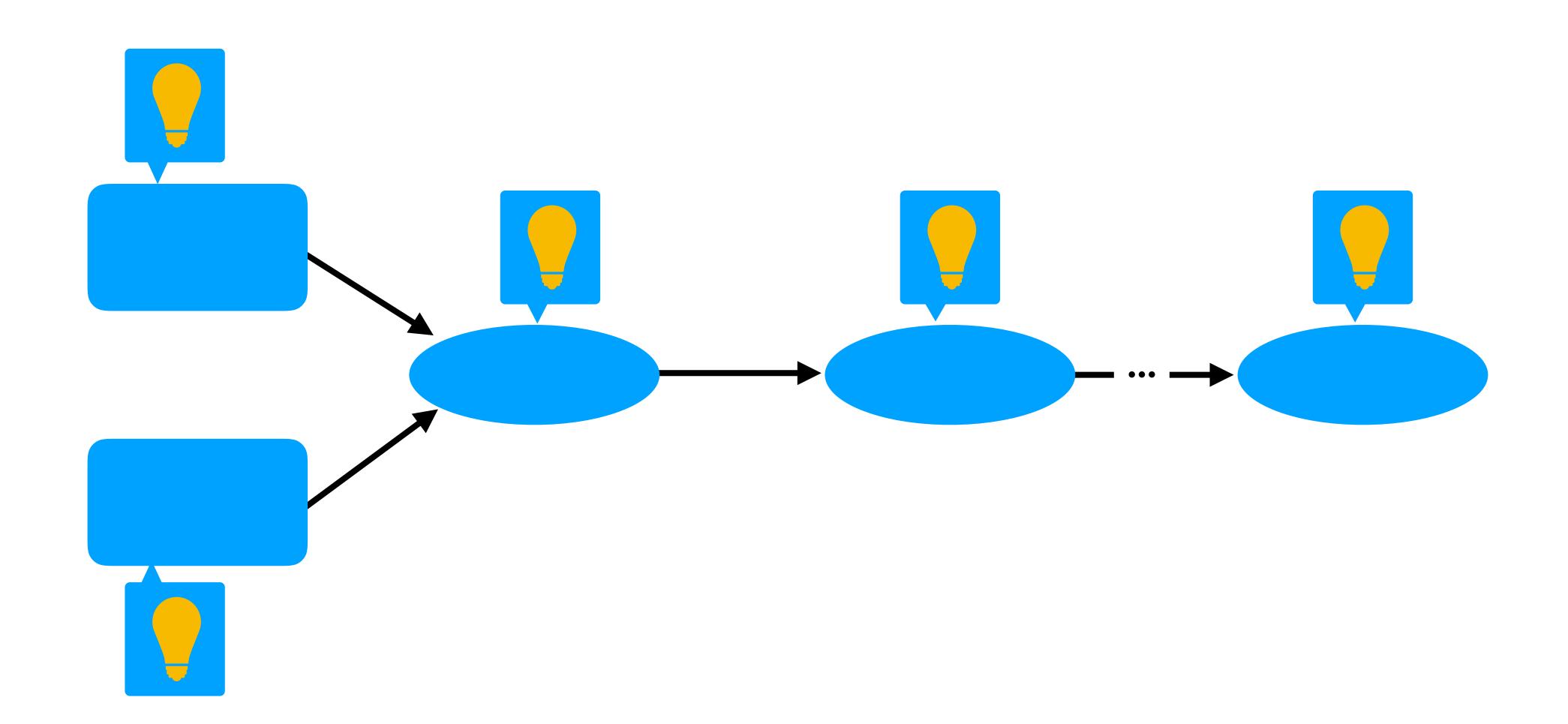


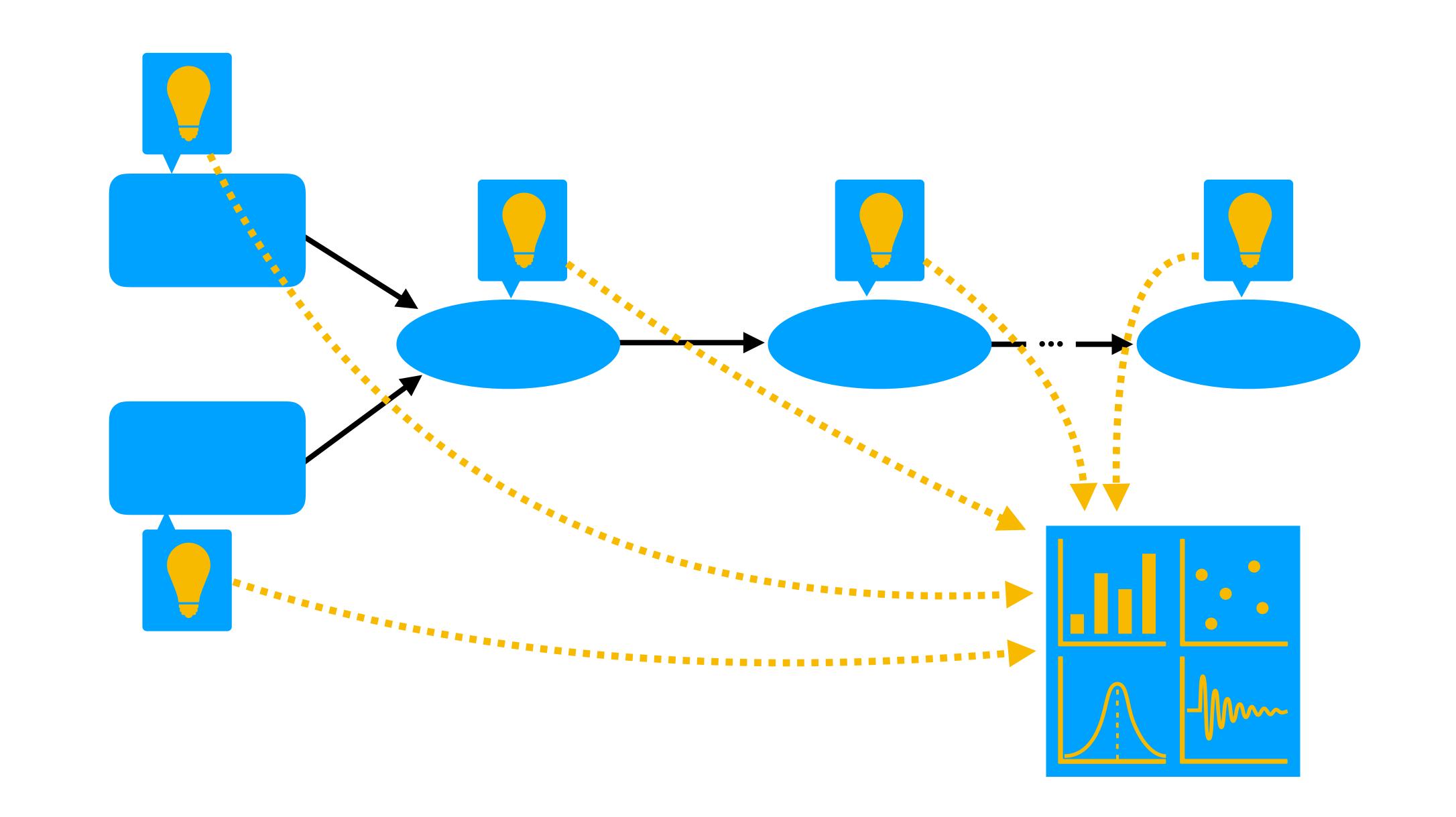








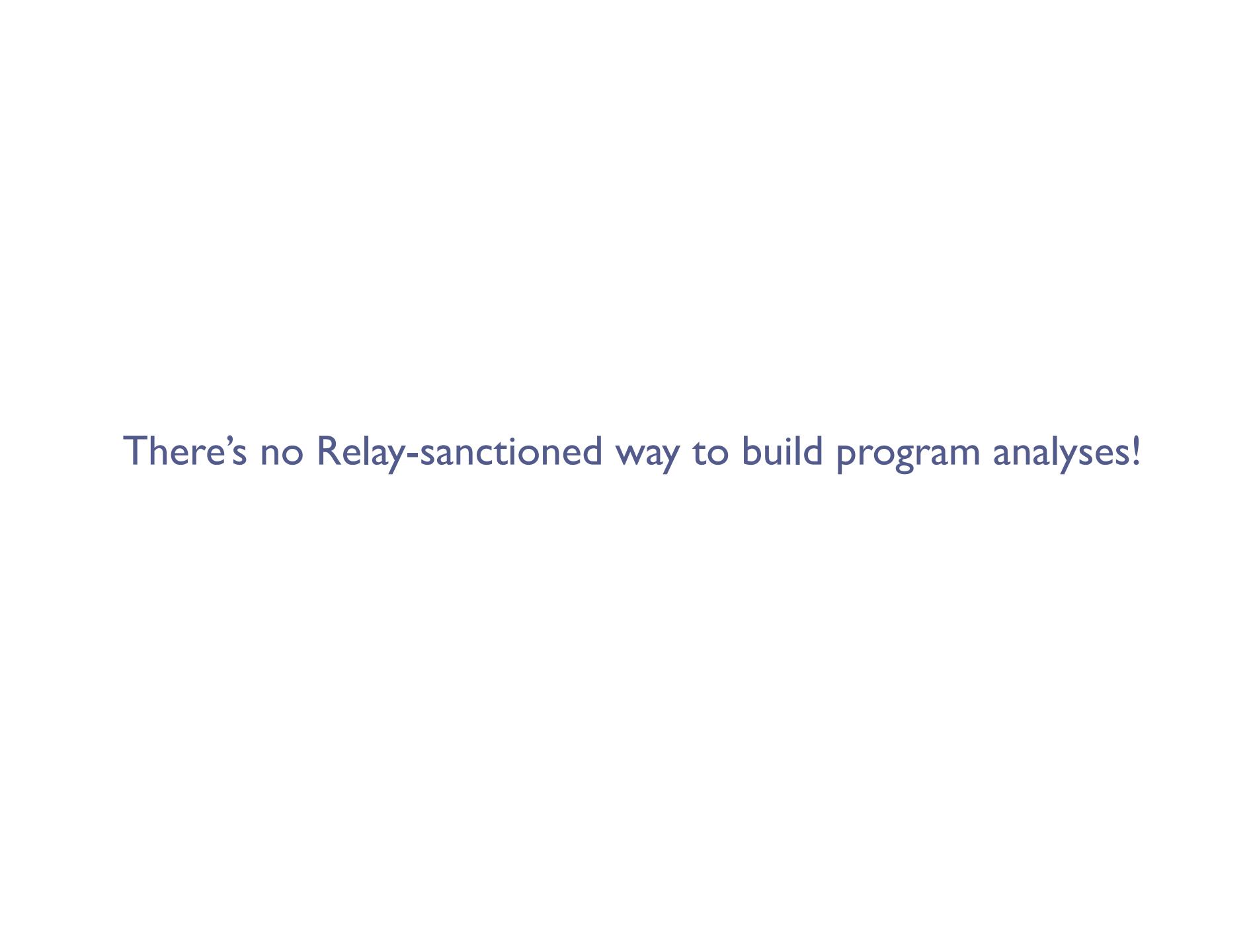




```
1 class MacCounter : private ExprVisitor {
    public:
    MacCounter() {
       count_ = 0;
 6
     static int64_t GetTotalMacNumber(const Expr& expr) {
       LOG(INFO) << "This pass only counts MACs in direct CONV 2D, "
                 << "CONV 2D Transpose and Dense ops";
 8
       MacCounter counter;
       counter(expr);
10
       return counter.count_;
12
13
    private:
14
     void VisitExpr_(const CallNode* call_node) final {
       static const auto& fprep =
16
           Op::GetAttr<FMacCount>("FMacCount");
17
       auto f = fprep.get(call_node->op, nullptr);
18
       if (f != nullptr) count_ += f(GetRef<Call>(call_node));
19
       ExprVisitor::VisitExpr_(call_node);
20
22
     int64_t count_;
24 };
```

```
1 class MacCounter : private ExprVisitor {
   public:
    MacCounter() {
      count_ = 0;
                 1 class FindDef : private ExprVisitor {
    static int64_t
                      private:
      LOG(INFO) <<
 8
                      VarMap<Expr> expr_map_;
      MacCounter c
 9
10
      counter(expr
                       void VisitExpr_(const LetNode* l) final {
      return count
                         CHECK_EQ(expr_map_.count(l->var), 0);
13
                         expr_map_[l->var] = l->value;
14
   private:
    void VisitExpr
                         VisitExpr(l->value);
      static const
16
                         VisitExpr(l->body);
         Op::GetA
      auto f = fpr 10
      if (f != nul
19
20
      ExprVisitor:...sicinpr_(cate_mode)
22
    int64_t count_;
```

24 };



Duplication of effort

- Duplication of effort
- High barrier to entry for new developers

- Duplication of effort
- High barrier to entry for new developers
- Less readability and maintainability

[Relay][RFC] Analysis Infrastructure #3895



MarisaKirisame opened this issue on Sep 4 · 0 comments



MarisaKirisame commented on Sep 4

Contributor



• •

RN there is already a few analysis in relay.

For example, quantize analyze for the best range, an WIP bitpack analyze for the correct layout, Partial Eval do a trivial analysis for functions id, ANF do analysis for scope...

One can even say that type inference is an analysis.

And annotations like stop_fusion is analysis as well.

RN there is two way to deal with analysis: returning a data structure special annotate node.

[Relay][RFC] Analysis Infrastructure #3895



MarisaKirisame opened this issue on Sep 4 · 0 comments

[RFC] Data-flow Analysis Functionality on TVM IR #4468

New issue



DKXXXL opened this issue 4 hours ago · 4 comments



DKXXXL commented 4 hours ago • edited ▼



•

No one assigned

Assignees

Problem

When developing program passes on TVM IR (the one once was **Halide IR**), it is normal to ask for all sorts of information requiring program analysis, for example, live variable analysis for dead code elimination. This requirement becomes urgent when TVM has to directly issue intrinsic and the subsequent processing stage (for example LLVM) **cannot analyze the program because of these intrinsic**.

Labels

None yet

Projects

None yet

```
1 class FindDef : private ExprVisitor {
    private:
     VarMap<Expr> expr_map_;
     void VisitExpr_(const LetNode* l) final {
       CHECK_EQ(expr_map_.count(l->var), 0);
 6
       expr_map_[l->var] = l->value;
       VisitExpr(l->value);
 9
       VisitExpr(l->body);
10
```

Needs documentation!

```
(class FindDef): private ExprVisitor {
    private:
     VarMap<Expr> expr_map_;
     void VisitExpr_(const LetNode* l) final {
       CHECK_EQ(expr_map_.count(l->var), 0);
 6
       expr_map_[l->var] = l->value;
       VisitExpr(l->value);
       VisitExpr(l->body);
 9
10
```

Needs documentation!

```
class FindDef): private ExprVisitor {
    private:
                              Needs a standard data
     VarMap<Expr>(expr_map_;) interchange format!
     void VisitExpr_(const LetNode* l) final {
       CHECK_EQ(expr_map_.count(l->var), 0);
 6
       expr_map_[l->var] = l->value;
       VisitExpr(l->value);
       VisitExpr(l->body);
 9
10
```

Needs documentation!

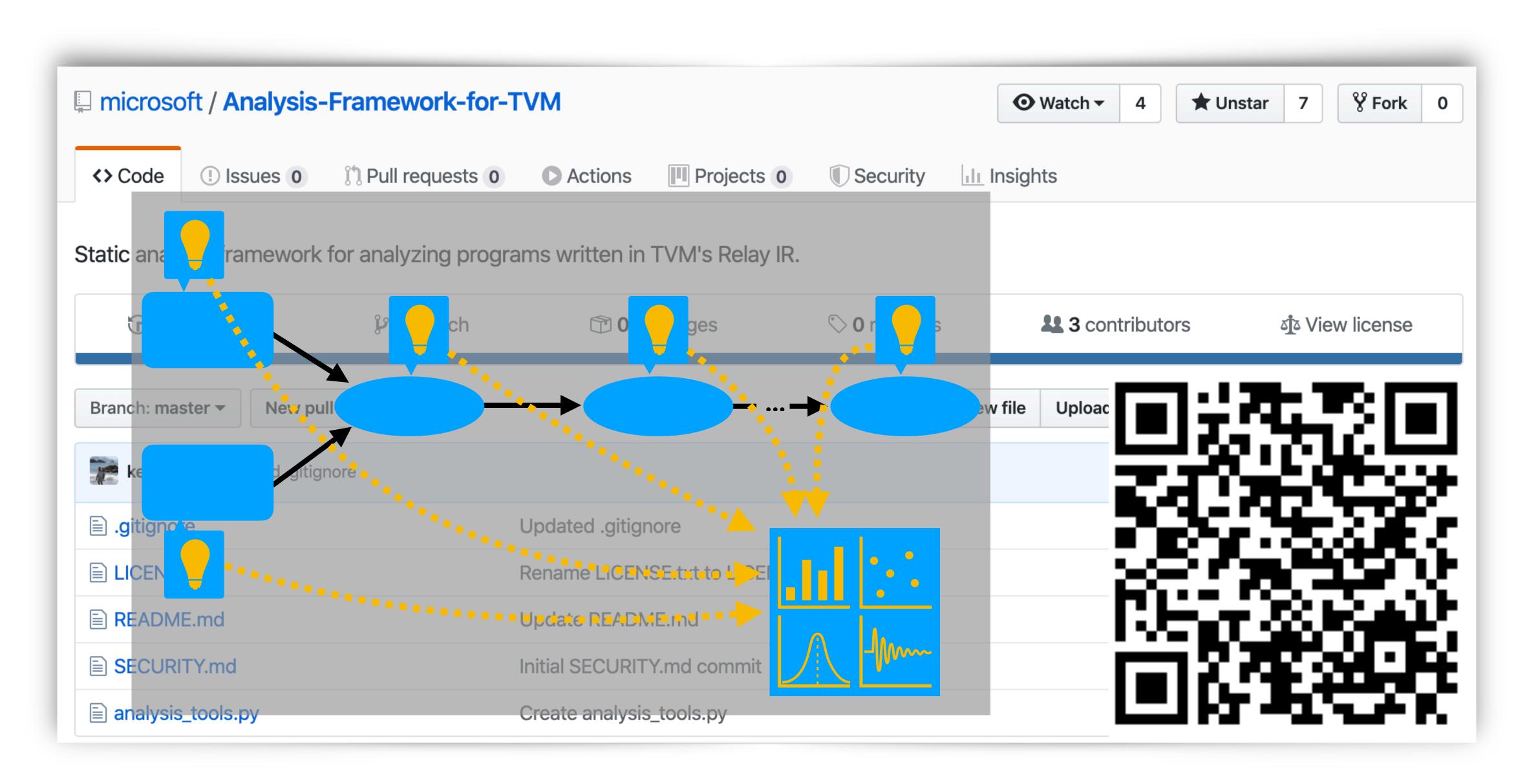
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       expr_map_[l->var] = l->value;
       VisitExpr(l->value);
       VisitExpr(l->body);
 9
10
```

...and needs to be discoverable/accessible!

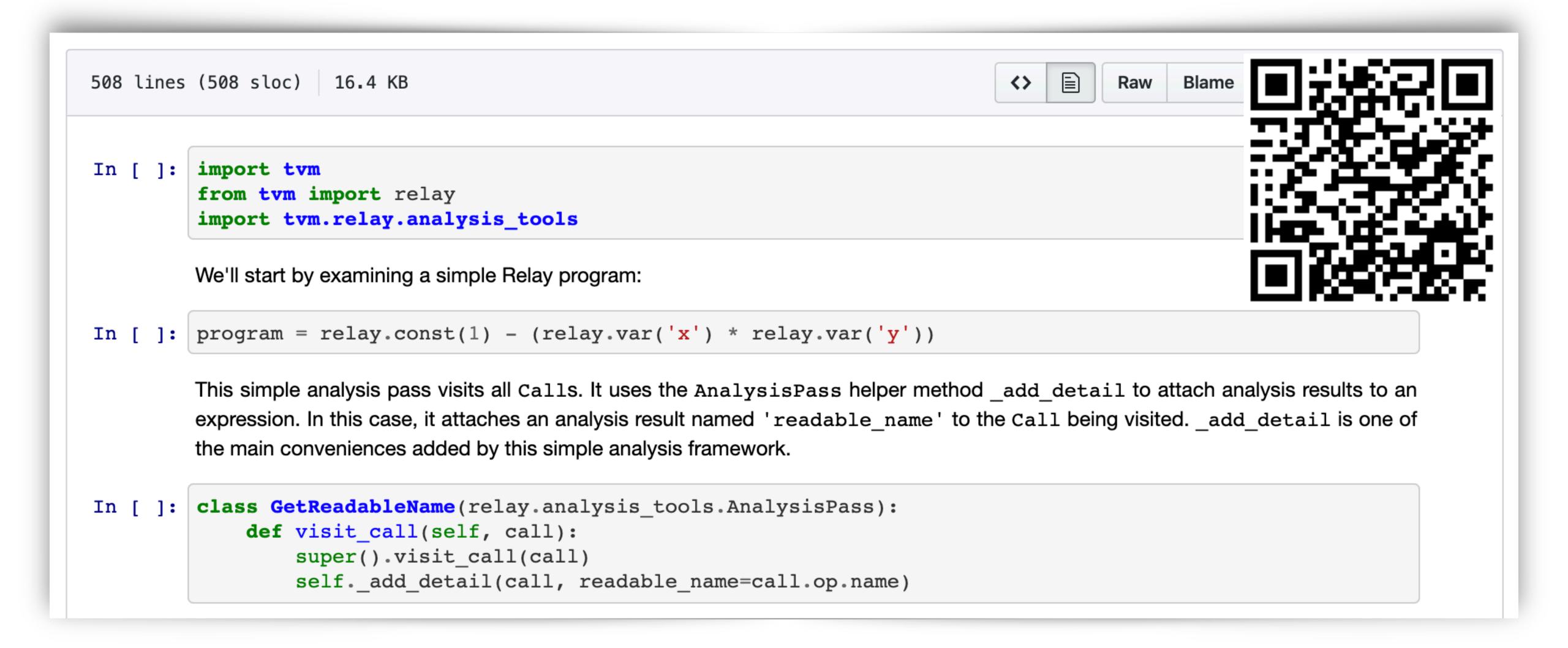
Supports many types of program analyses

- Supports many types of program analyses
- Quick to write new analyses

- Supports many types of program analyses
- Quick to write new analyses
- Promotes composing analyses together



https://github.com/microsoft/Analysis-Framework-for-TVM



https://github.com/gussmith23/tvm/blob/analysis-framework-demo/demo.ipynb

Moving forward





gussmith23 opened this issue 4 days ago · 0 comments



gussmith23 commented 4 days ago

Contributor



<u>...</u>)

Please also see #3895, which is @MarisaKirisame's RFC around a specific change to support analyses in Relay.

This RFC pertains to building a centralized, comprehensive program ana primary uses of program analyses in Relay: for generating analysis data things such as quantization, and for generating human-readable data us exploring Relay programs. Whereas Marisa's request pertains more to thinspired by the second use-case, and motivated by a desire to build a fracases. Such frameworks exist -- see LLVM's framework, which is design both the compiler and the developer.

I built a small analysis framework for Relay this past summer at Microsof readable analyses of Relay programs. A demo of this framework can be



https://github.com/apache/incubator-tvm/issues/4449