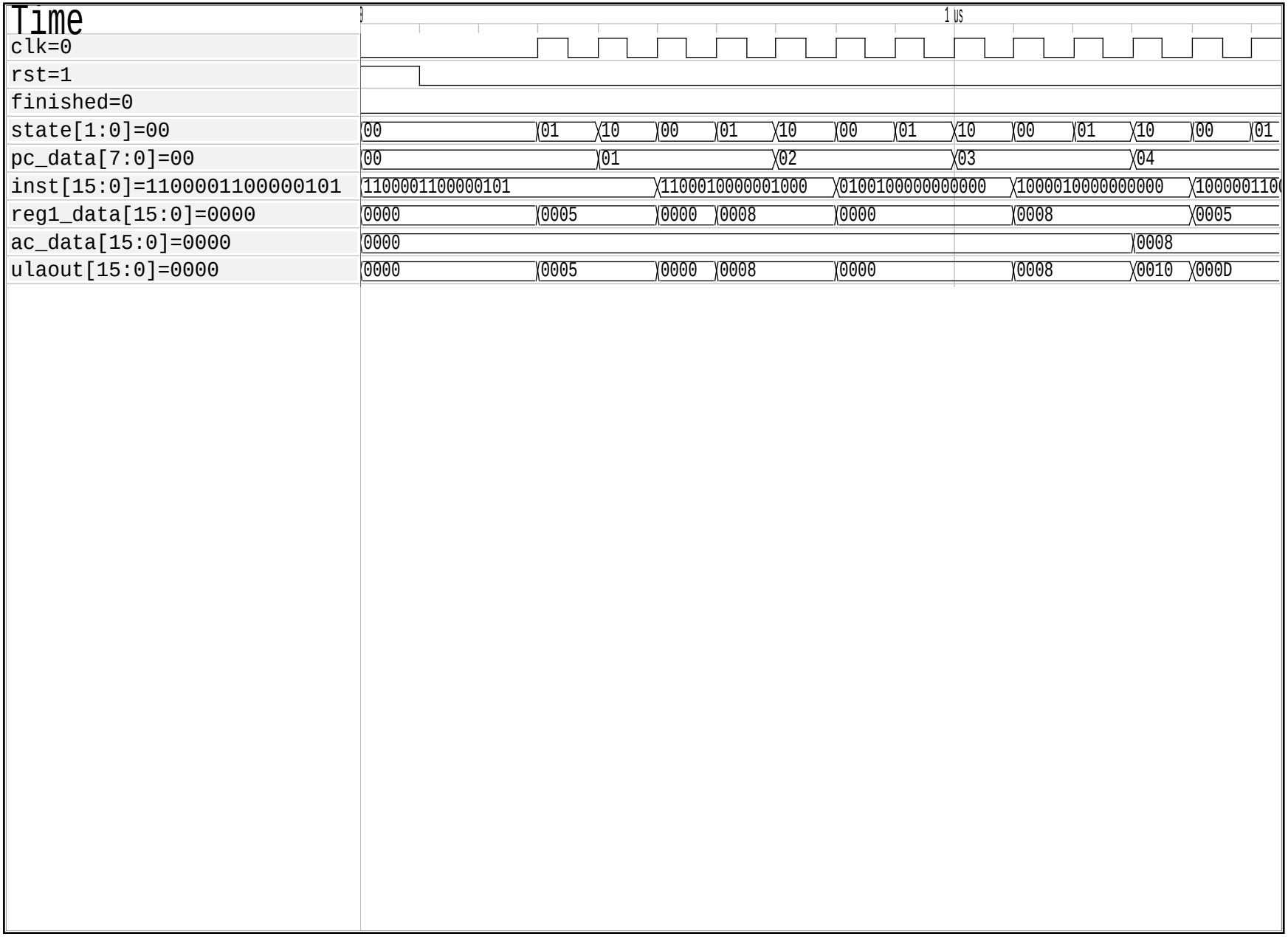
Time
0
1 us
clk=0
rst=1
finished=0
state[1:0]=00
00 01 10 00 01 10 00 01 10 00 01 10 00 01
pc_data[7:0]=00
00 01 02 03 04
inst[15:0]=1100001100000101
1100001100000101 1100010000001000 0100100000000000 1000010000000000 1000001100
reg1_data[15:0]=0000
0000 0005 0000 0008 0000 0008 0005
ac_data[15:0]=0000
0000 0008
ulaout[15:0]=0000
0000 0005 0000 0008 0000 0008 0010 000D