

2.71.

\*Scatchword



byte within word = {0, 1, 2, 3}

$2^3 \ 2^2 \ 2^1 \ 2^0$

/\* Declaration \* type for the unsigned \*/

type def unsigned packed\_t

/\* Extract byte from word. Return as signed integer \*/

int xbyte (packed\_t word, int bytenum)

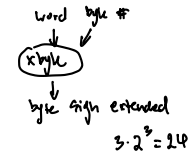
Signed -  $X, X, X, \dots, X, X \rightarrow$  unsigned  $X, X, X, \dots, X, X$

return as signed integer.

packed\_t - unsigned

bytenum - 0 1 1 1 ... 1 1

3 2 1 0



3

bytenum 00000000 00000000 00000000 00000011

word 00000011 00000000 00000000 00000000

& 0xFF 00000000 00000000 00000000 11111111

Output: 00000000 00000000 00000000 00000000

Desired output: -1

bytenum: 3

bytenum << 3:  $3 \cdot 2^3 = 24$

Word: (-1)

Word 27 24: 000 00000 000 00000 000 00000 111 11111

& 0xFF: 000 00000 000 00000 000 00000 111 11111

000 00000 000 00000 000 00000 111 11111

Output: 255

a. The problem with this code is that it uses the AND operator on the shifted bits and does not take care of the sign encoding. In other words, the code only works for nonnegative integers. For example, the code does not work for -1 as shown below.

Desired output: -1

bytenum: 1

bytenum << 3:  $1 \cdot 2^3 = 4$

Word: 3 2 1 0 (-1)

Word 7 4: 111 11111 111 11111 111 11111 000 00000

& 0xFF: 000 00000 000 00000 000 00000 111 11111

000 00000 000 00000 000 00000 000 00000

Output: 0

The output should be -1 if you're taking the third byte, but it's 0

b.

3 2 1 0

Significance

#3: leftshift:  $3 - 3 = 0 \cdot 2^0 = 0$  111 11111 111 11111 111 11111 111 11111

#2: leftshift:  $3 - 2 = 1 \cdot 2^1 = 2^2 = 4$  111 11111 111 11111 000 00000

#1: leftshift:  $3 - 1 = 2 \cdot 2^2 = 2^4 = 16$  111 11111 000 00000 000 00000

#0: leftshift:  $3 - 0 = 3 \cdot 2^3 = 24$  111 11111 000 00000 000 00000

Full file is under 271b.c in submission file

```
#include <stdio.h>
#include <assert.h>

typedef unsigned packed_t;

int xbyte(packed_t word, int bytenum) {
    //Holds amount needed to push the desired byte to the end of the 32-bit series
    int arith_left_shift = (3-bytenum) << 3;
    //Holds amount needed to push the byte that is at the end of the series to the least significant placement of 0
    int arith_right_shift = 24;
    //Explicit casting to signed encoding
    int output = (int)(word << arith_left_shift) >> arith_right_shift;
    return output;
}
```

2.82 a)  $(x < y) == (-x > -y)$

False

Counter example:  $x + \sim x = -1$  Suppose  $\sim x$  is TMSN, then, in bit form:

$$\begin{array}{rcl} x & = & -1 - \sim x \\ \sim x & = & 111 \dots 111 \\ & + & 1000 \dots 001 \\ \hline -x & = & 1 + \sim x \\ & = & 1000 \dots 000 \end{array}$$

The result is 0, which does not satisfy the above statement if y is a positive integer

b)  $((x+y) < 4) + y - x == 17^k y + 15^k x$

True

$$(x+y) \cdot 2^k + y - x = 16x + 16y + y - x = 17^k y + 15^k x$$

c.  $\sim x + \sim y + 1 = \sim(x+y)$

It is true that, if a is a signed int, then: The statement is true

$$\begin{array}{lcl} a + \sim a & = & -1 \\ a + \sim a + 1 & = & 0 \\ \text{let } a = x+y & & \end{array}$$

$$\begin{array}{lcl} x+y + \sim(x+y) + 1 & = & 0 \\ \sim(x+y) + 1 & = & -x - y \\ \sim(x+y) + 1 & = & 1 + \sim x + 1 + \sim y \\ & = & 1 + \sim x + \sim y \\ \sim(x+y) & = & \sim x + \sim y \quad \blacksquare \end{array}$$

d.  $(ux - uy) == -(\text{unsigned})(y - x)$

$$\begin{array}{lcl} ux \rightarrow \text{unsigned} & uy \rightarrow \text{unsigned} & \\ -(\text{unsigned})(y - x) & \rightarrow & \text{explicit casting} \\ -(uy - ux) & = & (ux - uy) \quad \text{True} \end{array}$$

e.  $((x > 2) < 2) <= x$

$x \rightarrow \text{signed}$  True

$$\begin{array}{lcl} 11 & 0 \dots 00 & \checkmark \\ 00 & \dots 11 \dots 00 & \checkmark \\ 00 & \dots 00 \dots 01 & \checkmark \\ 11 & 111 \dots 11 & \checkmark \\ > 2 & & \\ 1111 & \dots 11 & \\ < 2 & & \\ 1111 & \dots 00 & \checkmark \\ & \text{higher register num} & \end{array}$$