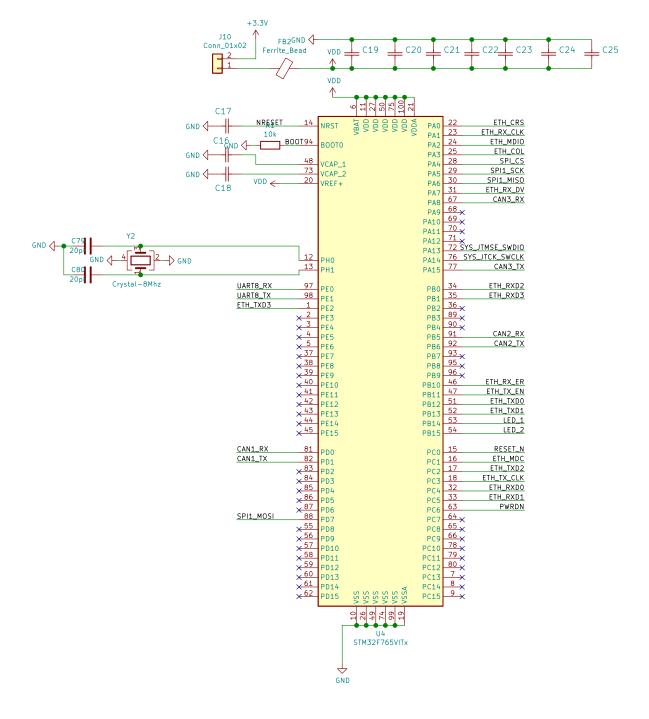
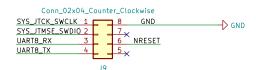


Microcontroller



DEBUG



MII interface

ETH_MDIO	—◆ETH_MDIO
ETIL MDC	—DETH_MDC
ETH_COL	DETH COL
	— DETH_COL
ETH DV CLV	— deth_cks — deth_rx_cl
ETH TV CLV	
ETH_RX_DV	— ₫ ETH_TX_CL
FTH RX FR	— CETH_RX_DV
ETH_RX_ER	— ₫ ETH_RX_ER
ETH_RXD0	— ₫ ETH_RXD0
ETH_RXD1	TETH DYD1
	— dETH_RXD2
ETH DVD3	TETH RXD3
	— GETH_KXD3
ETH_TX_EN	B ETH TV EN
ETH TVDA	DETH_TX_EN
CTU TVD4	— D ETH_TXD0
ETH TVDO	DETH_TXD1
	— D ETH_TXD2
ETH_TXD3	— D ETH_TXD3

Switch manage interface

SPI1_SCK	— D SPL SCK
SPI1_MISO	—DSPI_MISO
SPI1_MOSI	—DSPLMOS
SPI_CS	—DSPL CS
	D 31 1_C3
PWRDN	— D PWRDN
RESET_N	
	DKESEI_N

Led indication

