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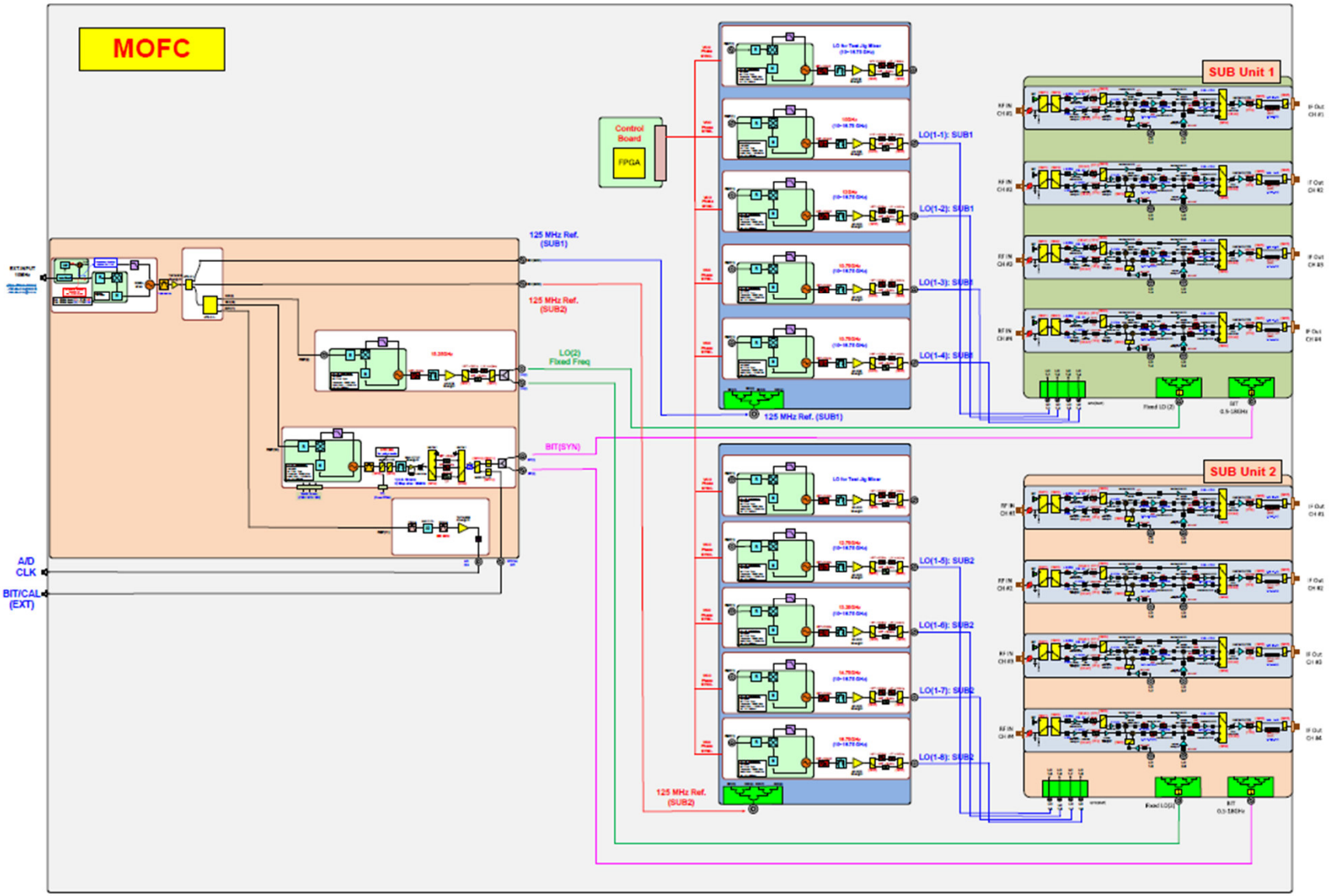
MOFC
CONTROL PLAN

2025.05.15

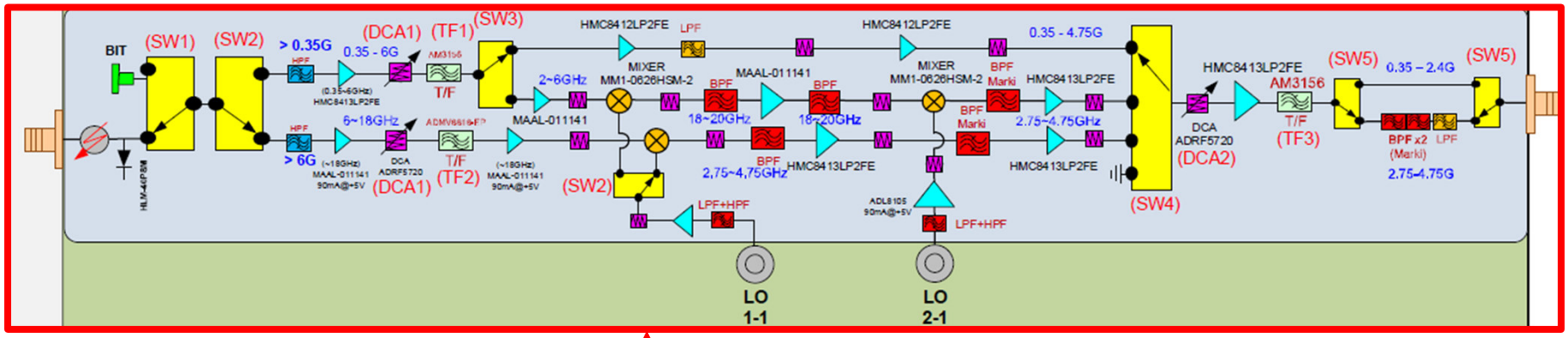
Broadband Wireless & Microwave Leading Company

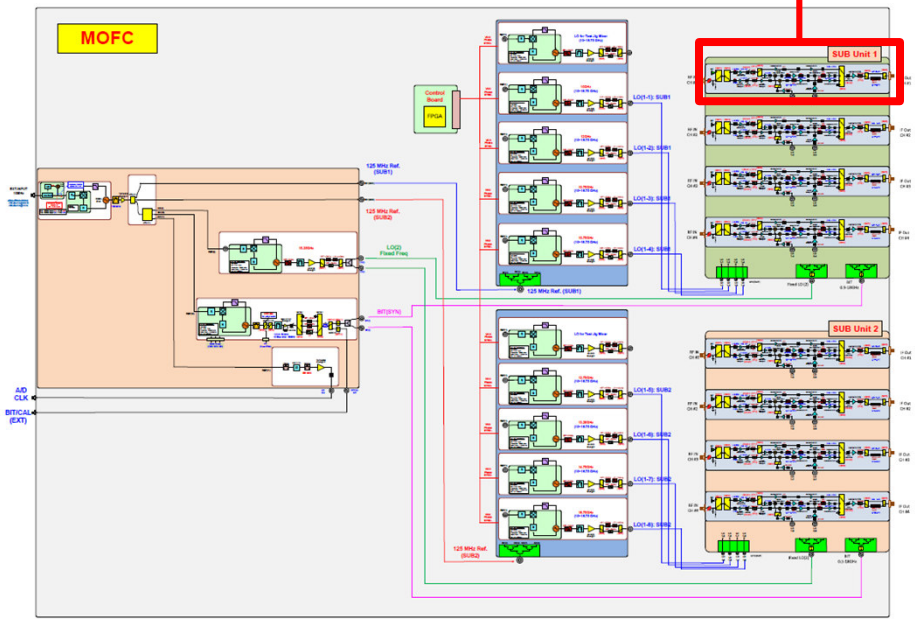
Broadern
broadband wireless technology

1. MOFC BLOCKDIAGRAM



2.1 INDIVIDUAL MODE(1)

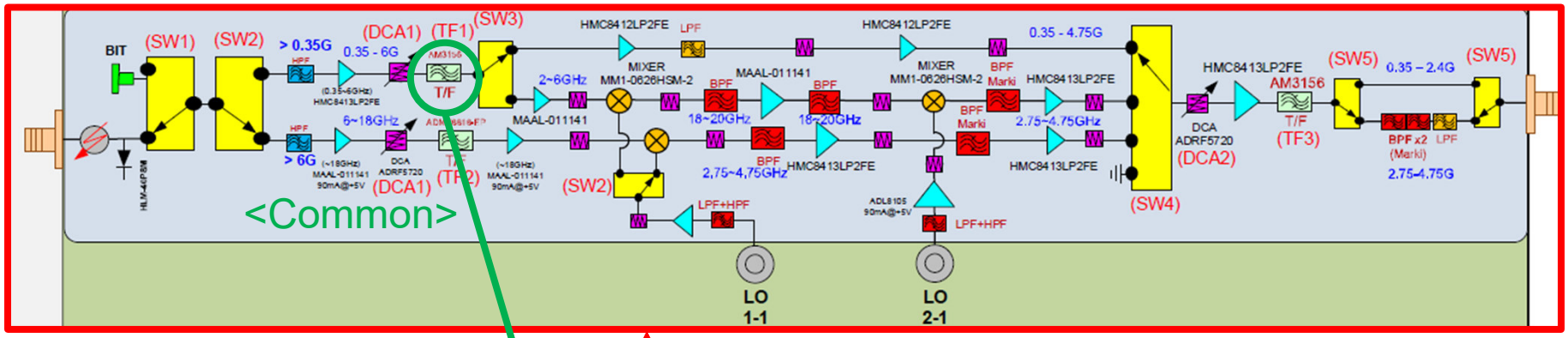


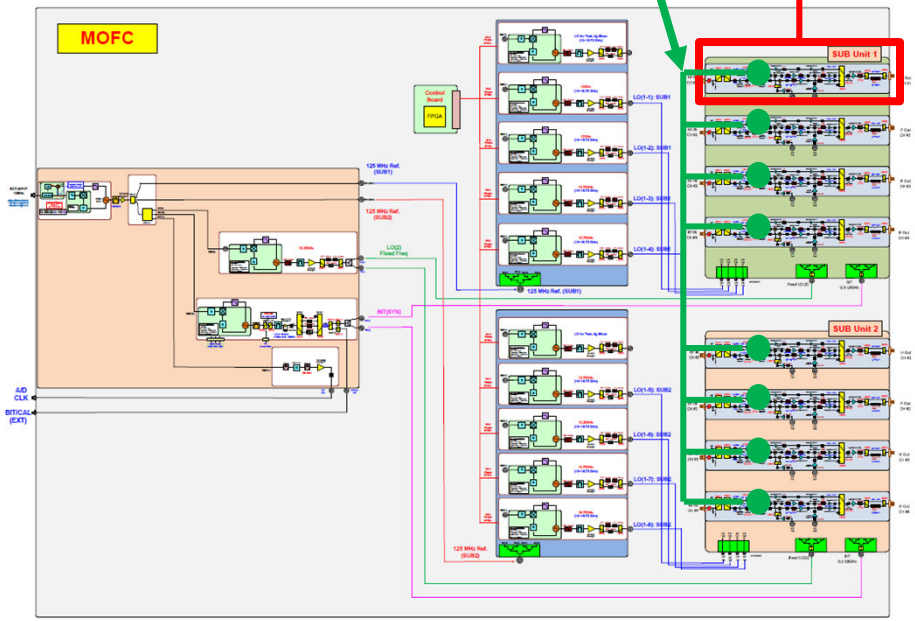


Individual Mode Option #1

- Accessing each of eight channels is individually configured by the master, **304** IO Pins are required.
- Need to reduce the I/O Pins.

2.1 INDIVIDUAL MODE(2)

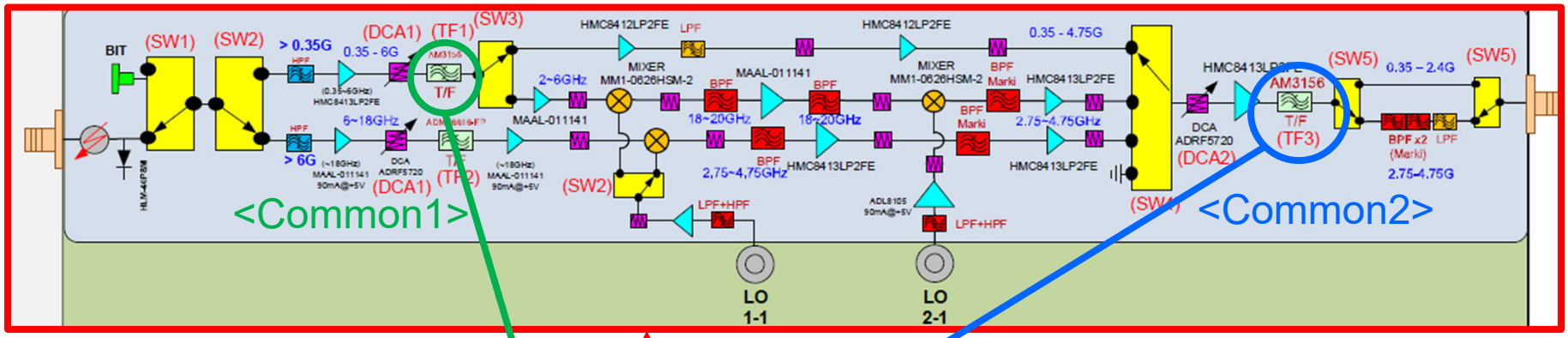


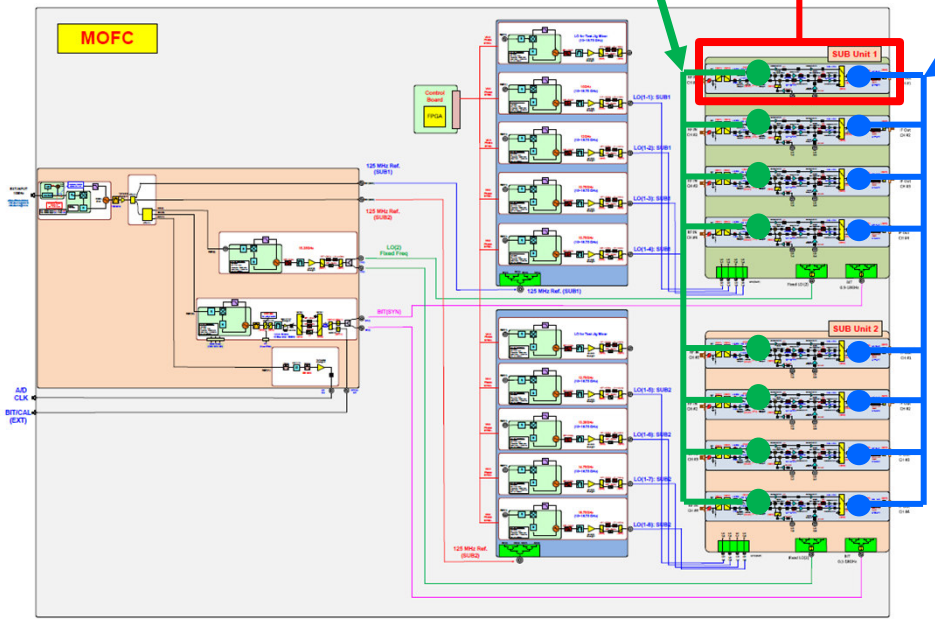


Individual Mode Option #2

- If each of eight channels is individually configured by the master and set TF1(CH1~8) as a common, then I/O pin will be **234**.
- Still need to reduce the I/O Pins.

2.1 INDIVIDUAL MODE(3)

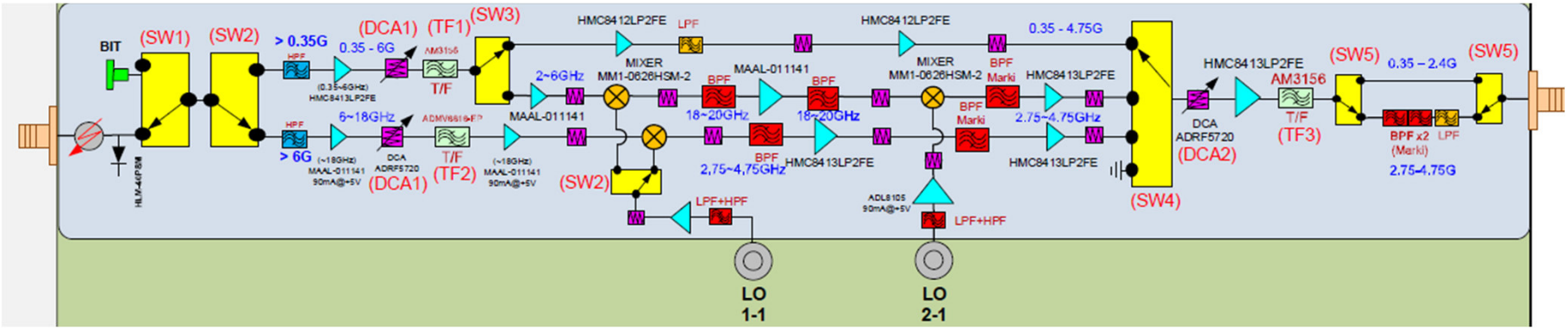




Individual Mode Option #3

- If each of eight channels is individually configured by the master and set TF1(CH1~8) as a common1 & TF3 (CH1~8) as a common2, then I/O pin will be required **164**.
- Satisfy with I/O Pin quantities.

3. COMMON QUAD MODE



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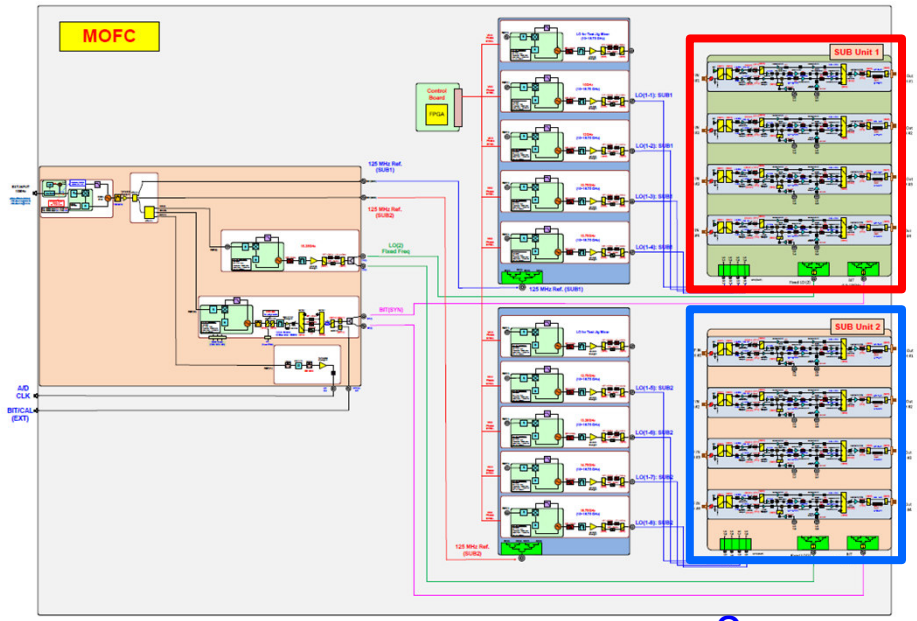
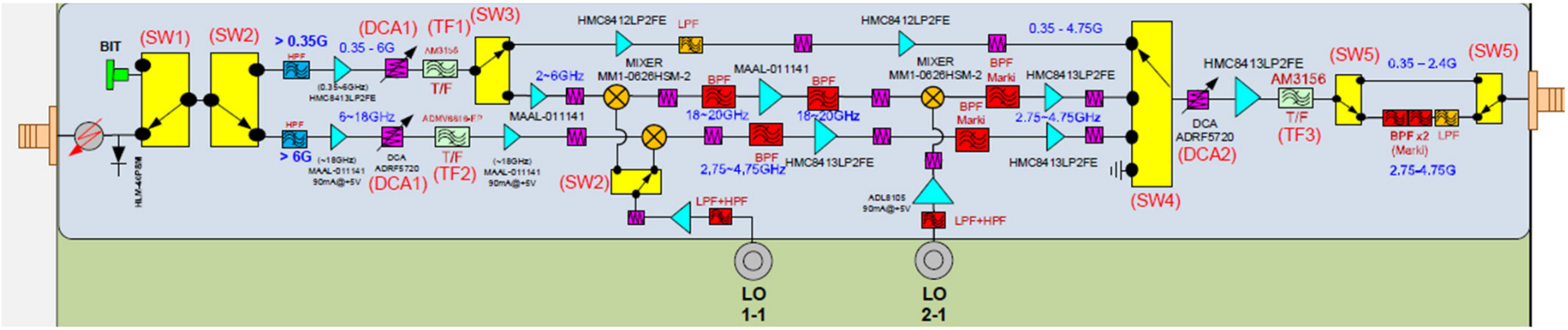


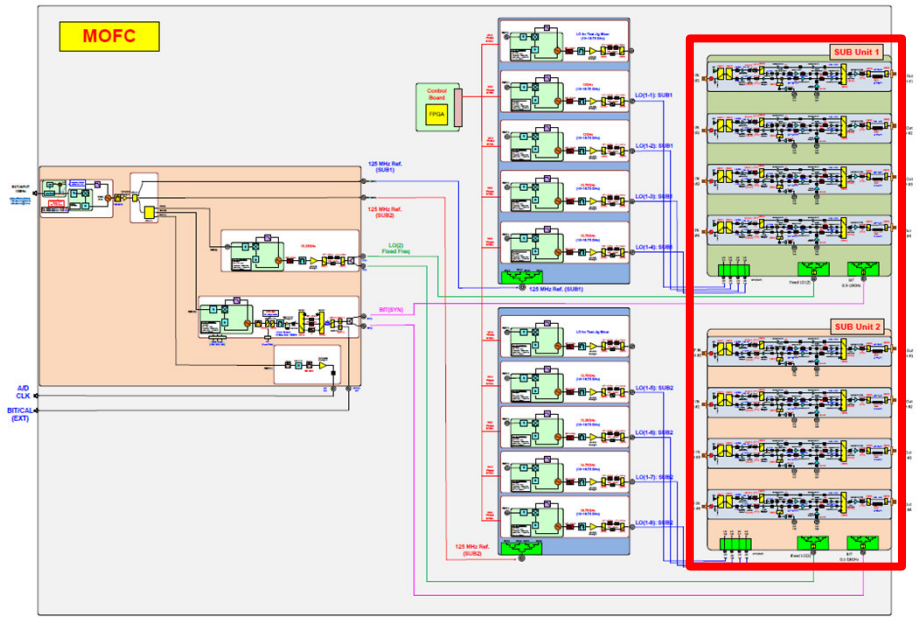
<2>

Common Quad Mode

- The master fills configurations for channels 1 and 2. channel 1's configuration is used for Ch1 ~ 4 and channel 2's configuration is used for CH5 ~ 8.
- I/O Pins will be **184** Pins.

3. COMMON OCTAL MODE





Common OCTAL Mode

- The master configurations only channels 1. This configuration is then applied to CH1 ~8.
- I/O Pins will be **184** Pins.

4. Conclusion

- Due to lack of I/O Pins, Single Channel Mode(Each of the eight channels is individually configured by the master) is impossible to do it for now. However, if set TF1(CH1~8) as common1 and TF3(1~8) as common2, then I/O pin quantities are satisfied.

- Common Quad Mode and Common Octal Mode are able to access.