

Project #5(Pjt05_uart_intr_race)

UART0 interrupt based TX : (race condition) Step 1

```
void uart_init()
{
    UBRR0H = 0x00; UBRR0L = 0x07; // 115.2K
    UCSRA |= (1 << U2X0);
    UCSRB |= (1 << TXEN0);
    UCSRB |= (1 << TXCIE0); // TX complete interrupt
}

char buf[64];
int volatile bufi, txend = 1;

int uart_putstart(char str[])
{
    char ch; int n = strlen(str);
    while(sizeof(buf) - strlen(buf + bufi) - 1 < n);
    strcpy(buf, buf + bufi); strcat(buf, str);
    bufi = 0;
    if (txend) {
        txend = 0;
        ch = buf[buji++];
        UDRO = ch;
    }
}
```

```
main()
{
    int i;
    uart_init();
    sei();
    for (i = 0; i < 10; i++) {
        uart_putstart( "I love you.Wn" );
        uart_putstart( "You love me.Wn" );
        uart_putstart( "He loves her.Wn" );
    }
    uart_putstart( "End !!!Wn" );
    while(1);
}
```

```
#include <avr/interrupt.h>
ISR(USART0_TX_vect)
{
    char ch = buf[buji];

    if (!ch) {
        bufi = 0, txend = 1; return;
    }
    if (ch == 'Wn')
        buf[buji] = 'Wr';
    else
        bufi++;
    UDRO = ch;
}
```

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Step 2

UART0 interrupt based TX : (mutual exclusion)

```
void uart_init()
{
    UBRR0H = 0x00; UBRR0L = 0x07; // 115.2K
    UCSRA |= (1 << U2X0);
    UCSRB |= (1 << TXEN0);
    UCSRB |= (1 << TXCIE0); // TX complete interrupt enable
}

char buf[64];
int volatile bufi, txend = 1;
int uart_putstart(char str[])
{
    char ch; int n = strlen(str);
    while(sizeof(buf) - strlen(buf + bufi) - 1 < n);

    cli();
    strcpy(buf, buf + bufi); strcat(buf, str);
    bufi = 0;
    if (txend) {
        txend = 0;
        ch = buf[buji++];

        UDRO = ch;
    }
    sei();
}
```

```
main()
{
    int i;
    uart_init();
    sei();
    for (i = 0; i < 10; i++) {
        uart_putstart( "I love you.Wn" );
        uart_putstart( "You love me.Wn" );
        uart_putstart( "He loves her.Wn" );
    }
    uart_putstart( "End !!!Wn" );
    while(1);
}
```

```
#include <avr/interrupt.h>
ISR(USART0_TX_vect)
{
    char ch = buf[buji];

    if (!ch) {
        bufi = 0, txend = 1; return;
    }
    if (ch == 'Wn')
        buf[buji] = 'Wr';
    else
        bufi++;
    UDRO = ch;
}
```