Project #2(Pjt02_uart_busy) step1

- Simple /IO through UART0 device
- UART0 device driver with Dumb Waiting

```
#include <avr/io.h>
#include <compat/deprecated.h>
#include <util/delay.h>

void  uart_init()
{
     UBRR0H = 0x00; UBRR0L = 0x07; // 115.2Kbps
     sbi(UCSR0A, U2X0);            // 115.2Kbps
     sbi(UCSR0B, TXEN0);           // TX enable
}

int  uart_putchar(char ch)
{

     if (ch == '\n')
         uart_putchar('\r');
     _delay_ms(100);
     UDR0 = ch;
     return(1);
 }
```

```
main()
{
    char  c;
    for (c = 'A' ; c <= 'Z' ; c++)
         uart_putchar(c);
    uart_putchar('\n');
    while(1);
}
```

Project #2(Pjt02_uart_busy) step2

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```
#include <avr/io.h>
#include <compat/deprecated.h>
#include <util/delay.h>

void  uart_init()
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}

int  uart_putchar(char ch)
{

      if (ch == '\n')
          uart_putchar('\r');
      _delay_ms(100);
      UDR0 = ch;
      return(1);
 }
```

```
int uart_putstr(char *sp)
{
      for ( ; *sp; sp++)
            uart_putchar(*sp);
      return(1);
}
```

```
main()
{
     char  c;
     for (c = 'A' ; c <= 'D' ; c++)
          uart_putchar(c);
     uart_putchar('\n');

     uart_putstr("ABCD\n");
     while(1);
}
```