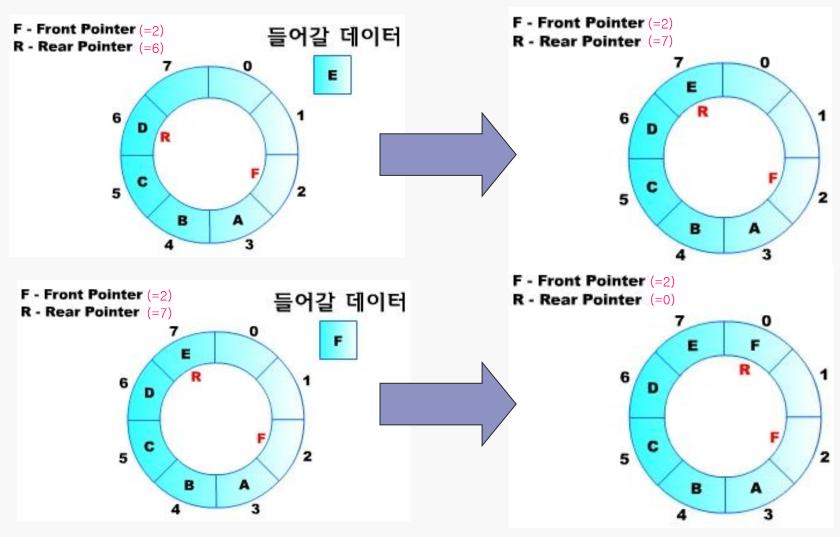
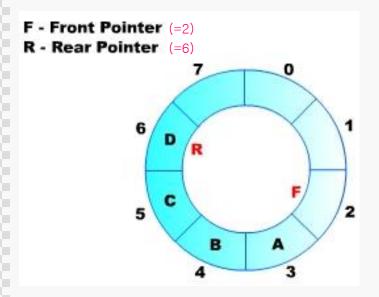
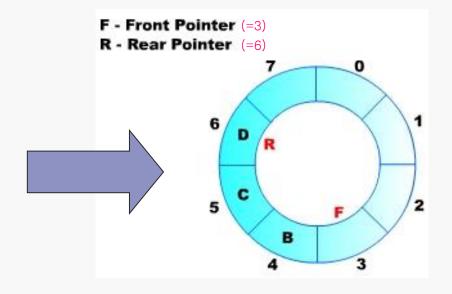
♦ Circular Queue – insert

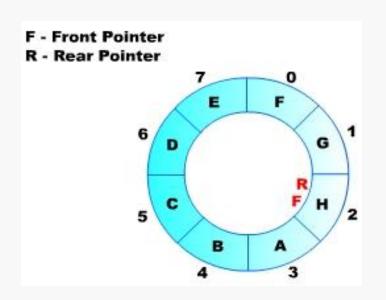


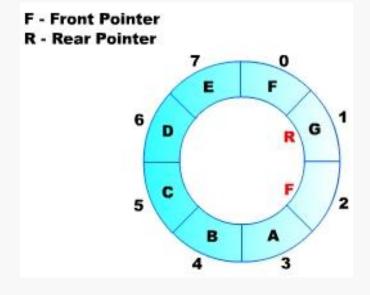
Circular Queue – delete





Circular Queue – full & empty





Data Structure for UARTO device driver(Circular Queue)

```
#define Q_SIZE 256
char q[Q_SIZE]
int f, r;

void q_init()
{
    f = r = 0;
}
```

UARTO output device driver based on I/O interrupt

```
#include <stdio.h>
#include <avr/io.h>
#include <compat/deprecated.h>
FIIF
      Mystdout = FDEV_SETUP_STREAM (uart_putchar, NULL,_FDEV_SETUP_WRITE);
char uart_busy;
void uart init()
    stdout = &Mystdout;
    uart_busy = 0;  // false
    q_init();
    UBRROH = 0x00; UBRROL = 0x07;
    sbi(UCSROA, U2X0); // UCSR)A |= (1 << U2X0);
    sbi(UCSROB, TXENO);
    sbi(UCSROB, TXCIEO);
```

```
#include <stdio.h>
#include <avr/io.h>
#include <util/delay.h>
     uart_putchar(char ch, FILE *stream)
     if (ch == ' \forall n')
         uart_putchar('₩r', stream);
     cli(); /* clear interrupt flag */
     if (!uart_busy) {
         UDR0 = ch;
         uart busy = 1;
     else {
         while(q_insert(ch) == 0) {
              sei();
              _delay_us(100);
              cli();
     sei();
             /* set interrupt flag */
     return(1);
```

```
#include <avr/interrupt.h>

ISR(USARTO_TX_vect)
{
  char ch;

  if ((ch = q_delete()) == 0)
     uart_busy = 0;
  else
     UDRO = ch;
}
```

```
main()
{
   uart_init();
   sei();
   app_prime(2000);
   while(1);
}
```

```
int is_prime(int n)
  int i;
 for (i = 2; i \le n/2; i++)
    if ((n \% i) == 0)
       return(0);
  return(1);
app_prime(int t)
  int n, count = 0;
  for (n = 2; n \le t; n++) {
   if (is_prime(n)) {
       count++;
       printf("%d is a prime"
                "number !!!\n", n);
 printf("count=%d₩n", count);
```