10.2 8-Bit Operation,8-Digit×2-Line Display Example

Display is not shifted. Writes II. Display is not shifted.	Step	Instruction											
Sets to 8-bit operation and selects 2-line display and cursor to the original position. Sets to 8-bit operation and selects 2-line display and selects 2-line display and cursor to the original position. Sets to 8-bit operation and selects 2-line display and selects 2-line display and cursor to the original position. Sets dot character four. Turns on display and cursor to the selects 2-line display and cursor to the selects 2-line display and cursor to the selects 2-line display and cursor to the original position. Sets dot character four. Turns on display and cursor to the selects 2-line display and select	No	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Display	Operation
Selects 2-line display and selects 2-line display and cursor. Sets Display on/off control O 0 0 0 0 0 0 1 1 1 0 O 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	1				,			Initialized. No display.					
3 Display on/off control 0 0 0 0 1 1 1 0 * * *	2												
All display is in space mode because of initialization Sets mode to increment the address by one and to shift it address by one and to shift it one with the total content in the address by one and to shift it of write to the DD/CGRAM. But to the DD/CGRAM bisplay is not shifted.		_					1	1	0	*	*		5×8 dot character font.
Color	3	Display on/off control											
Address by one and to shift the cursor to the right at the time of write to the DD/CGRAM. Display is not shifted.		0	0	0	0	0	0	1	1	1	0		because of initialization.
Set DDRAM address	4	Entry mode set											
Sets DDRAM address Set DDRAM address O O O O O O O O O		0	0	0	0	0	0	0	1	1	0		
Set DDRAM address Set DDRAM address O O O O O O O O O													of write to the DD/CGRAM.
H Been selected by initialization when the power was turned on. The cursor is incremente by one and shifted to the right of the right of the power was turned on. The cursor is incremente by one and shifted to the right of the right of the power was turned on. The cursor is incremente by one and shifted to the right of the right of the power was turned on. The cursor is positioned at the power was turned on. The cursor is positioned at the power was turned on. The cursor is positioned at the power was turned on. The cursor is positioned at the power was turned on. The cursor is positioned at the power was turned on. The cursor is positioned at the power was turned on. The cursor is positioned at the power was turned on. The cursor is positioned at the power was turned on. The cursor is positioned at the power was turned on. The cursor is positioned at the power was turned on. The cursor is positioned at the power was turned on. The cursor is positioned at the power was turned on. The cursor is positioned at the power was turned on. The cursor is positioned at the power was turned on. The cursor is positioned at the power was turned on. The cursor is positioned at the power was turned on. The power w													
1	5	Wri	te dat	ta to C	CGRA	M/DI	DRAN	1				Н	
On. The cursor is incremente by one and shifted to the right		1	0	0	1	0	0	1	0	0	0		
Write data to CGRAM/DDRAM 1 0 0 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0													on. The cursor is incremented
7 Write data to CGRAM/DDRAM 1 0 0 1 0 0 1													by one and shifted to the right
1 0 0 1 0 0 1 0 0 1	6						-					- - -	
1	7	Write data to CGRAM/DDRAM										HITACHI	Writes I.
Set DDRAM address Company		1	0	0	1	0	0	1	0	0	1	mracm	
10	R											Sets DDRAM address so that	
Sets mode to shift display at the time of write.	0						0	0	0	•	•	HITACHI	t
9 Write data to CGRAM/DDRAM HITACHI Writes M. Writes M.		U	U	1	1	U	U	U	U	U	U		The cursor is positioned at the Head of the second lime.
1	9	Wri	te dat	a to C	CGRA	M/DI	DRAN	1				ШТАСШ	
10									1	0	1		
11 Write data to CGRAM/DDRAM 1 0 0 1 0 0 1 1 1 1 1 12 Entry mode set 0 0 0 0 0 0 0 1 1 1 1 1 13 Write data to CGRAM/DDRAM 1 0 0 1 0 0 1 1 0 0 1 14 Writes O. HITACHI MICROCO Writes O. HITACHI MICROCO Writes M. Display is shifted the left. The first and second lines both shift at the same time. 14 Return home Returns both display and cursor to the original position.	10	_					•						
1	10											· ·	
1		***			10D 1	3.5/33.7							W '4 O
12 Entry mode set 0 0 0 0 0 0 0 1 1 1 1 1 Write data to CGRAM/DDRAM 1 0 0 1 0 0 1 1 0 0 1 13 Write data to CGRAM/DDRAM 1 0 0 1 0 0 1 1 0 0 1 Trachi ICROCOM Writes M. Display is shifted the left. The first and second lines both shift at the same time. 14 Return home 15 Return home 16 Return home 17 Return both display and cursor to the original position	11	Wri	Write data to CGRAM/DDRAM										Writes O.
The first and second lines both shift at the same time. Tachi Itachi Itachi		1	0	0	1	0	0	1	1	1	1	MICROCO_	
13 Write data to CGRAM/DDRAM 1 0 0 1 0 0 1 1 0 0 1 14 ITACHI ICROCOM Writes M. Display is shifted the left. The first and second lines both shift at the same time. Return home Returns both display and cursor to the original position	12	Entry mode set										THE A CHIL	Sets mode to shift display at
Write data to CGRAM/DDRAM 1 0 0 1 0 0 1 1 0 0 1 15 Return home 10 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	12		•			0	0	0	1	1	1		the time of write.
1 0 0 1 0 0 1 1 0 1 14 15 Return home 1	13												Writes M. Display is shifted to
15 Return home HITACHI MICROCOM Cursor to the original position									1	0	1		the left. The first and second lines both shift at the same
The tall Home cursor to the original position with the property of the original position of the	14						•					:	
The tall Home cursor to the original position with the property of the original position of the												:	
MICROCOM cursor to the original position	15	Reti	Return home									HITACHY	Returns both display and
		0	0	0	0	0	0	0	0	1	0		cursor to the original position (address 0).

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