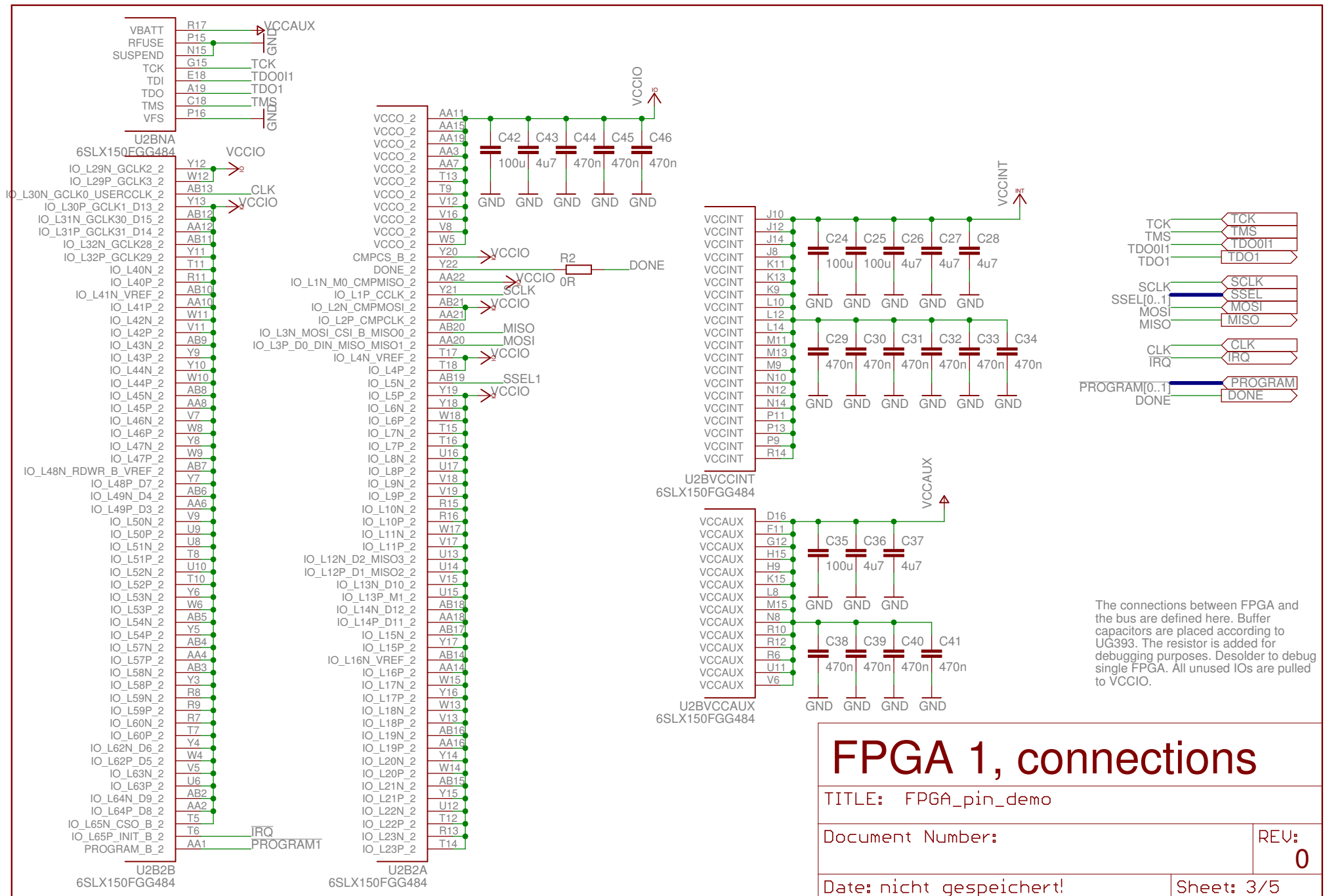


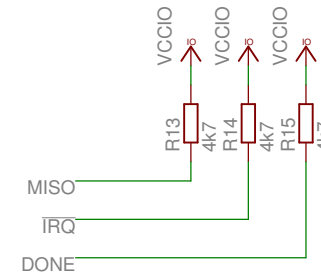
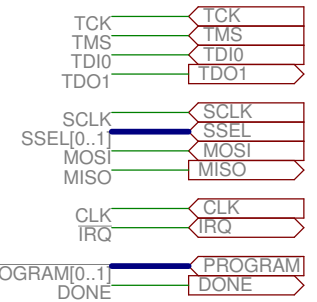
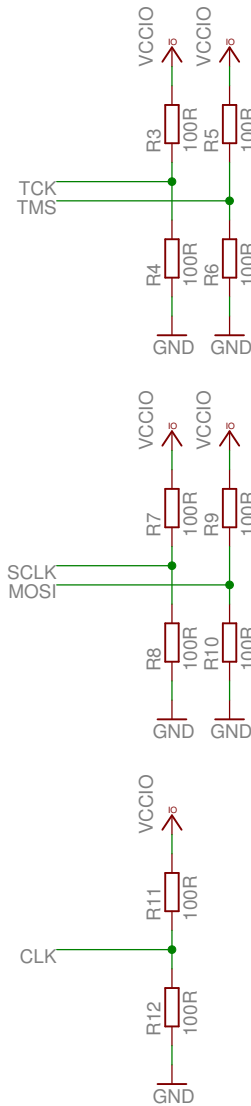
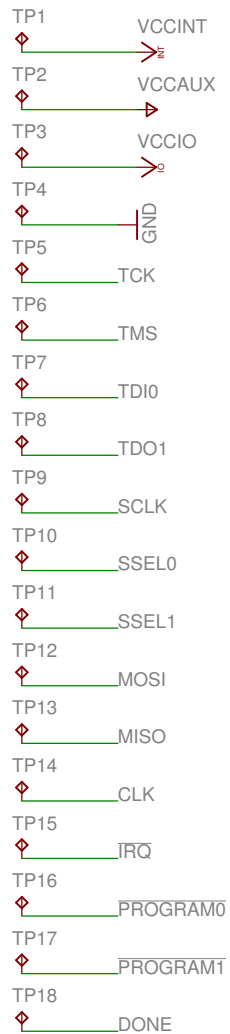
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FPGA 1, connections		
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# Bus Termination

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