

VERSION HISTORY

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01 NOTE

02 DIAGRAM

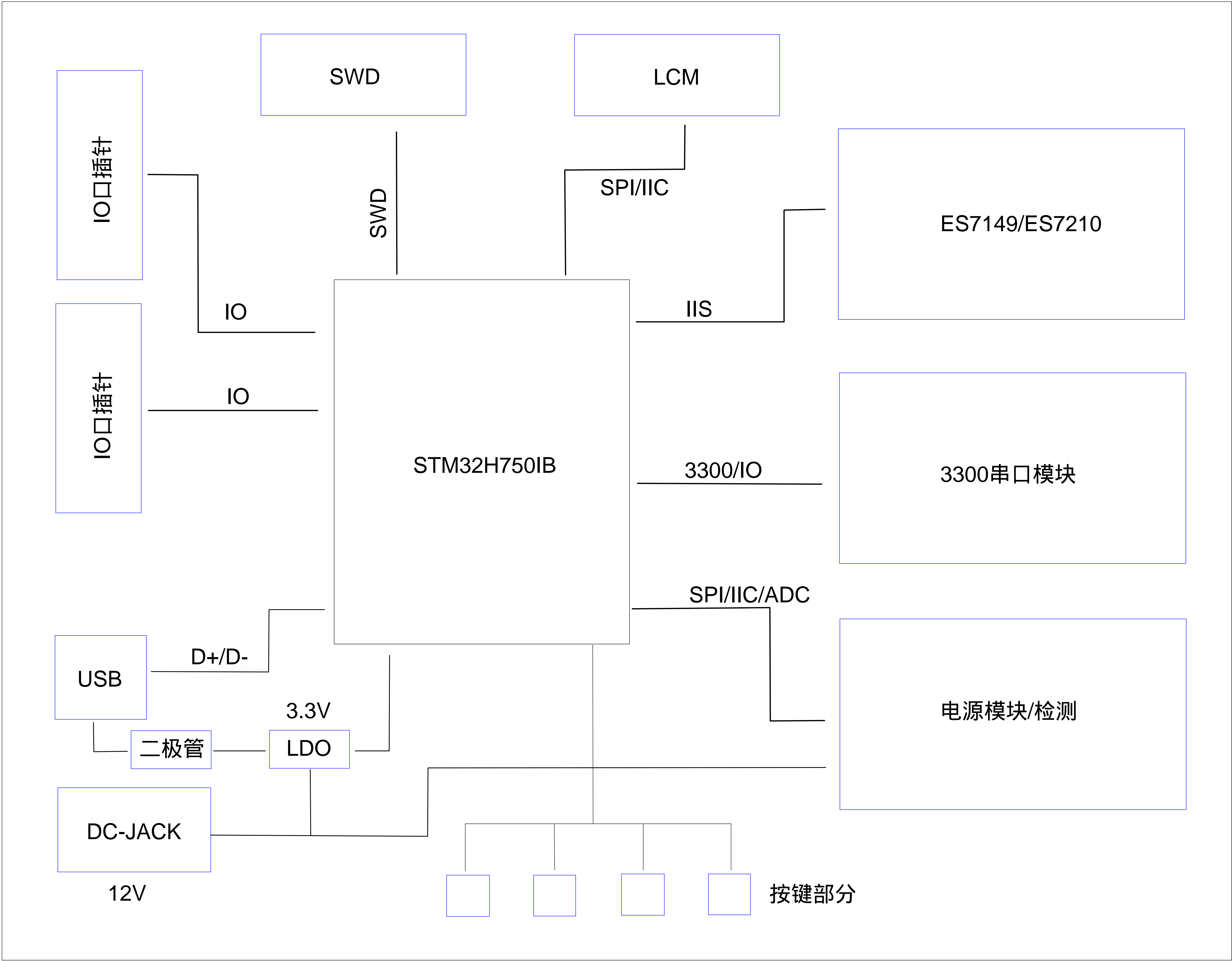
03 STM32H750IB

Revision	Description	Data	Author
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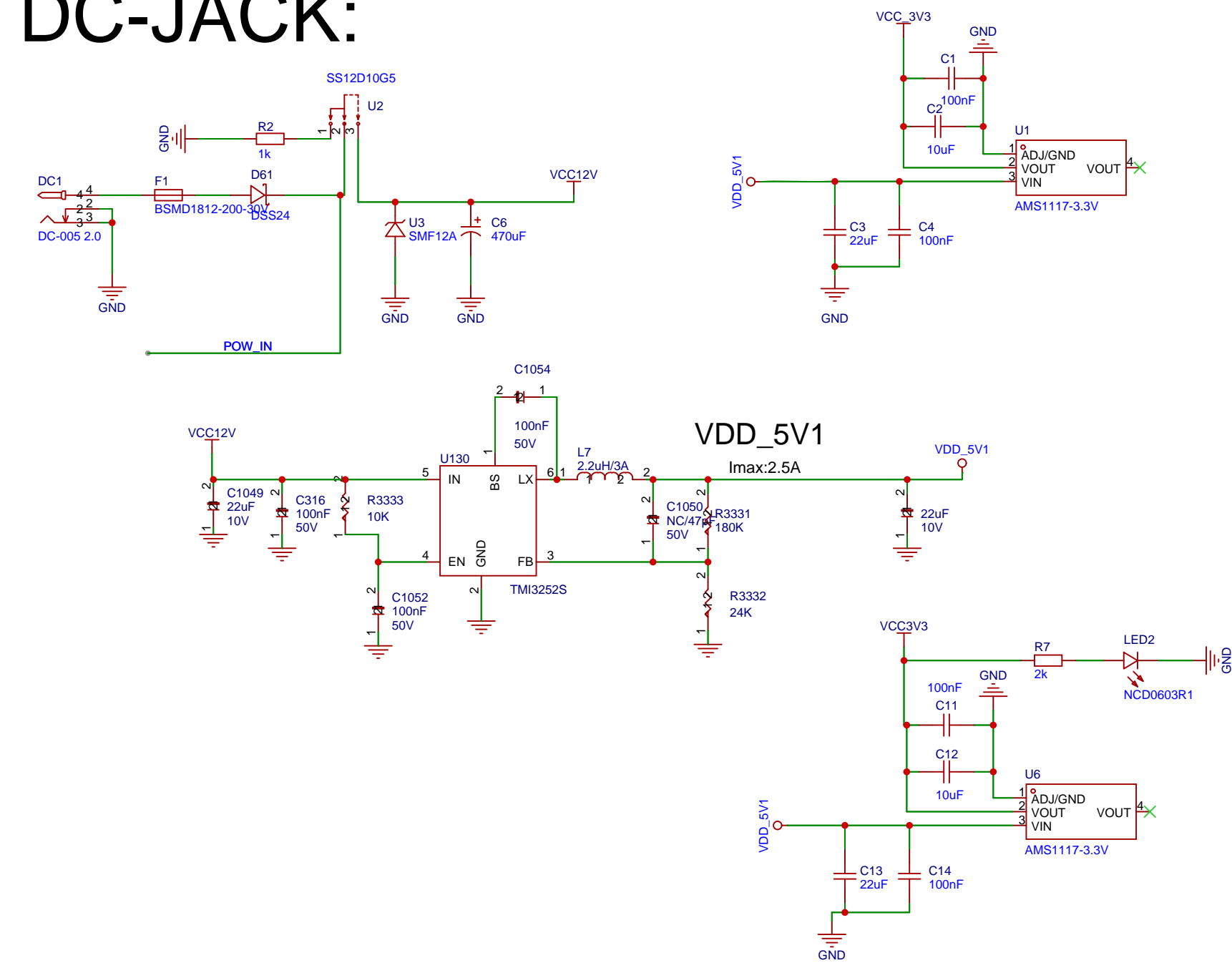
首版设计 2025.1.14

原理图	Schematic1			更新日期	2025-03-10
网页	01-NOTE			创建日期	2025-01-07
绘制	汪远心	物料编码			
审阅		STM32H750			
	版本	尺寸	页 1 共 6		
		V1.0	A2	ListenAI	

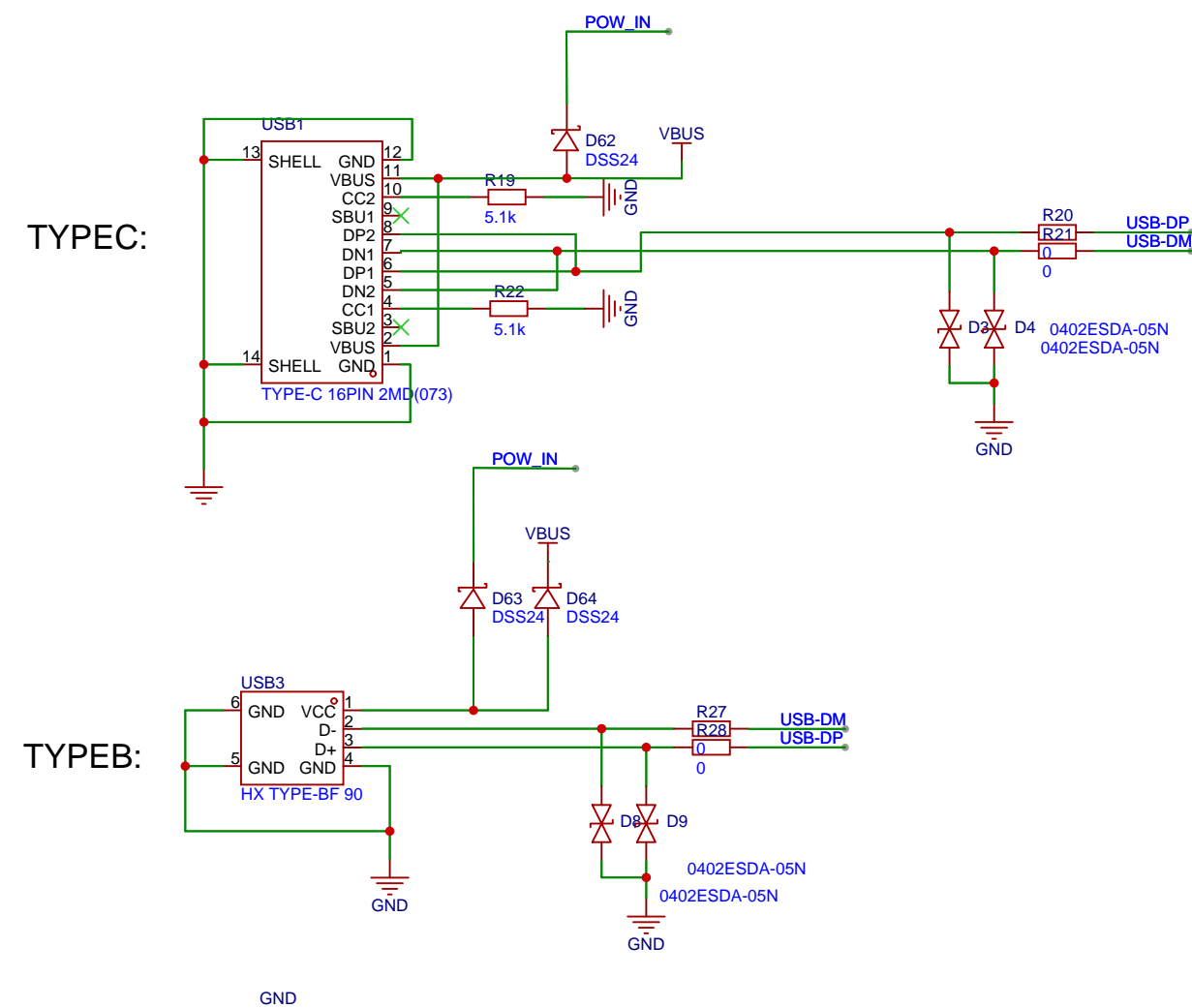
硬件方框图：



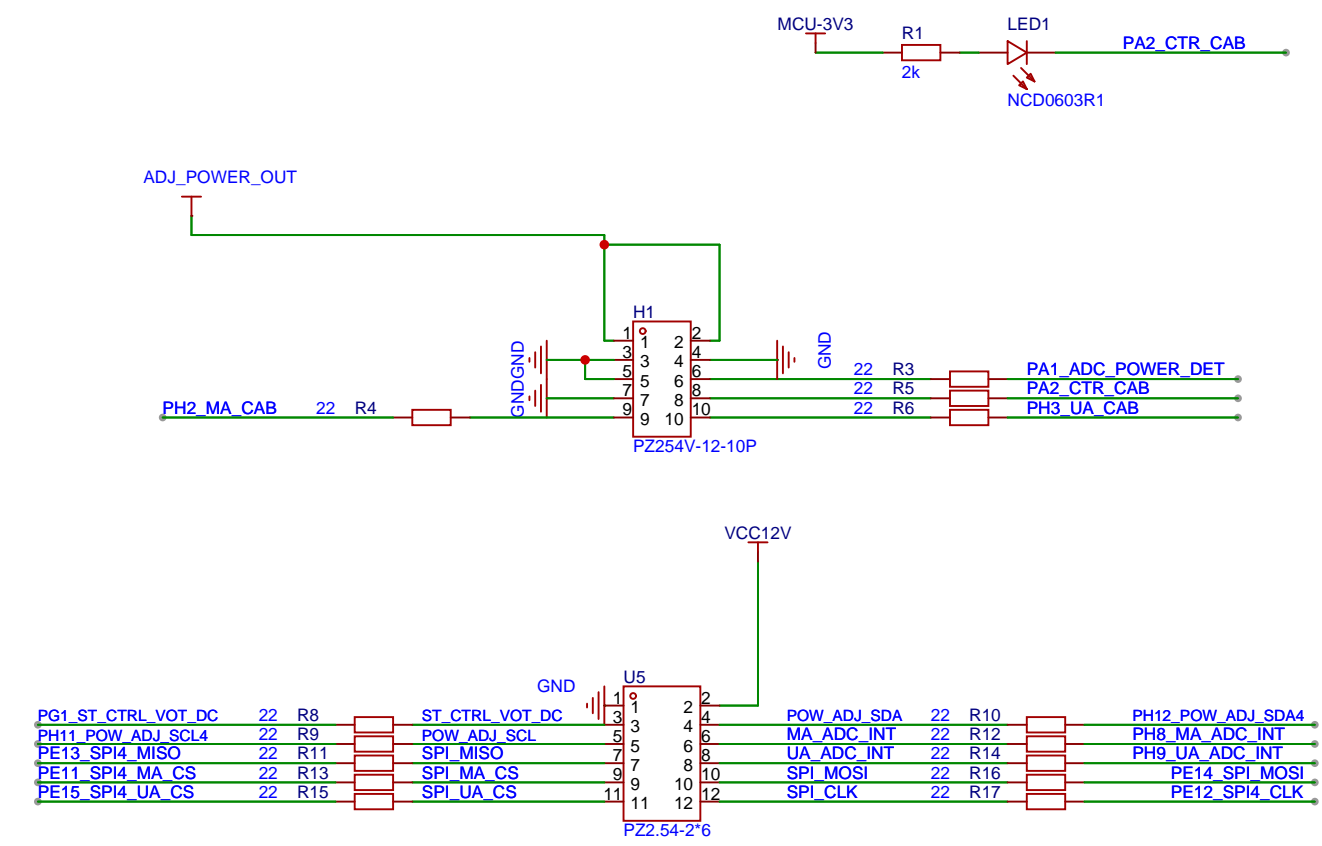
DC-JACK:



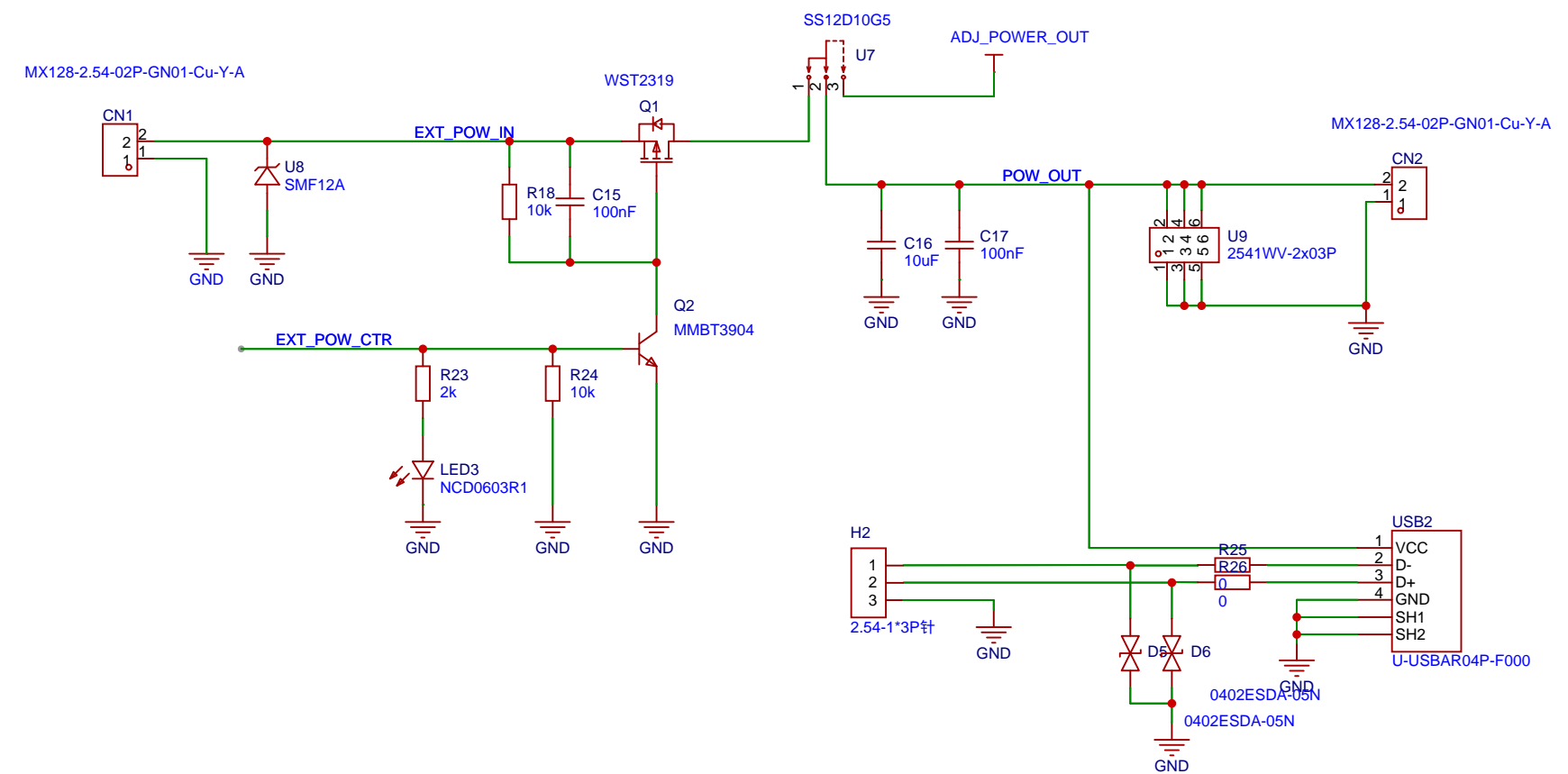
USB-JACK:




POWER_MODULE

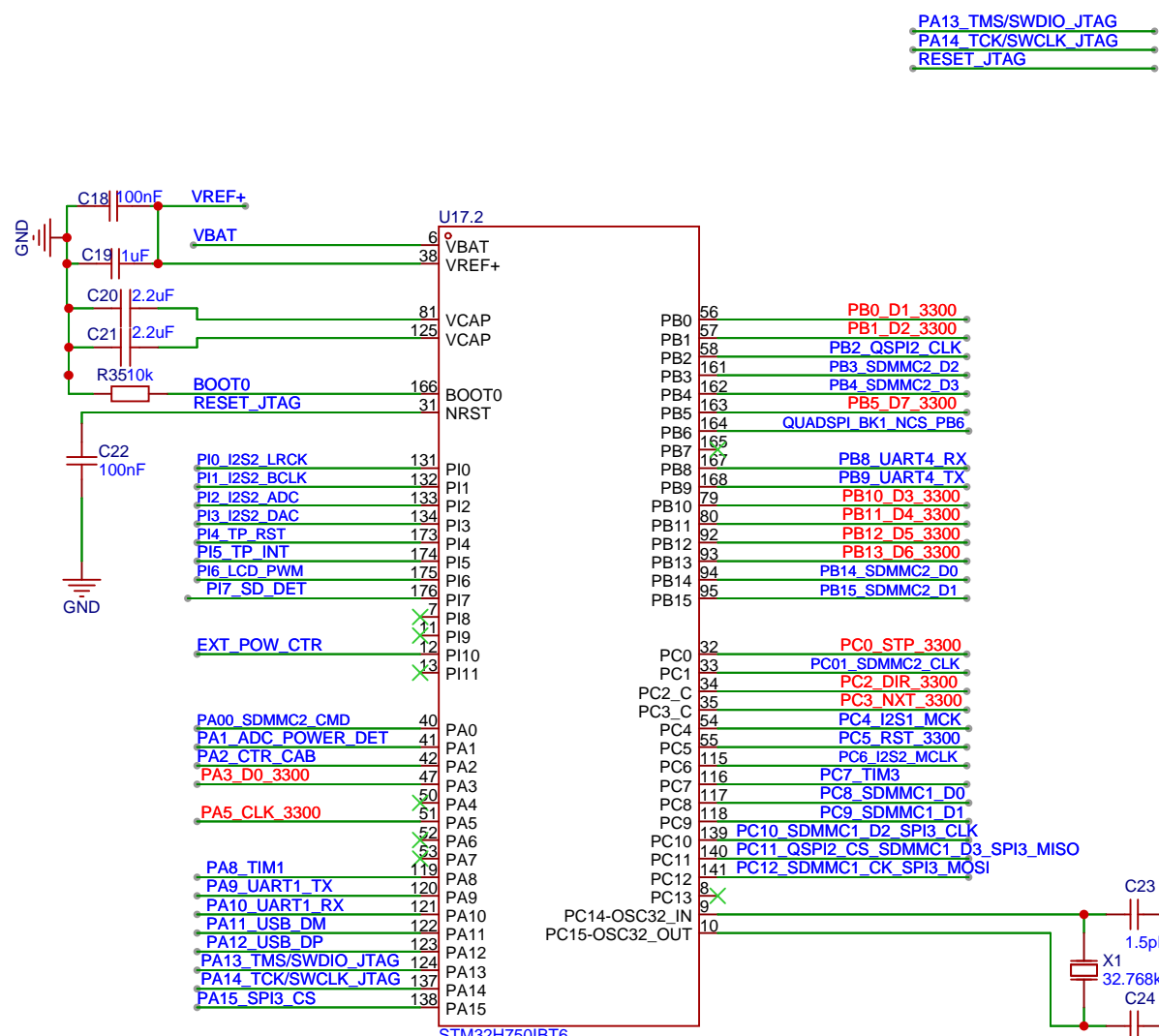


POWER_OUT

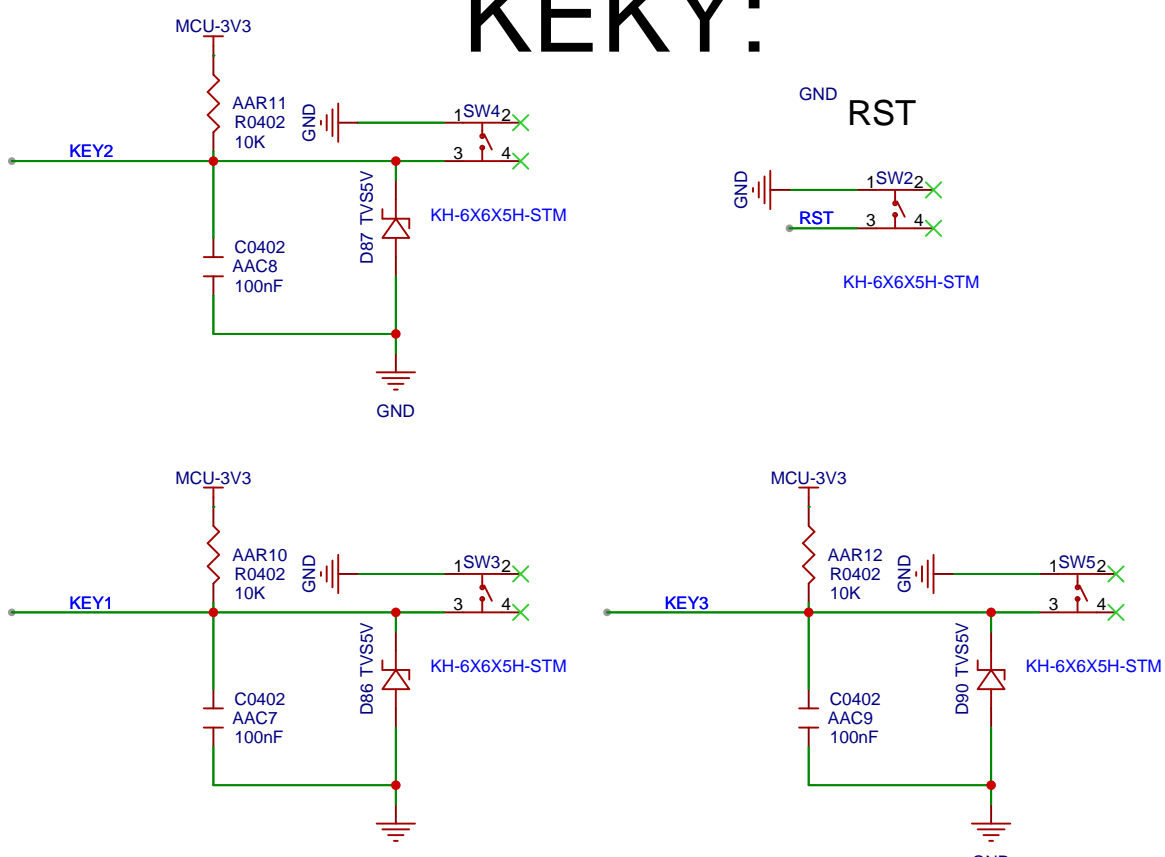


原理图	Schematic1			更新日期	2025-04-19
				创建日期	2025-03-27
图页	03-POW			物料编码	
绘制	hy	STM32H750			
审阅					
		版本	尺寸	页	3 共 6
 嘉立创EDA	V1.0	A2	ListenAI		

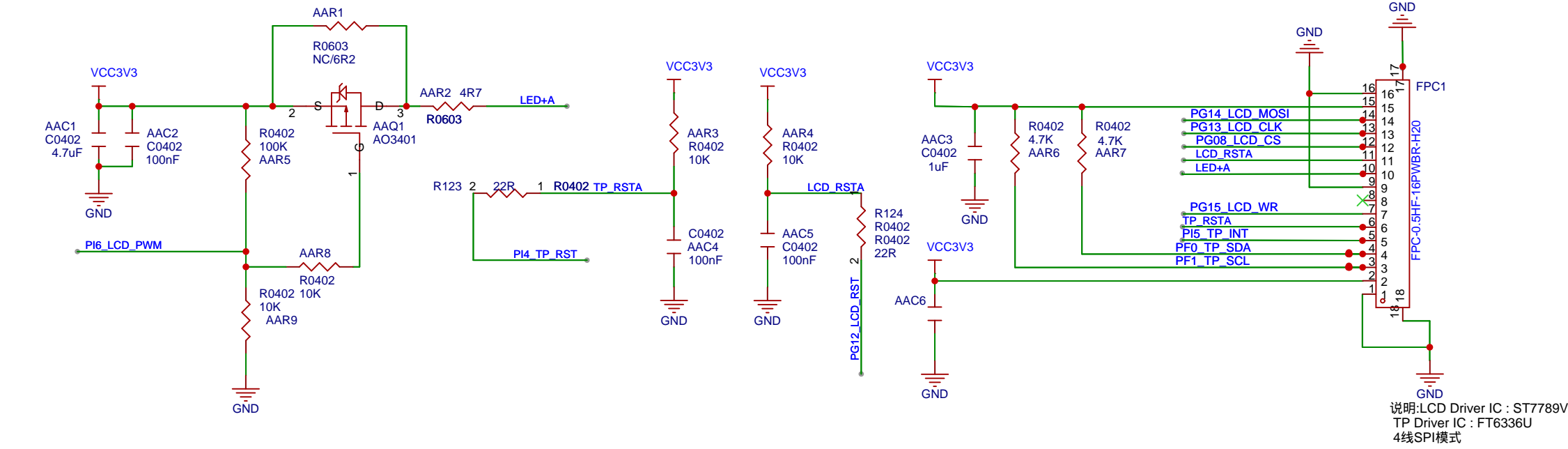
STM32H750IB: JTAG:



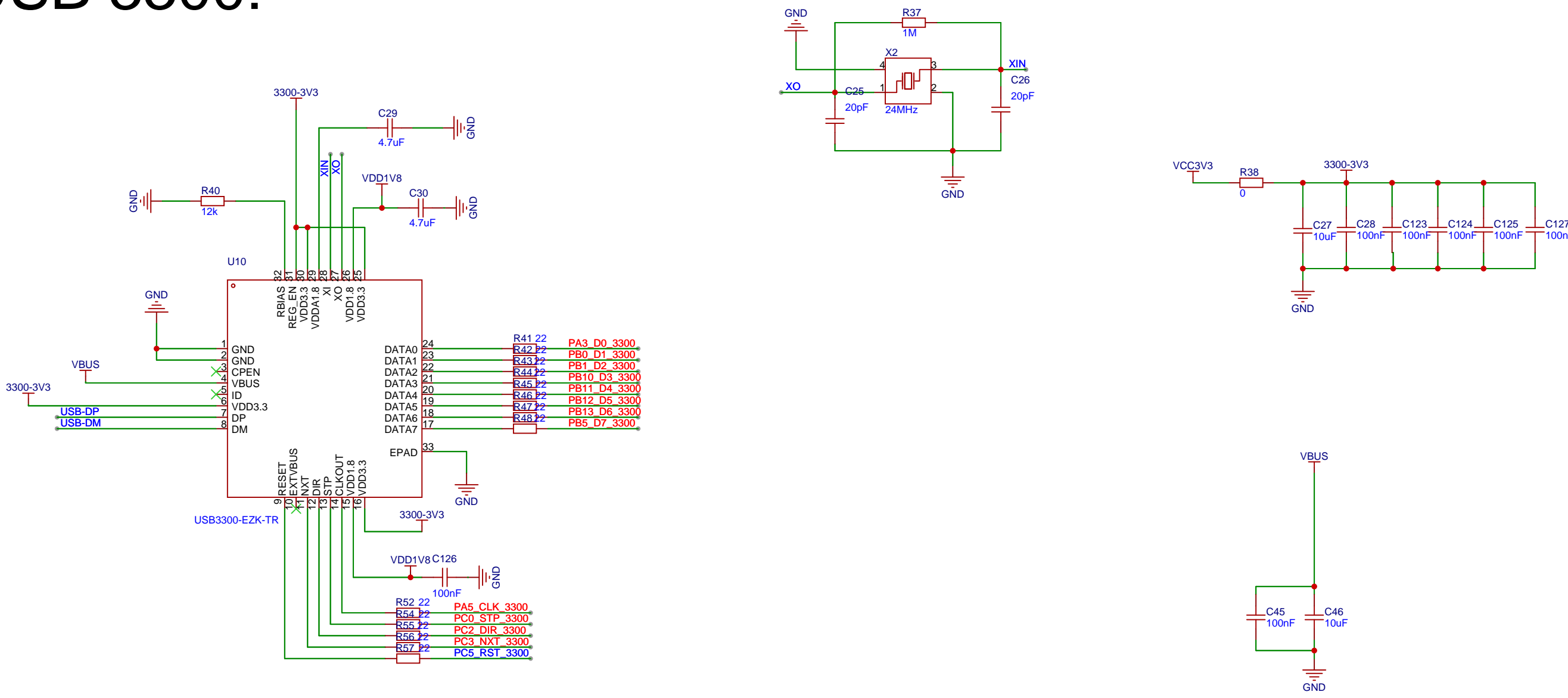
KEY:



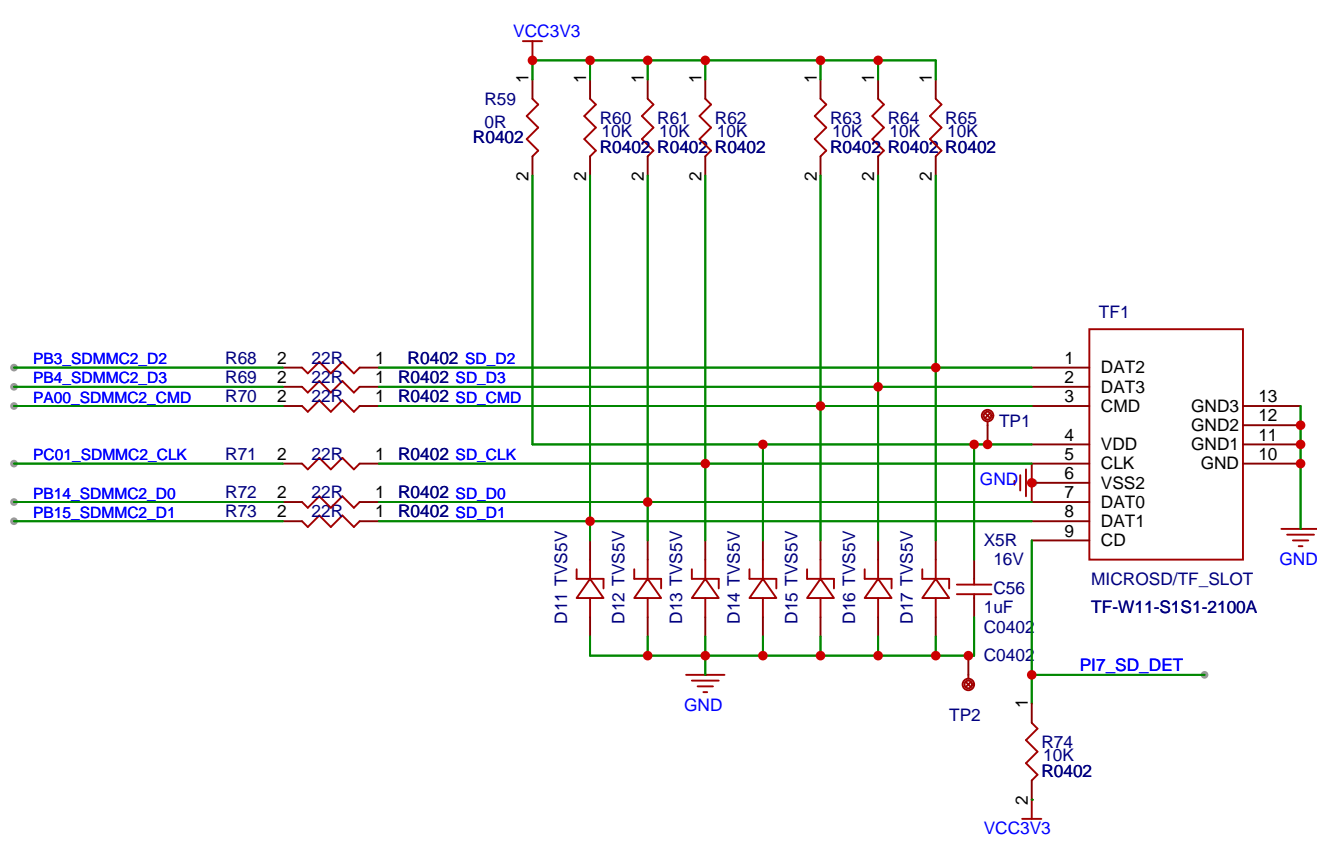
LCD+TP :



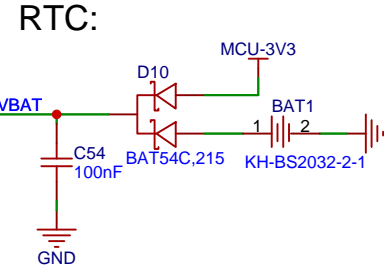
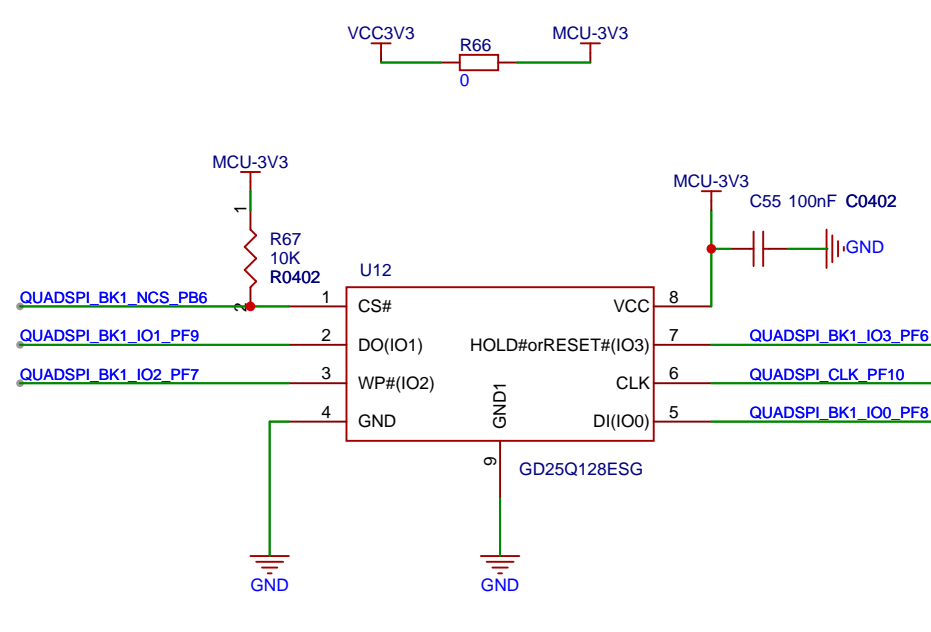
USB 3300:



MICRO TF CARD:

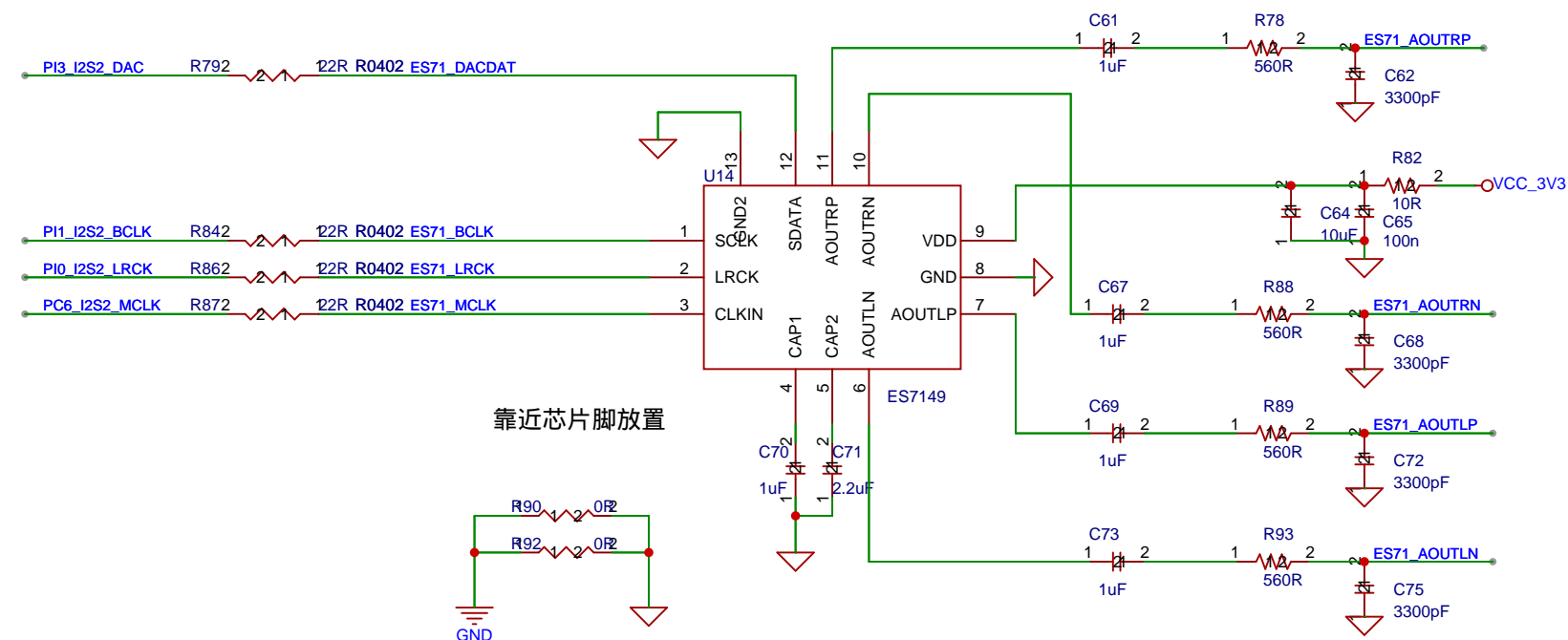


FLASH

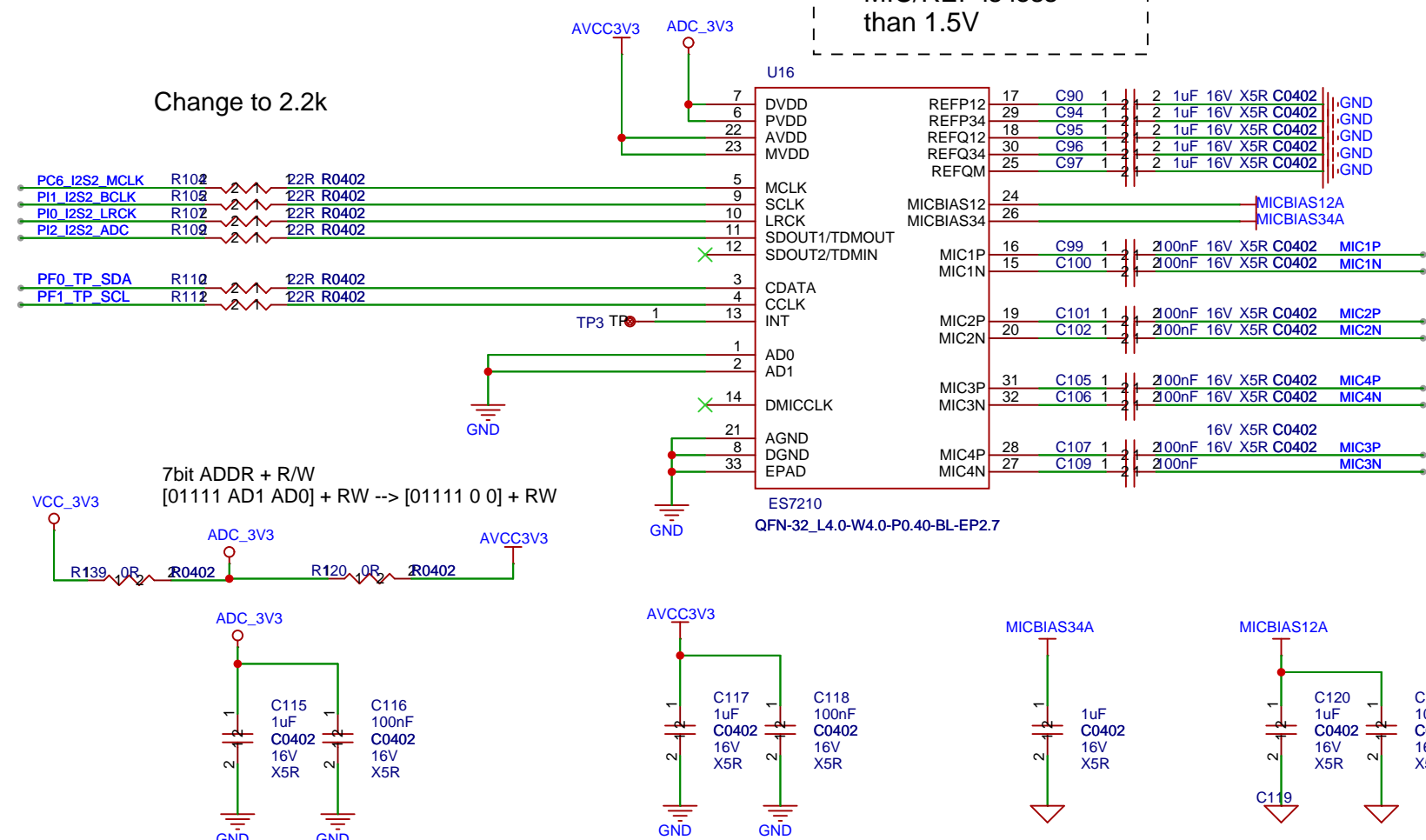


原理图	Schematic1	更新日期	2025-04-19
图元	04-STM32H750IB	创建日期	2025-01-14
绘制	汪远心	物料编码	
审阅			
STM32H750			
版本	V1.0	尺寸	A2
页	4	共	6
ListenAI			

ES7149 :



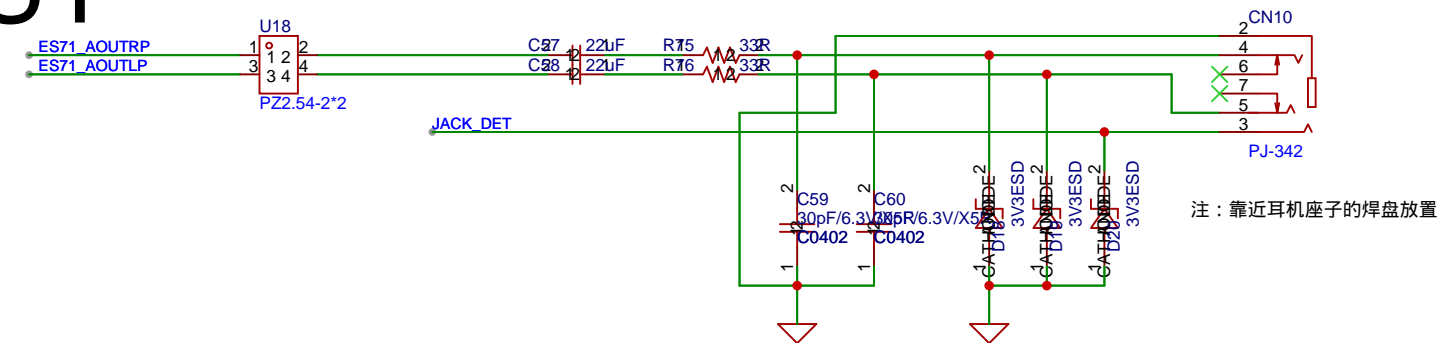
ES7210 :



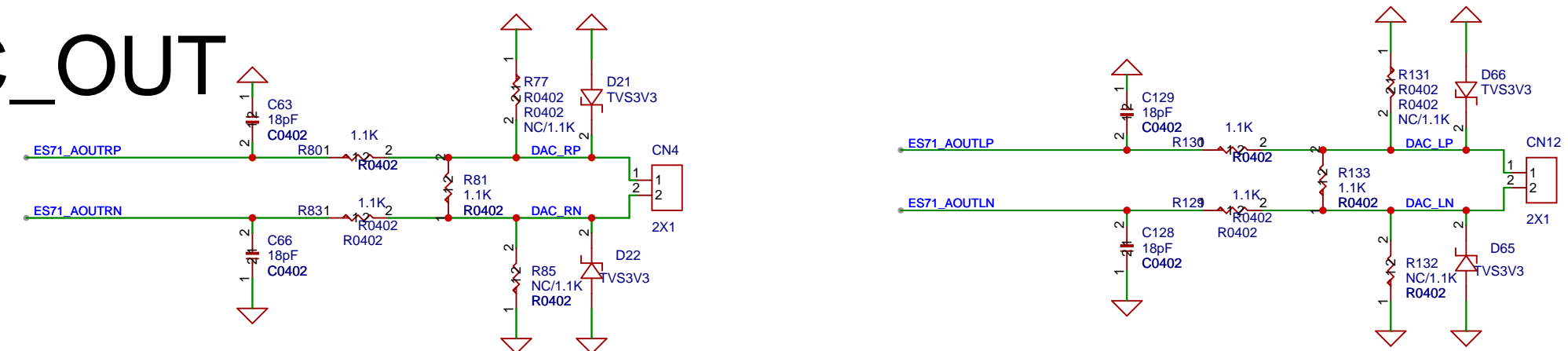
Note:

Make sure that the input common mode voltage of MIC/REF is less than 1.5V

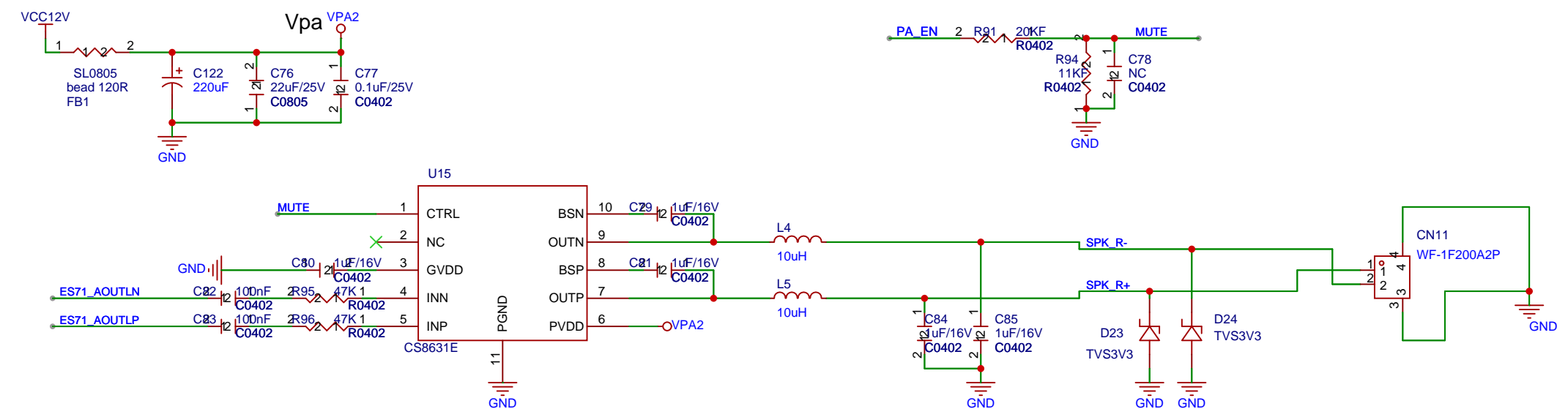
DAC_OUT



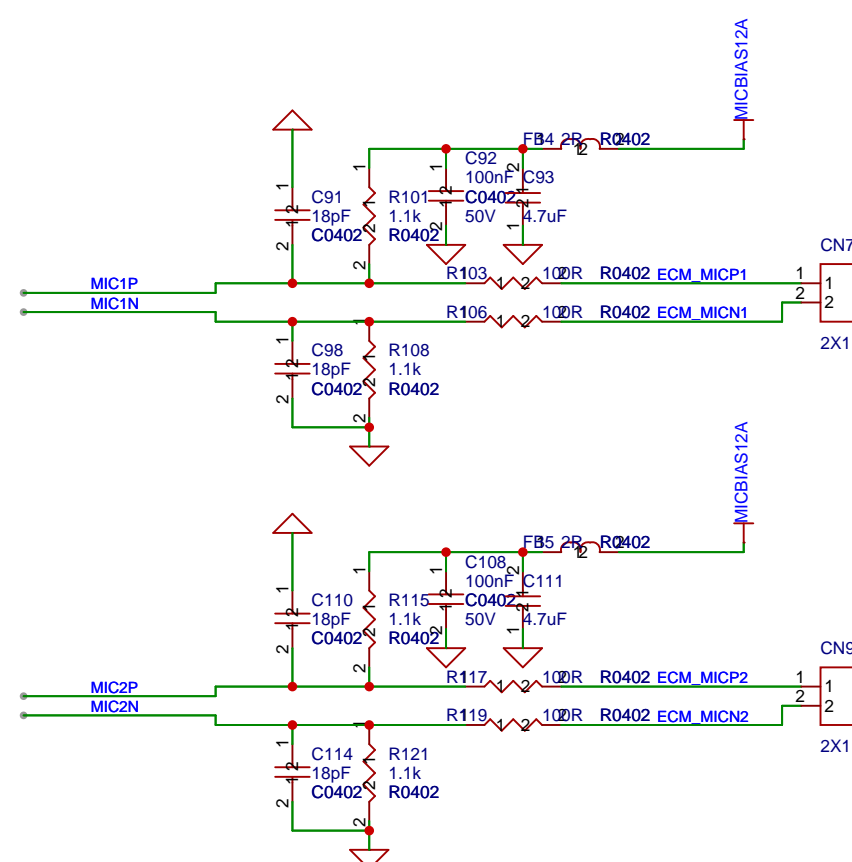
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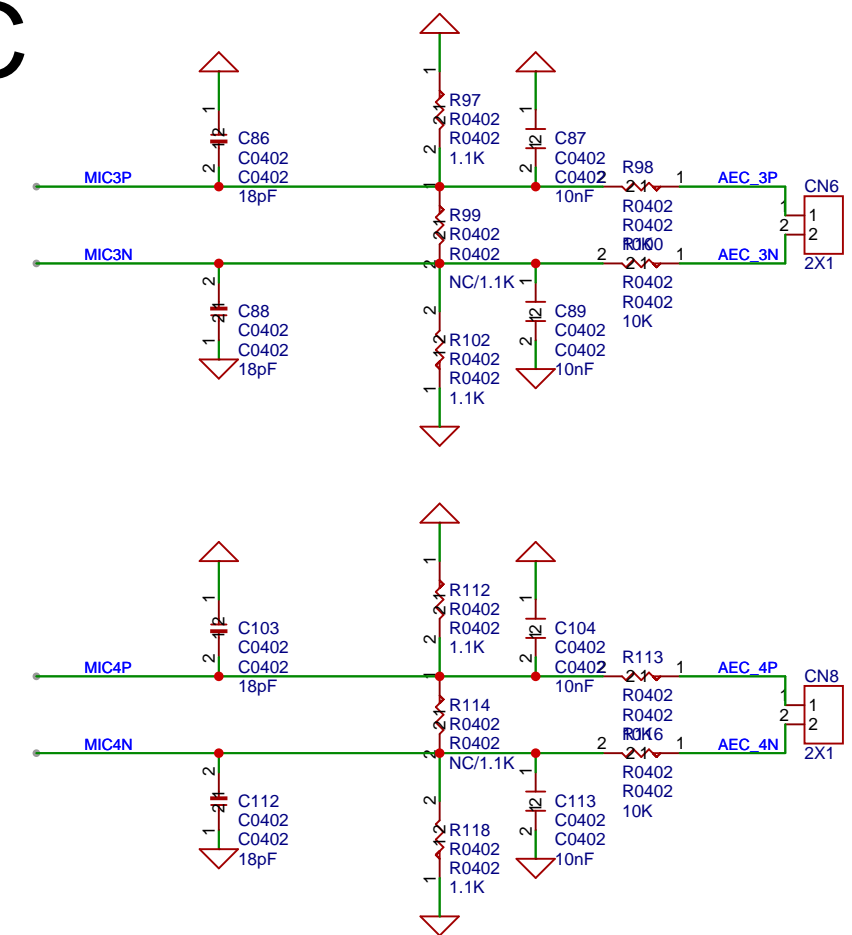
PA




MIC

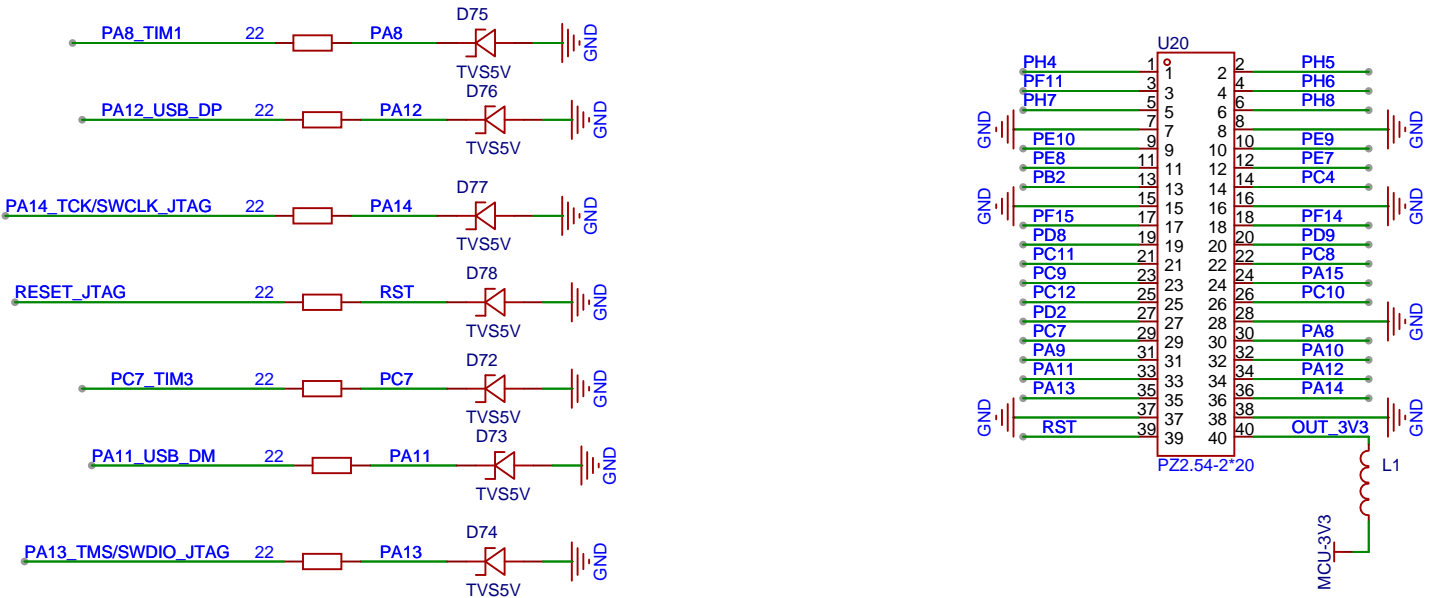


AEC

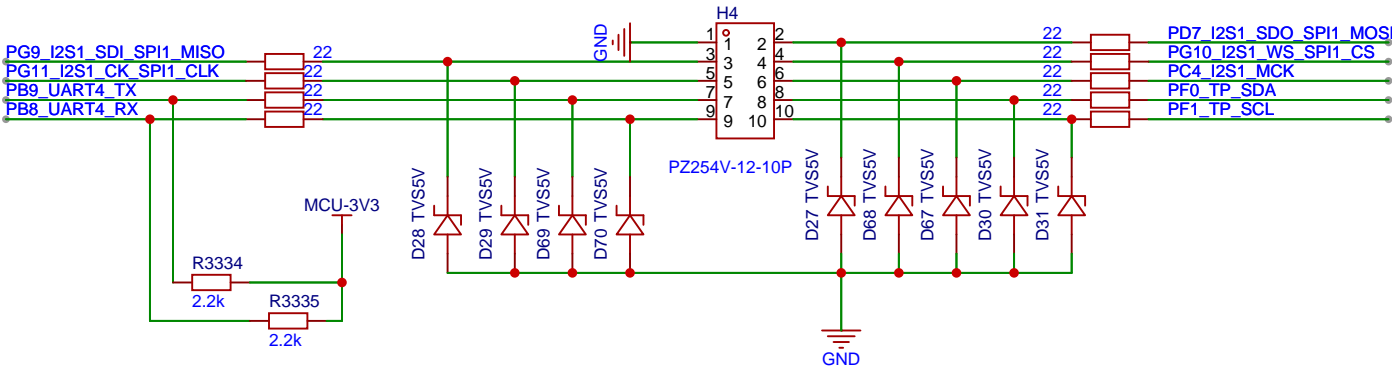


原理图	Schematic1			更新日期	2025-04-19
				创建日期	2025-04-02
图页	05 ADC&DAC&SPK			物料编码	
绘制	hy	STM32H750			
审阅					
		版本	尺寸	页	5 共 6
 嘉立创EDA		V1.0	A2	ListenAI	

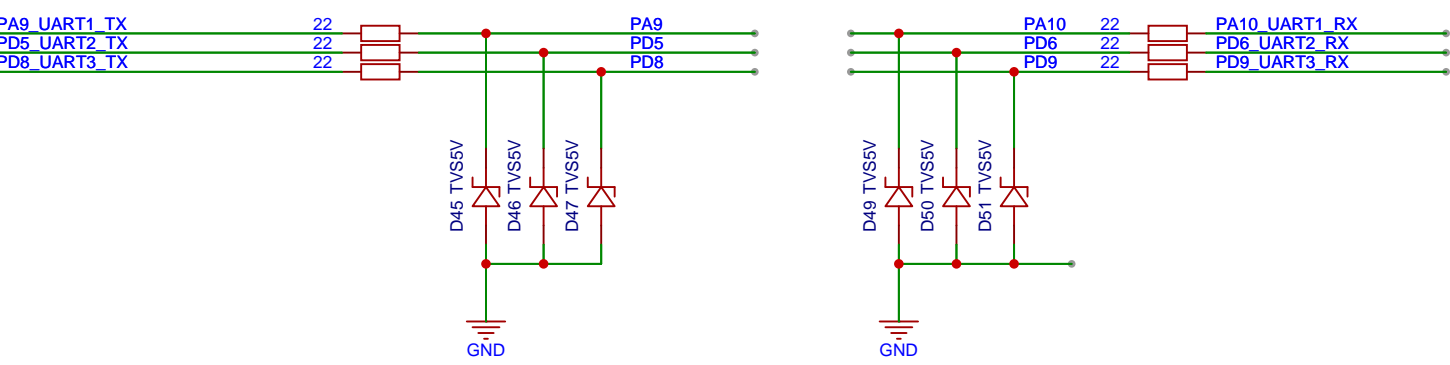
排针



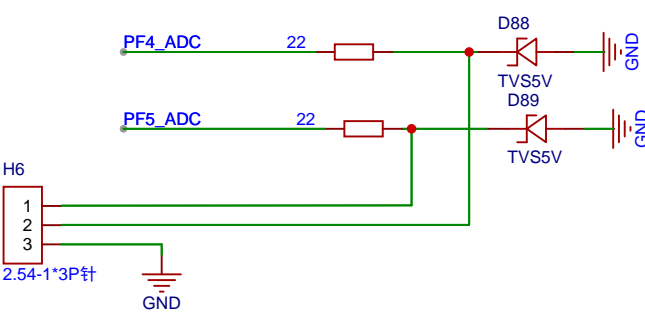
I2S1



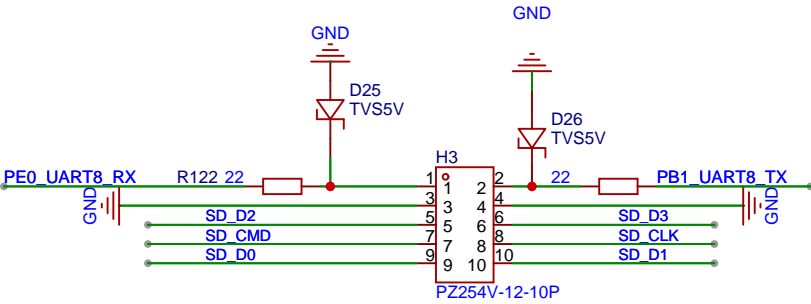
UART&I2C



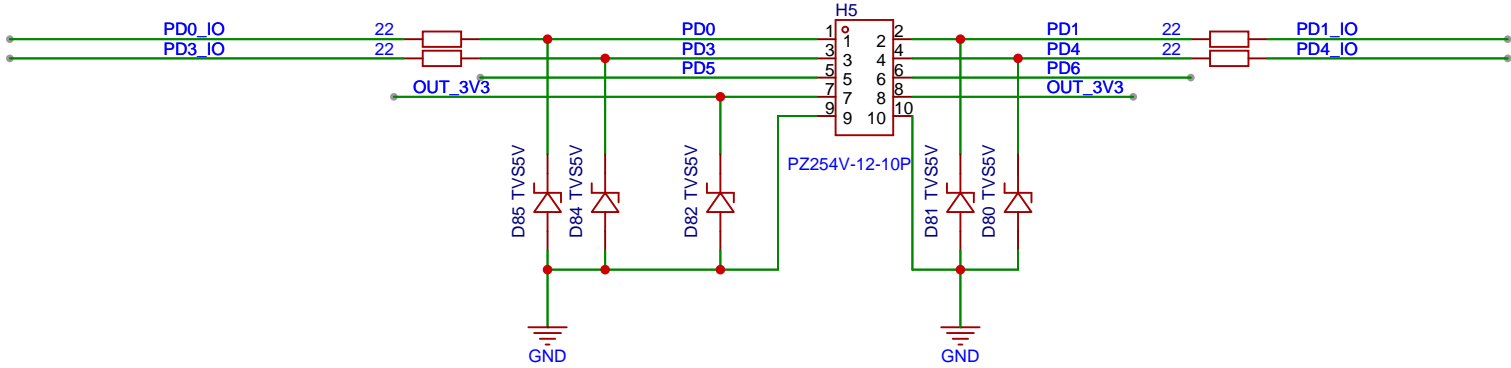
ADC



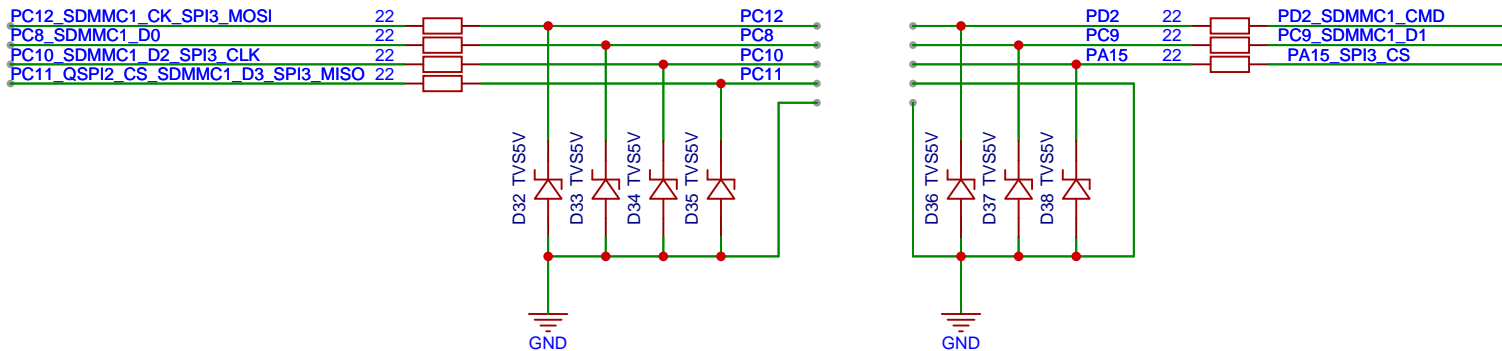
SD2



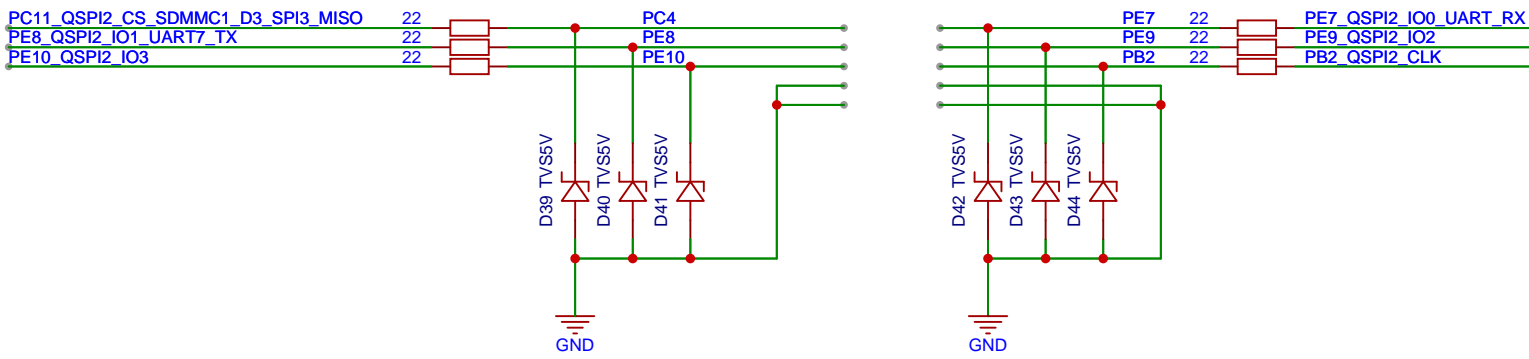
IO



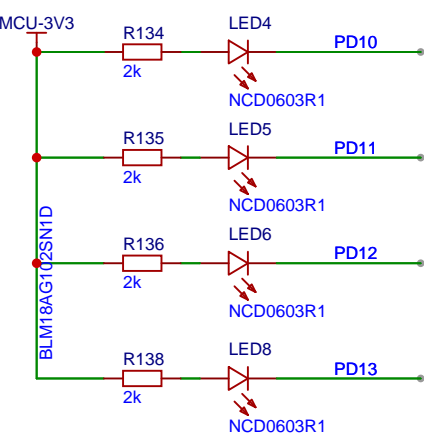
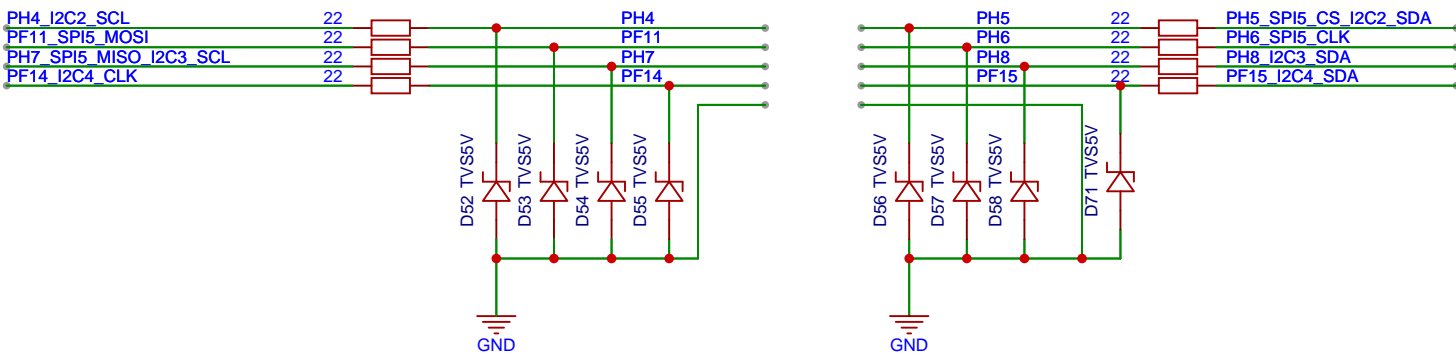
SD1




QSPI



SPI5



原理图	Schematic1			更新日期	2025-04-19
图页	06 IO_MAX			创建日期	2025-04-09
绘制	pengwang	STM32H750			
审阅					
		版本	尺寸	页	6 共 6
		V1.0	A2	ListenAI	