

experiment1

Addr7 = 1

Addr6 = 0

Addr5 ~ 3 = 000 ~ 111 ($Y_0 \sim Y_7$)

Addr2 ~ 0 = 000 ~ 111

Memory Chip	Address range	binary
Y_0	80h ~ 87h	10000xxx
Y_1	88h ~ 8Fh	10001xxx
Y_2	90h ~ 97h	10010xxx
Y_3	98h ~ 9Fh	10011xxx
Y_4	0A0h ~ 0A7h	10100xxx
Y_5	0A8h ~ 0AFh	10101xxx
Y_6	0B0h ~ 0B7h	10110xxx
Y_7	0B8h ~ 0BFh	10111xxx