

HAMID FARZANEH

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Dresden - Germany



EDUCATION

PhD in Computer Science
Compiler Design

Dresden University of Technology

Aug 2022 – Present

Dresden - Germany

MSc in Computer Engineering
Computer Systems Architecture
GPA: 18.45/20

Shahid Beheshti University

Sep 2019 – Nov 2021

Tehran - Iran

BSc in Computer Engineering
Software Engineering
GPA: 16.20/20

Shiraz University

Sep 2014 – Aug 2019

Shiraz - Iran

PROGRAMMING LANGUAGES

C/C++

CUDA

Python

Chisel

OpenMP

Verilog

Java

TOOLS AND TECHNOLOGIES

MLIR

PyTorch

Chipyard

HLS Tools

LLVM

Gem5

Verilator

Linux

REFEREES

Prof. Jeronimo Castrillon

@ jeronimo.castrillon@tu-dresden.de

Center for Advancing Electronics Dresden (cfaed) - TU Dresden, Germany

Dr. Asif Ali Khan

@ asif_ali.khan@tu-dresden.de

TU Dresden, Germany

RESEARCH INTEREST

- Compiler
- Computer Architecture
- Hardware Accelerators
- Heterogeneous Systems
- In/Near-Memory Systems

EXPERIENCE

Teaching Assistant

Course: Hardware Accelerator

Feb 2021 – Jun 2021 Shahid Beheshti University

- Helped the teacher with class preparation, assignments and the course's final project.

Teaching Assistant

Course: Test and Testability

Sep 2020 – Jan 2021 Shahid Beheshti University

- Helped the teacher with class preparation, assignments and the course's final project.

Teaching Assistant

Course: Operating Systems Lab

Feb 2017 – Jun 2017 Shiraz University

- Organized tutorial discussions on synchronizations and Linux file system

Teaching Assistant

Course: Programming Principles

Feb 2016 – Jun 2016 Shiraz University

- Helped the teacher with class preparation and course assignments
- Hold extra classes 4 times a week

Teaching Assistant

Course: System Programming and Assembly Language

Feb 2015 – Jun 2015 Shiraz University

- Performed all regular duties of a T.A. including occasional lectures and marking