

COMPUTER ORGANIZATION & ARCHITECTURE

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LAB SESSION: 05

DATE: March 03, 2023

Topics:

A Guide to Debugging MIPS code

Data declaration

The QtSPIM control bar has the following controls on it:

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From left to right, they are:

- Load file
- Reinitialize and load file
- Save logs
- Print logs
- Clear registers
- Reinitialize simulator
- Run/continue program
- Pause
- Stop
- Step through the program line-by-line
- Help Common issues:
 - Make sure you always use the “Reinitialize and load file” button (unless it’s the first program you’re loading since you’ve opened QtSPIM), so that residual values from previous executions don’t interfere with your new execution.
 - If you see a message along the lines of “Instruction references undefined symbol at ... jal main” that means you forgot to load your program, or you didn’t define a main in your program.
 - Use la only for loading addresses of variables declared in the .data segment of your program. Using la like: la \$reg2, offset(\$reg1) is technically correct, but it does something very different from what you’re probably expecting.

It simply loads the effective address, rather than load the actual value from that address. In other words, it executes the following operation: $\$reg2 = (\text{value in } \$reg1) + \text{offset}$, and not: $\$reg2 = \text{value in } (\$reg1 + \text{offset})$ as you might expect. You need to use lw for that.

- If you have taken care of all these common mistakes and bugs still persist, you need to debug your program by manually stepping through the code line-by-line.

Debugging Line-by-Line :

Since MIPS assembly is a low-level language that is directly assembled into bytecode that is executed by the processor (or in our case, a simulator such as QtSPIM), there aren't any specialized tools to debug the programs.

- The good news, however, is that since QtSPIM is a simulator that shows you real time contents of the registers and the entire memory (as well as the execution stream of code), you don't need any more tools than QtSPIM to debug any MIPS program.

Setting a breakpoint: A breakpoint is a point in your code up to which the program will execute in one go. At that point, you will gain control and you can step through it line by line.

Right click on a line and then choose “Set breakpoint” or “Clear breakpoint” to set or clear a breakpoint.

Real-time register values: The two tabs on the left show you the contents of all the integer and floatingpoint registers (in separate tabs) in real time.

Real-time execution stream: If you step through your program line-by-line, QtSPIM will highlight the line of code (in the “Text” tab) that is about to be executed.

A single line in the text tab looks like this: [00400010] 00c23021 addu \$6, \$6, \$2 ; 187: addu \$a2 \$a2 \$v0 It is interpreted as follows:

- [00400010] - The address (in memory) of the current instruction is 0x00400010, in hexadecimal notation. This is also the value of the PC (program counter).
- 00c23021 - The 32-bit representation of the assembly instruction, written in hexadecimal notation.
- addu \$6, \$6, \$2 - The raw interpreted assembly command that will be executed.
- 187: The line number in the source file that contains this command
- addu \$a2 \$a2 \$v0 - The command as written by the human in the source file.

Real-time memory values: You can inspect values across the entire address space in real time by clicking on the “Data” tab (next to the “Text” tab). A single line in the data tab

looks like this: [90000180] 90000024 90000033 9000003b 90000043 \$. . . 3 . . . ; . . . C . . . It is interpreted as follows:

- [90000180] - The address (in hexadecimal notation) of the first byte being displayed on this line. Every line in this screen displays 16 bytes of information. (Which means the next line will have the address [90000190] because that's $90000180 + 10$, and 10 is 16 in hexadecimal.)
- 90000024 90000033 9000003b 90000043 - The spaces exist for your convenience, but it basically means the 16 addresses starting at (and including) 90000180, have the following 16 values in them: 90000024 90000033 9000003b 90000043. This string is also in hexadecimal, which means every pair of digits in this string corresponds to a byte of information. As you can see, there are 16 bytes of information on this line.

• **IMPORTANT:** Note the endianness of the values described above. In the first chunk containing the first 4 bytes (90000024), the first address has the byte 0x24, the second and third addresses have bytes 0x00, and the fourth address has byte 0x90.

- \$. . . 3 . . . ; . . . C . . . - Any information following the 16 bytes of information described in the previous bullet is just the same information but printed out in a human readable form. In other words, if any of the 16 bytes correspond to an ASCII value, that ASCII value is printed out in this section. If the value contained in a byte doesn't correspond to a known, displayable, standard ASCII character it's displayed as a dot.

The following is a quick reference to the most commonly used assembler directives, which was extracted from the above source.

.align *n* Align the next datum on a 2^n byte boundary. For example, `.align 2` aligns the next value on a word boundary. `.align 0` turns off automatic alignment of `.half`, `.word`, `.float`, and `.double` directives until the next `.data` or `.kdata` directive.

.ascii *str* Store the string *str* in memory, but do not null-terminate it.

.asciiz *str* Store the string *str* in memory and null-terminate it.

.byte *b1*,..., *bn* Store the *n* values in successive bytes of memory.

.data <*addr*> Subsequent items are stored in the data segment. If the optional argument *addr* is present, subsequent items are stored starting at address *addr*.

.globl *sym* Declare that label *sym* is global and can be referenced from other files.

.space *n* Allocate *n* bytes of space in the current segment (which must be the data segment in SPIM).

.text <*addr*> Subsequent items are put in the user text segment. In SPIM, these items may only be instructions or words (see the `.word` directive below). If the optional argument *addr* is present, subsequent items are stored starting at address *addr*.

.word *w1*,..., *wn* Store the *n* 32-bit quantities in successive memory words.

Data Declarations

- Placed in section of program identified with assembler directive **.data**
- Declares variable names used in program; storage allocated in main memory (RAM)

format for declarations:

name: storage_type value(s)

- create storage for variable of specified type with given name and specified value

- value(s) usually gives initial value(s); for storage type `.space`, gives number of spaces to be allocated

Note: labels always followed by colon (:)

example

```
var1:    .word    3        # create a single integer variable with initial value 3
array1:  .byte    'a','b'  # create a 2-element character array with elements initialized
                                # to a and b
array2:  .space   40        # allocate 40 consecutive bytes, with storage uninitialized
                                # could be used as a 40-element character array, or a
                                # 10-element integer array; a comment should indicate
```

which!

• A Simple Program

```
#sample example 'add two numbers'
```

```
.text                                # text section
.globl main                          # call main by SPIM

main:  la $t0, value                 # load address 'value' into $t0
        lw $t1, 0($t0)               # load word 0(value) into $t1
        lw $t2, 4($t0)               # load word 4(value) into $t2
        add $t3, $t1, $t2            # add two numbers into $t3
        sw $t3, 8($t0)               # store word $t3 into 8($t0)

.data                                # data section
value: .word 10, 20, 0               # data for addition
```