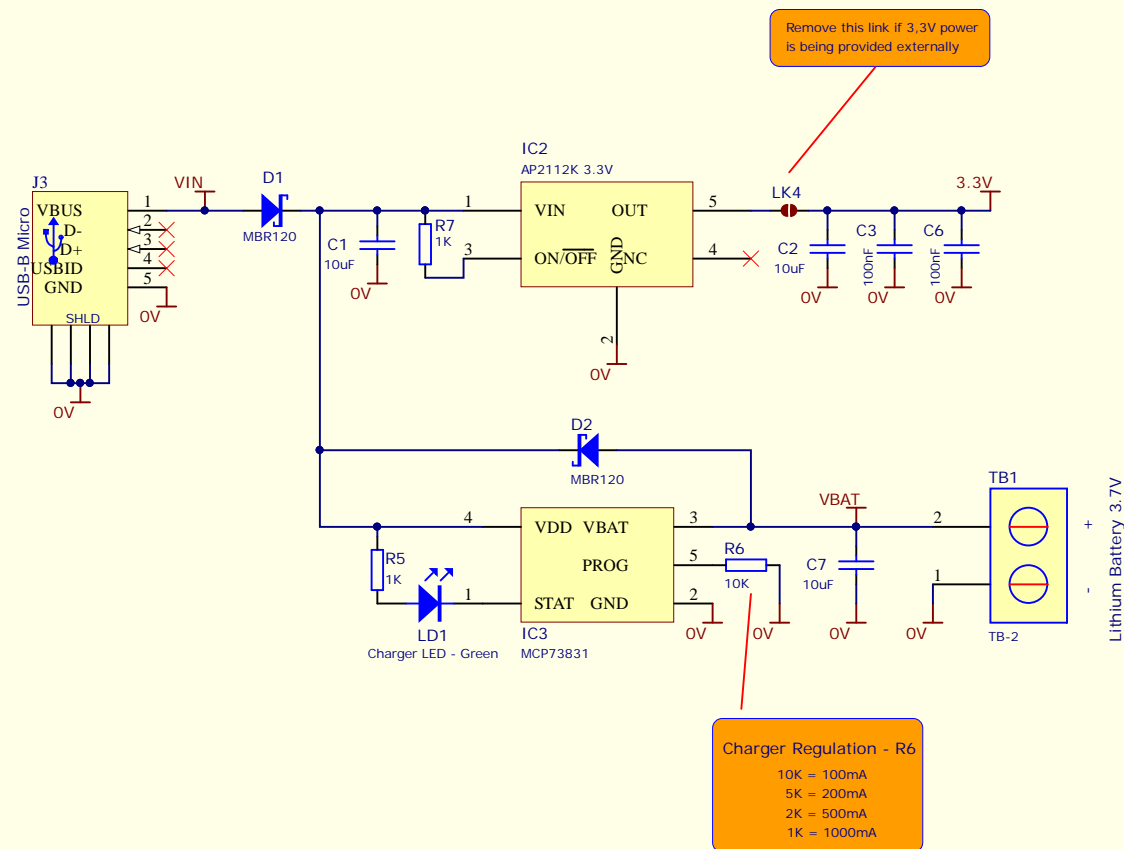
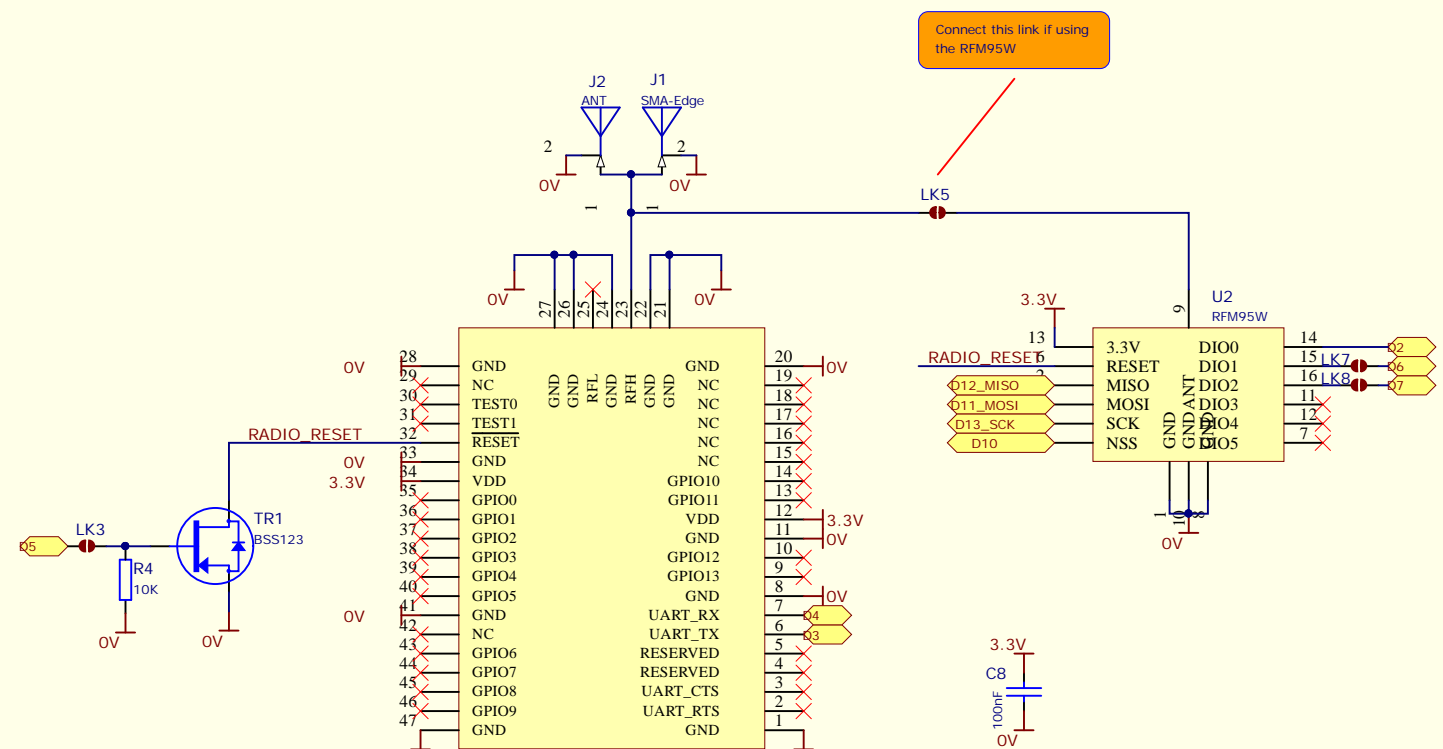


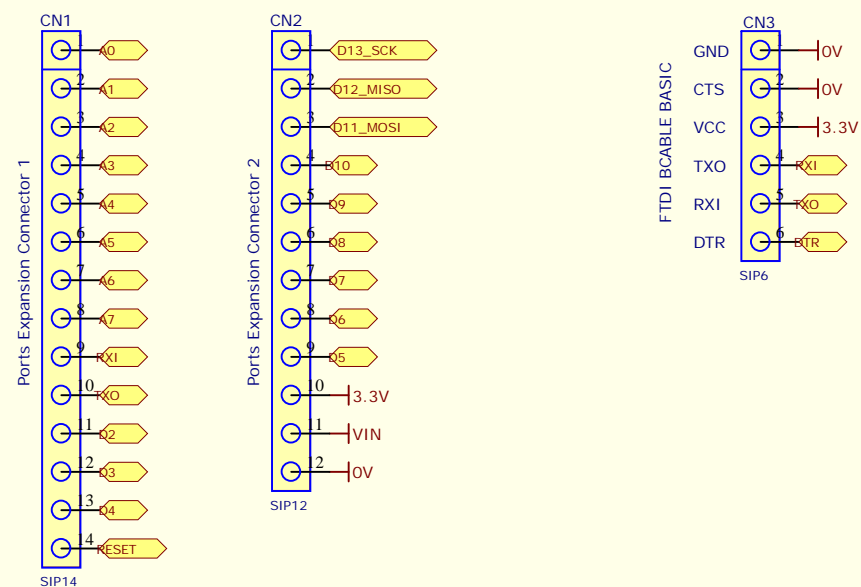
## Power



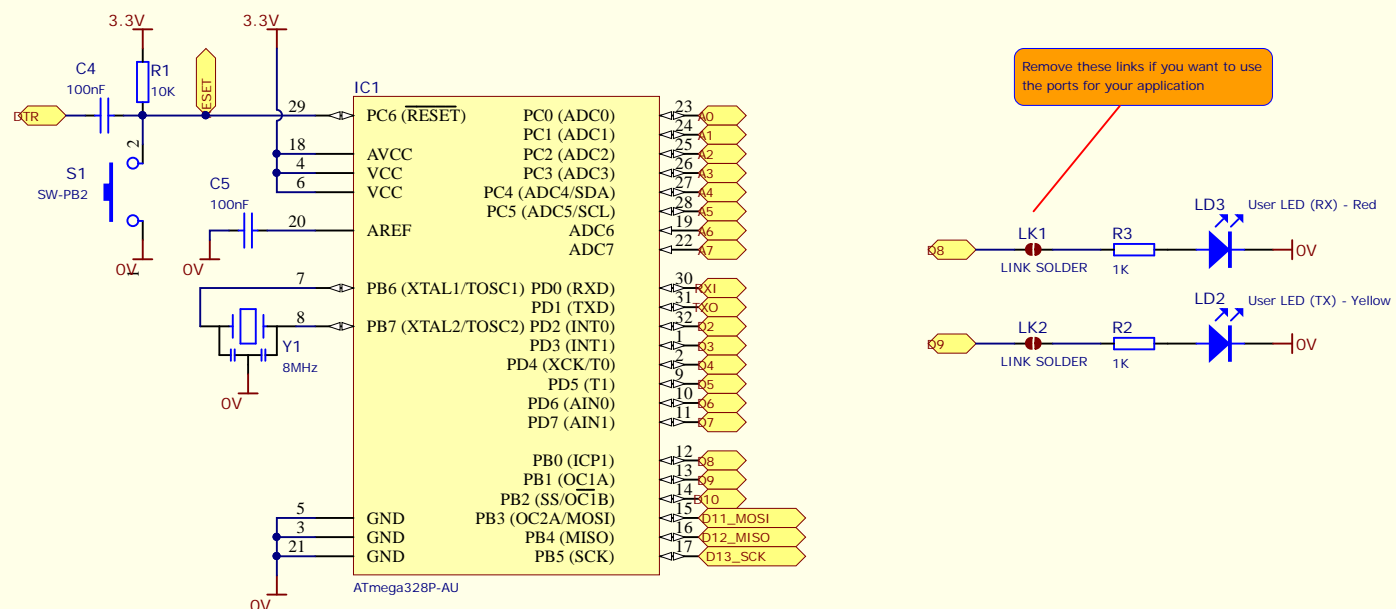
## Radio



## Expansion Connectors



## Application Processor



1

2

3

4

A

A

B

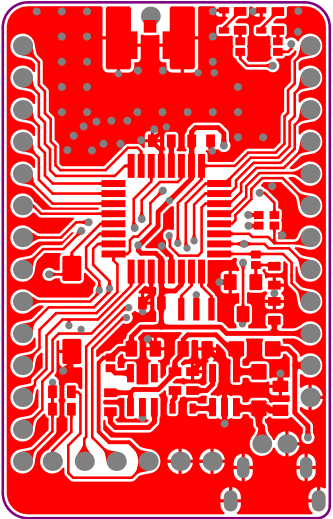
B

C

C

D

D



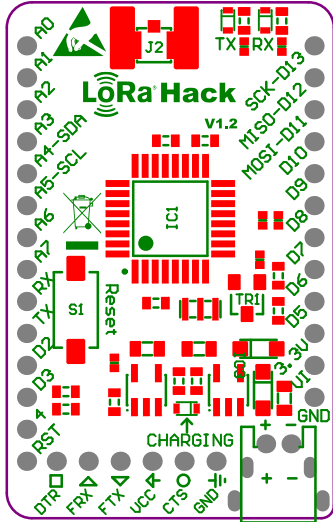
Project:	Lora Hack
Version:	1.2
Variant:	[No Variations]
PCB Designer:	Luis Goncalves
Date:	2017-06-16
File Name:	hacker_lora.PcbDoc

1

2

3

4



Project:	Lora Hack
Version:	1.2
Variant:	[No Variations]
PCB Designer:	Luis Goncalves
Date:	2017-06-16
File Name:	hacker_lora.PcbDoc

1

2

3

4

A

A

B

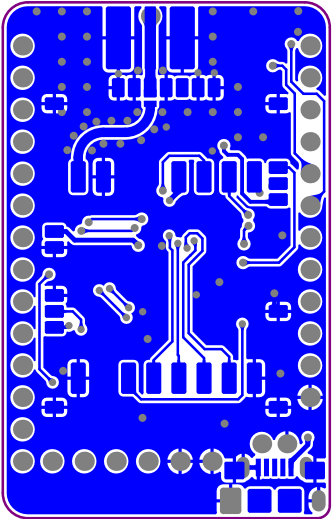
B

C

C

D

D



Project:	Lora Hack
Version:	1.2
Variant:	[No Variations]
PCB Designer:	Luis Goncalves
Date:	2017-06-16
File Name:	hacker_lora.PcbDoc

1

2

3

4

1

2

3

4

A

A

B

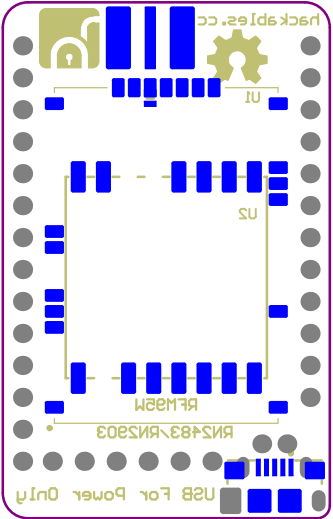
B

C

C

D

D



Project:	Lora Hack
Version:	1.2
Variant:	[No Variations]
PCB Designer:	Luis Goncalves
Date:	2017-06-16
File Name:	hacker_lora.PcbDoc

1

2

3

4