

Table 1 – 24-bit command frame encoding

Bytes/Bits									Device addressing		
Address byte								Instance byte	Opcode byte		
23	22	21	20	19	18	17	16	15...8	7...0	Device or instance or feature, see Table 2	
0	64 short addresses								1		
1	0	32 device group addresses							1		
1	1	1	1	1	1	1	0	1			
1	1	1	1	1	1	1	1	1			
1	1	0	16 special command spaces						1		
1	1	1	0	x	x	x	1	Command specific		Special command	
1	1	1	1	0	x	x	1	Reserved			
1	1	1	1	1	0	x	1	Reserved			

Table 2 – Instance byte in a command frame

Instance byte								Addressing					
15	14	13	12	11	10	09	08						
0	0	0	32 Instance numbers								Instance number		
1	0	0	32 Instance groups								Instance group		
1	1	0	32 Instance types								Instance type		
0	0	1	32 Instance numbers								Feature on instance number level		
1	0	1	32 Instance groups								Feature on instance group level		
0	1	1	32 Instance types								Feature on instance type level		
1	1	1	1	1	0	0	1	Feature broadcast		Reserved			
1	1	1	1	1	1	0	1	Feature on instance broadcast level					
1	1	1	1	1	1	1	1	Instance broadcast					
1	1	1	1	1	1	0	0	Feature on device level					
1	1	1	1	1	1	1	0	Device					
0	1	0	x	x	x	x	x	Reserved					
1	1	1	0	x	x	x	x	Reserved					
1	1	1	1	0	x	x	x	Reserved					
1	1	1	1	1	0	1	x	Reserved					
1	1	1	1	1	0	0	0	Reserved					

Table 3 – 24-bit event message frame encoding

Bits															Event scheme ^a / Source			
Event source information															Event information			
23	22	21	20	19	18	17	16	15	14	13	12	11	10	9...0				
0	64 short addresses				0	0	32 instance types				Event	1	Device					
0	64 short addresses				0	1	32 instance numbers						2 Device and instance					
1	0	32 device groups				0	0	32 instance types					3 Device group					
1	0	32 instance types				0	1	32 instance numbers					0 Instance					
1	1	32 instance groups				0	0	32 instance types					4 Instance group					
1	1	0	x	x	x	x	0	1	x	x	x	x	x	Reserved				
1	1	1	0	x	x	x	0	1	x	x	x	x	x	Reserved				
1	1	1	1	0	x	x	0	1	x	x	x	x	x	Device power cycle				
1	1	1	1	1	0	x	0	1	x	x	x	x	x	Device power cycle				
1	1	1	1	!	1	0	0	1	x	x	x	x	x	Device power cycle				
1	1	1	!	1	1	1	0	1	0	x	x	x	x	Device power cycle				
1	1	1	1	1	1	1	0	1	1	0	x	x	x	Device power cycle				
1	1	1	1	1	1	1	0	1	1	1	Short address and device group information, refer to 9.7.2.				Device power cycle			

^a Refer to 9.7.3 for further information on event schemes.