Yadunath R

yadunathr1@gmail.com|+918086528638|myportfolio.com

EDUCATION

TKMCE
BTECH IN EEE
May 2018 | Kerala
Cum. GPA: 9.34 / 10.0
University Rank 6

LEARNINGS

COURSERA
FPGA Design for Embedded Systems
Real-Time Embedded Systems

SKILLS

PROGRAMMING
C • C++ • Assembly
Matlab • Simulink
Multisim
Familiar:
Python•Linux

PROTOCOL
I2C• UART• CAN• SPI• RS232.• RF

CONCEPTS
Embedded programming
Software testing
Knowledge on power converters
Electronic circuits

EXPERIENCE

BOSCH | SOFTWARE ENGINEER
Sept 2022 - Present | Bangalore,karnataka

- Played a key role in the software development of radar systems utilizing the Infineon TC39 microcontroller
- Specialized in diagnostic and failure management for esteemed clients, including Mercedes-Benz, Honda, Renault, and Nissan .
- Played a key role in the development and optimization of CAN-based communication protocols for Automotive Domains.
- Collaborated with hardware and software teams to ensure compliance with CAN standards and protocols.
- Contributed to the design and architecture of ADAS features, focusing on ACC,CC etc.
- Developed an application for key generation for Type 1 security thereby considerably reducing the time for the process.

LARSEN AND TOUBRO LIMITED | DESIGN ENGINEER
Sept 2018 – Jan 2020 | Mysore,Karnataka

- Performed software development of there phase energy meters and the LTCT meters using RL78/I1C(Renesas) 16-bit microcontroller.
- Hands on experience with various communication protocol such as I2C,Sampark, IrDa, DLMS,RS232.
- Developed application interface for IrDa communication and CRC error check and reducing considerable amount of time in the process.
- Developed Bi-directional meters.
- Optimised the Battery mode operation of the meters and there by reducing the battery consumption and battery size ,leading to reduction in production cost upto 5 percent.

PROJECTS

APPLICATIONS

- Developed application for IrDA Communication,CRC calculations,Security key generation

REACTIVE POWER COMPENSATION USING INFINITE LEVEL INVERTER

- A new VSI topology which increase the voltage levels and provide better DC link utilisation

RECOGNITIONS

2018	University rank 6	Graduated from TKMCE
2020	Innovation Award	Larsen and Toubro Innovation Award
2021	Gate rank 2000	Gate Exam