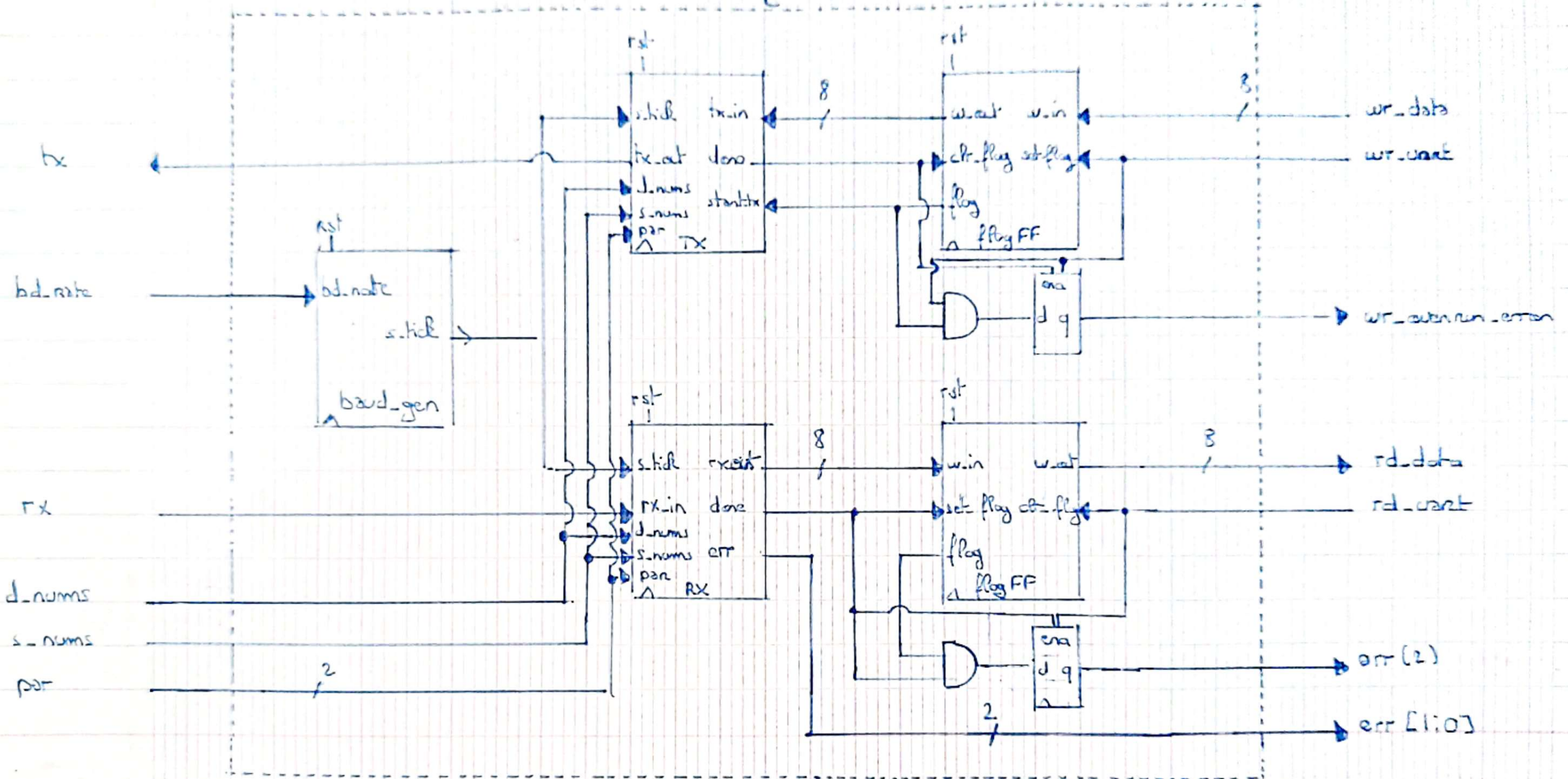


d_nums {
 '1': 8 data bits
 '0': 7 " " "

s_nums {
 '1': 2 stop bits
 '0': 1 " " "

par {
 '00' or '11': no parity scheme
 '01': odd parity scheme
 '10': even " " "

bd_rate {
 '000': 1200 bauds
 '01': 2400 "
 '10': 4800 "
 '11': 9600 "

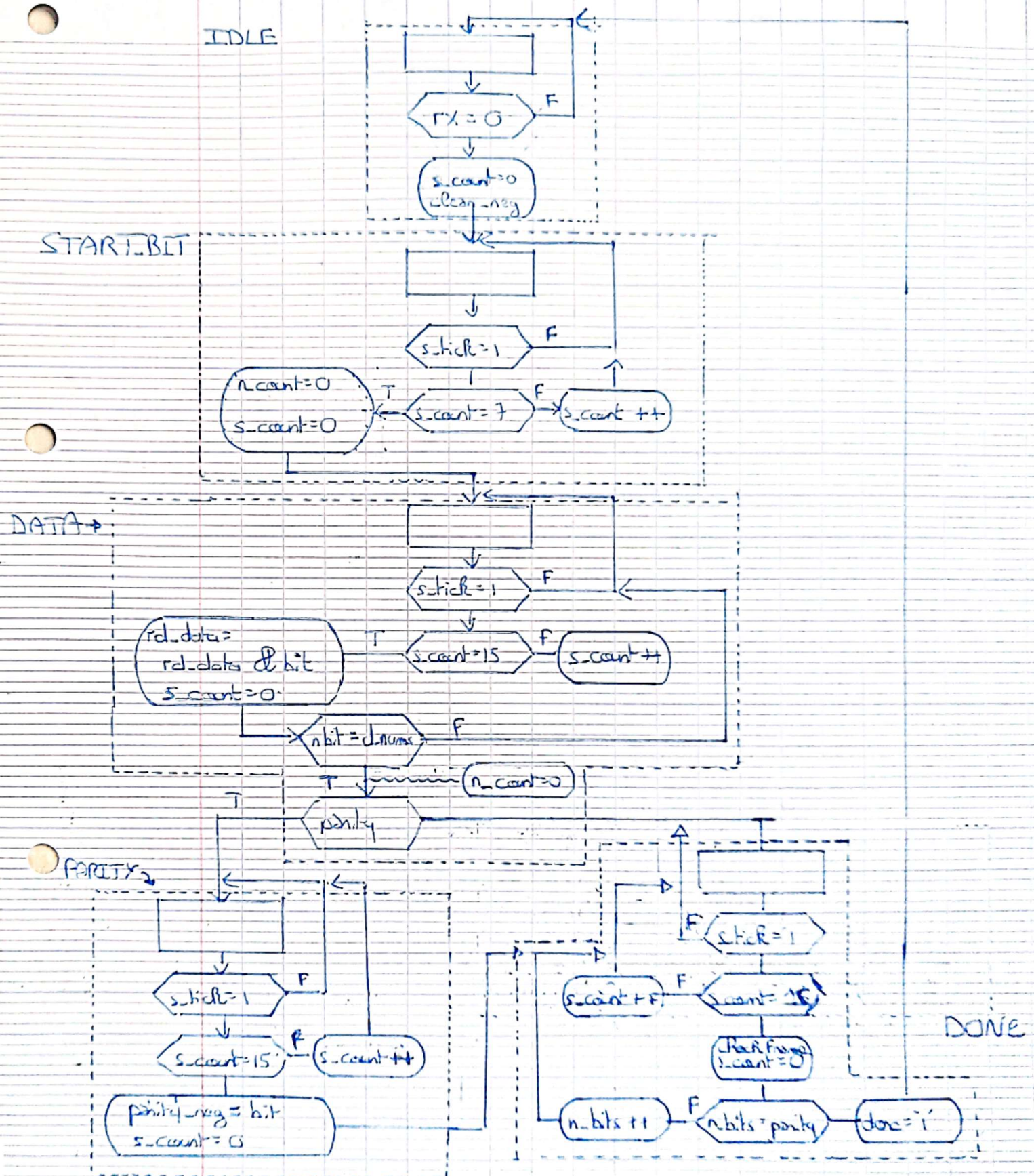


err(2): overrun error

err(1): parity error

err(0): frame error

DYNAMIC UART



DYNAMIC PART FSM D.