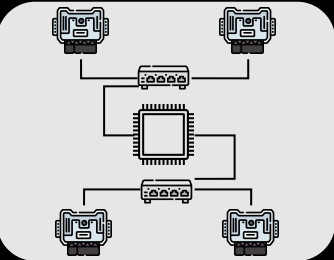
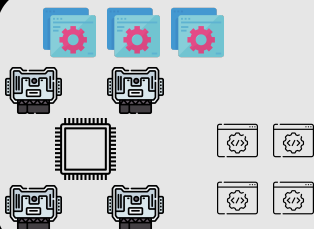


## E/E System Integrator Inputs

Properties, Requirements, Topology, and Optimization goals regarding Mapping, Routing, Scheduling, and Safety



## System Metamodel

### MIP Constraint Set

Mapping

Message Routing

Time-triggered Scheduling

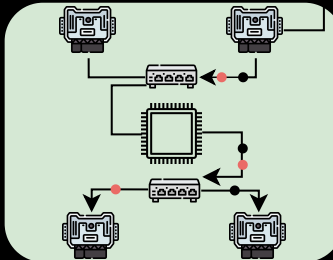
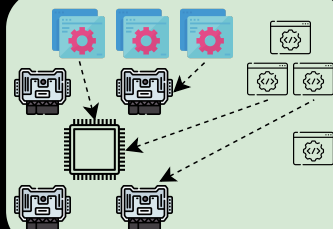
Safety Requirements

Optimization Objectives

Solving & Optimization

## E/E Designer Output

Automated Mapping and Message Routing Creation, Time-triggered Schedules Calculation, Fulfillment of Safety Requirements, Optimization



Used by →

Executing - - - →

Infeasible Solution



Design Error Identifier