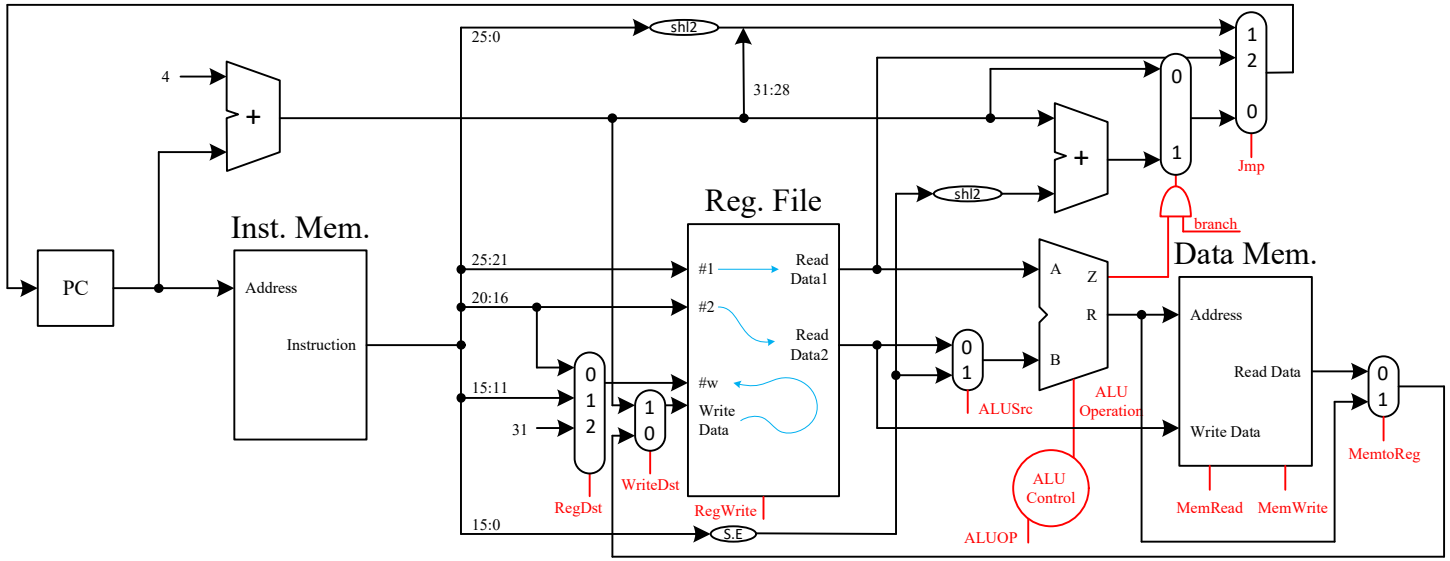
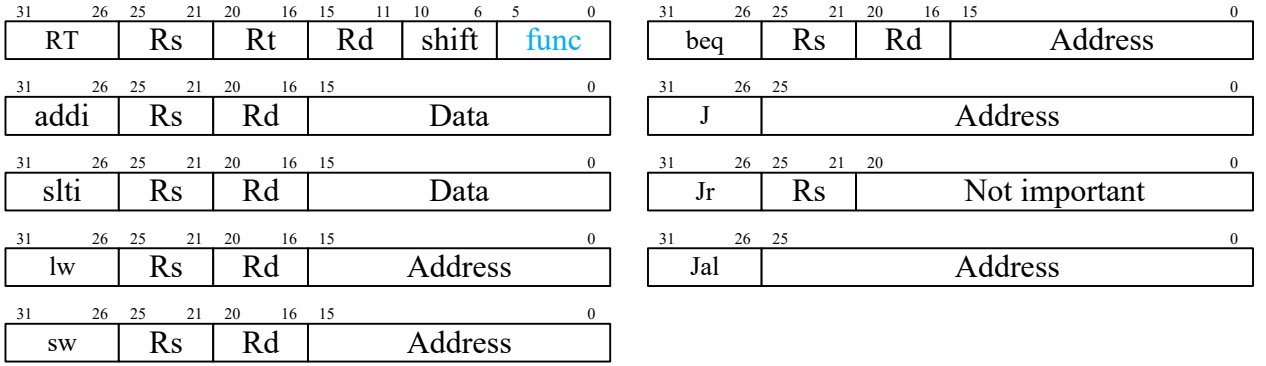
ALUOP	func	ALUOperation
00	Not Imp.	010
01	Not Imp.	011
10	and	000
10	or	001
10	add	010
10	sub	011
10	slt	111
11	Not Imp.	111





OPC		RegDst	WriteDst	RegWrite	ALUSrc	ALUOP	Branch	Jmp	MemRead	MemWrite	MemtoReg
000000	RT	1	0	1	0	10	0	0	0	0	1
000001	addi	0	0	1	1	00	0	0	0	0	1
000010	slti	0	0	1	1	11	0	0	0	0	1
000011	lw	0	0	1	1	00	0	0	1	0	0
000100	sw	x	x	0	1	00	0	0	0	1	x
000101	beq	x	x	0	0	01	1	0	0	0	x
000110	J	x	x	0	x	x	x	1	0	0	x
000111	Jr	x	x	0	x	x	x	2	0	0	x
001000	Jal	2	1	1	x	x	x	1	0	0	x