



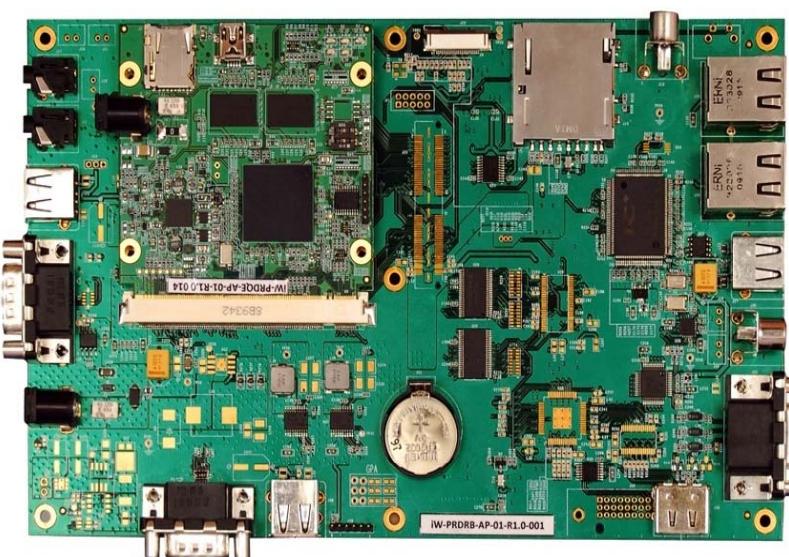
Module-2

RF PCB Design

Basics Concepts & Techniques

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FAST-NU, Islamabad

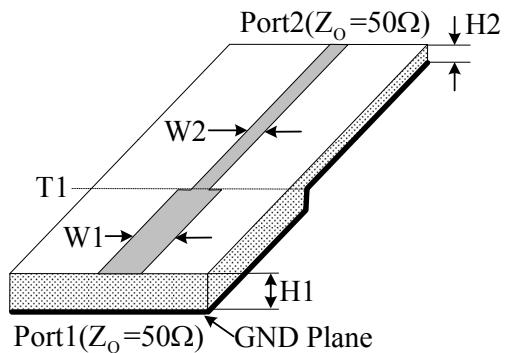
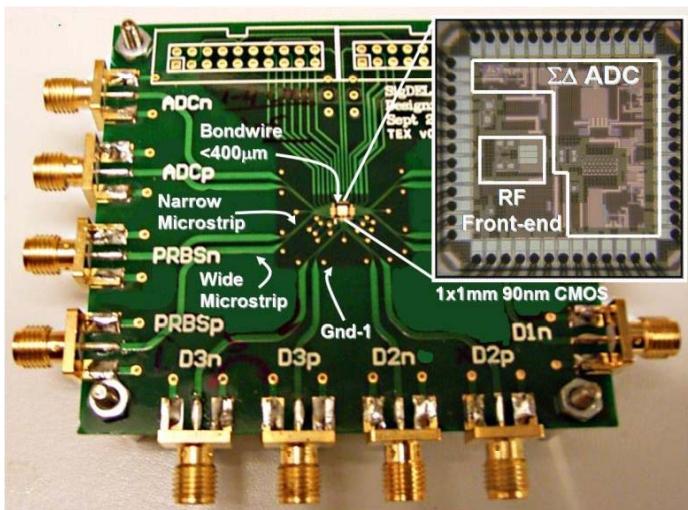
Objective of RF PCB Design Module



- Approach is to use the RF tool like ADS to implement the layout in Altium.
- Advantage: Productivity and accuracy at same time

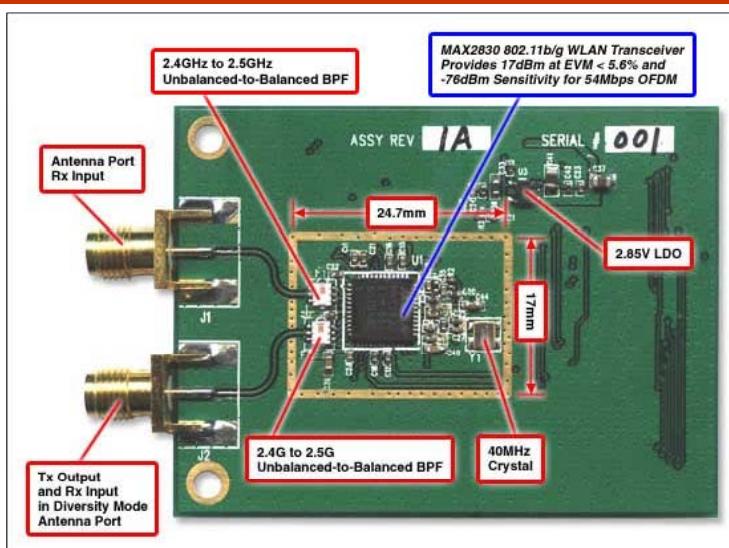
- Design of Digital PCB with RF commutation Chips modules on it!!
- Tools
 - Protel or Altium
 - Used together with ADS

Objective of Module-2



- 90nm Wideband RF Frontend Test Bed (1-6GHz)
 - 90nm Chip and 4-layer FR4 PCB Designed By Rashad
- Protel was used to design the board and ADS to design the transmission lines

Objective of RF PCB Design Module



- Complete RF PCB on High Speed Substrates using ADS tool suite
- Need strong theory and background of Electromagnetic
 - Impedance matching, TDR, Smith chart, S-Parameters, Transmission Lines, Transceiver Architecture , LNA, Mixer Design, PLL, DLL, Pas etc



Outline of Today's Lecture

- Objective of this three day module
 - RF PCB and RF as a part of High Speed Digital PCB
 - Tools for both applications
- Difference in RF and Digital PCB
 - Frequency Range
 - Sine vs. Square (Trapezoidal)
 - Narrow Band vs Wide Band
 - Termination types & 50Ω matching
 - Impedance Matching Criteria
 - PCB Material, Layer Stack

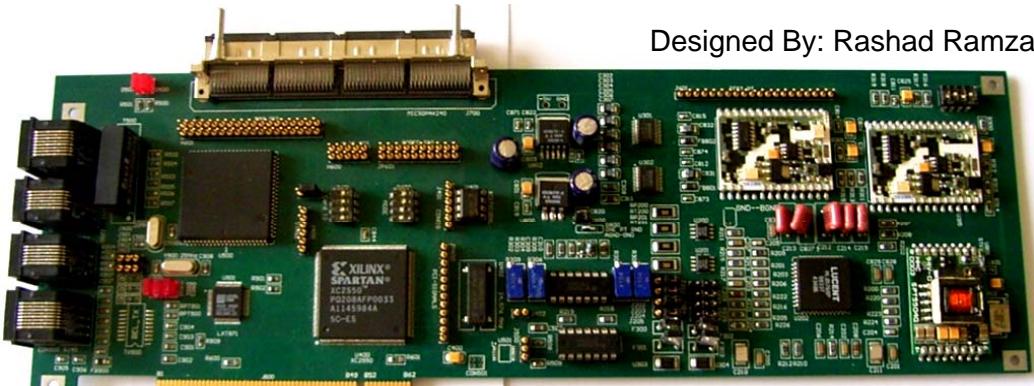


Outline of Today's Lecture

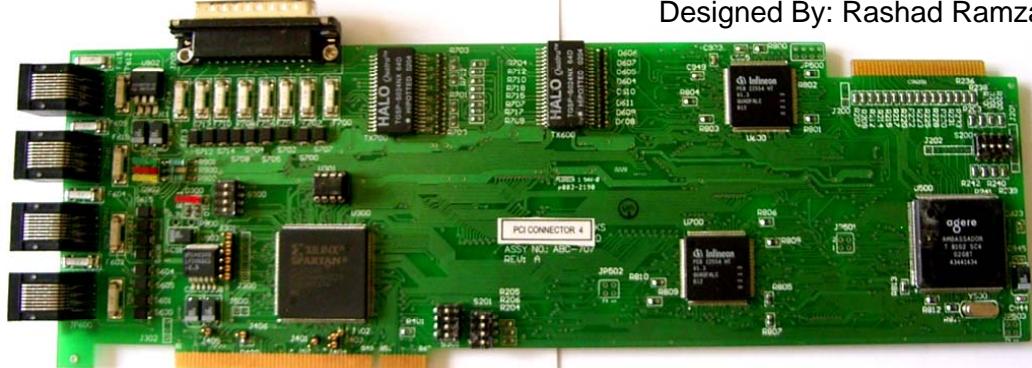
- Transmission Lines in RF PCBs and Digital PCBs
 - Basic Transmission Modes in PCB Traces
- Impedance Matching
 - Smith Chart A Necessary Tool
- S-Parameters

Module-1: Analog and Digital PCBs

Designed By: Rashad Ramzan



Designed By: Rashad Ramzan



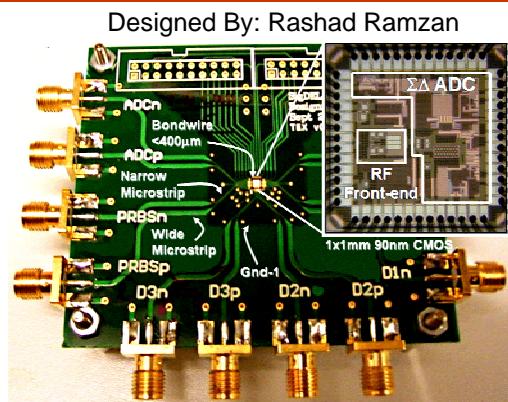
RF PCB Design:Lecture-1

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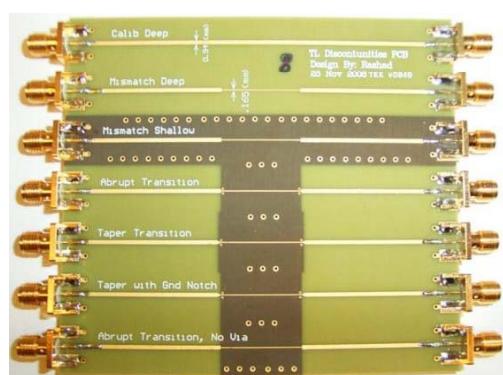
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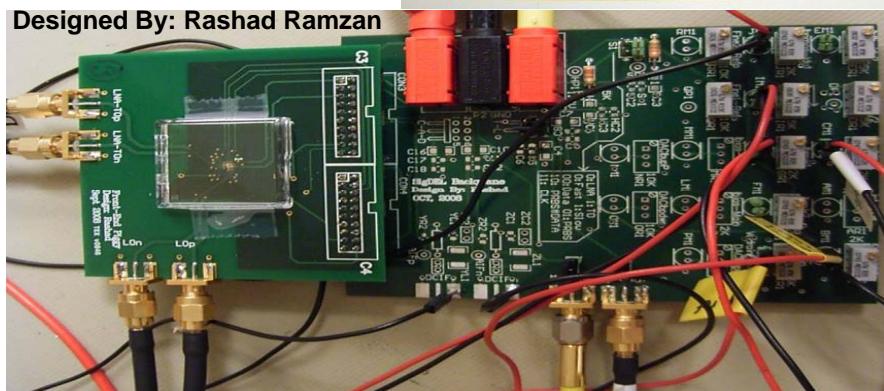
Module-2: RF PCBs



Designed By: Rashad Ramzan



Designed By: Rashad Ramzan

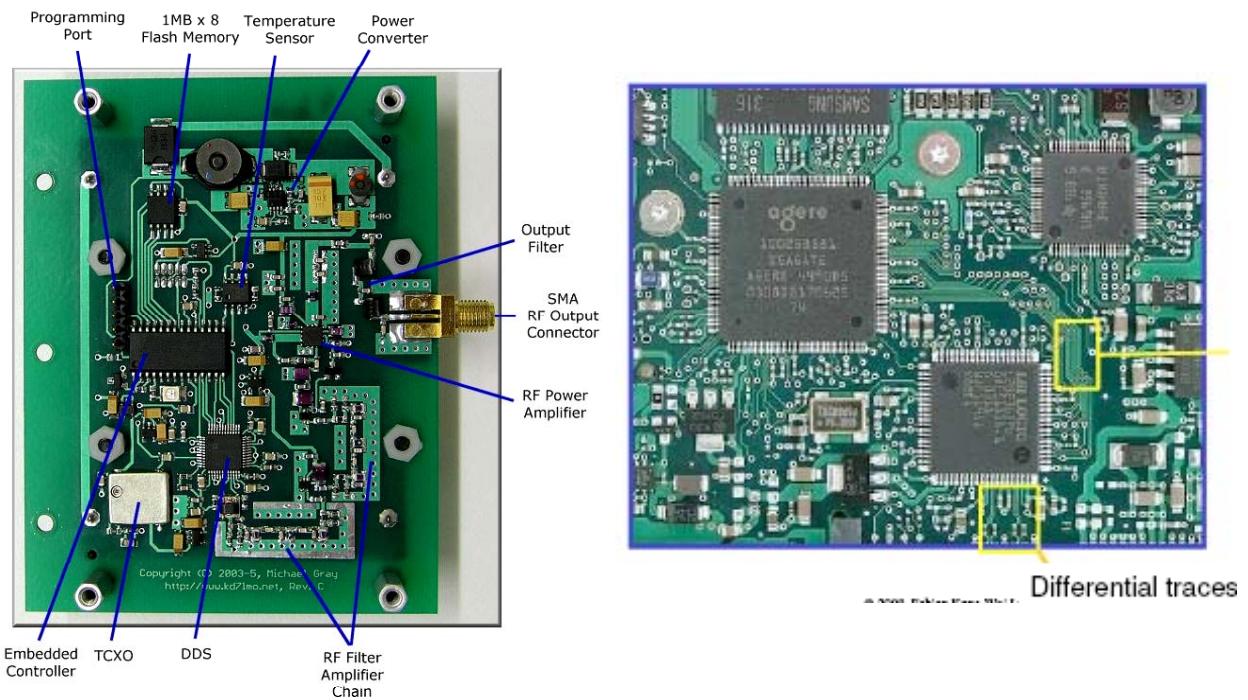


RF PCB Design:Lecture-1

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Digital & RF PCBs: Are they Same?

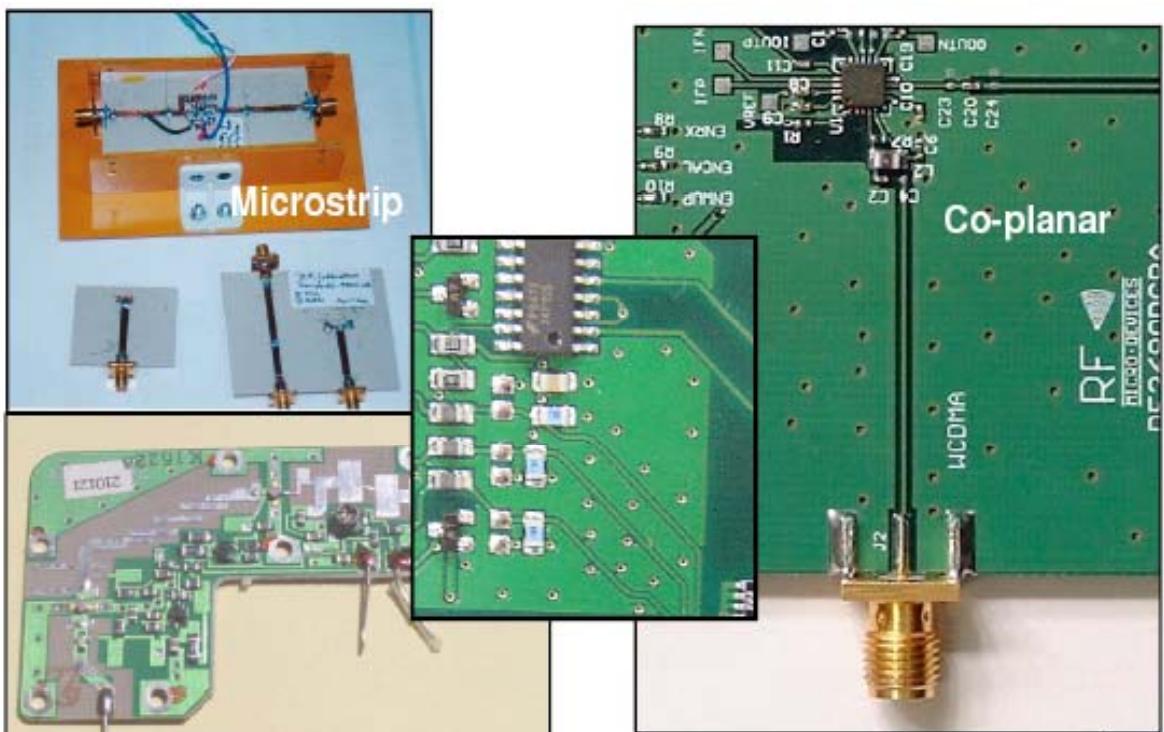


RF PCB Design:Lecture-1

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Digital & RF PCBs: Are they Same?



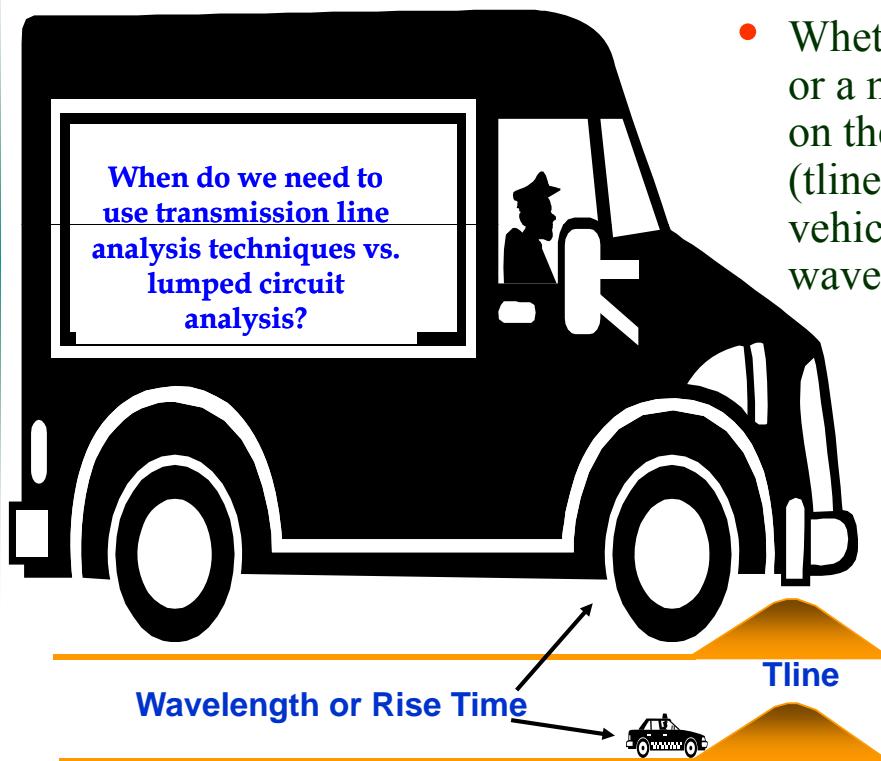
RF PCB Design:Lecture-1

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When a Trace is Transmission Line?



RF PCB Design:Lecture-1

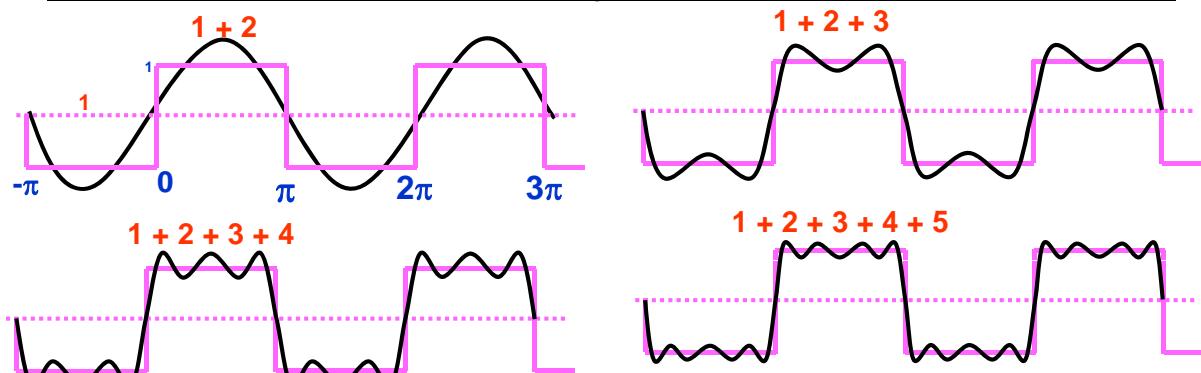
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RF vs. Digital PCB: Sine vs. Square

Digital signals are composed of an infinite number of sinusoidal functions – the Fourier series

The Fourier series is shown in its progression to approximate a square wave:



Square wave: $Y = 0 \text{ for } -\pi < x < 0 \text{ and } Y=1 \text{ for } 0 < x < \pi$

$$Y = 1/2 + 2/\pi(\sin x + \sin 3x/3 + \sin 5x/5 + \sin 7x/7 \dots + \sin(2m+1)x/(2m+1) + \dots)$$

1 2 3 4 5

May do with sum of cosines too.



RF vs. Digital PCB: Sine vs. Square

Where does that famous equation $F = \frac{0.35}{Tr}$ come from?

- It can be derived from the response of a step function into a filter with time constant tau

$$V = V_{input} (1 - e^{-t/\tau})$$

- Setting $V=0.1V_{input}$ and $V=0.9V_{input}$ allows the calculation of the 10-90% risetime in terms of the time constant

$$t_{10-90\%} = t_{90\%} - t_{10\%} = 2.3\tau - 0.105\tau = 2.195\tau$$

- The frequency response of a 1 pole network is

$$F_{3dB} = \frac{1}{2\pi\tau} \rightarrow \tau = \frac{1}{2\pi F_{3dB}}$$

- Substituting into the step response yields

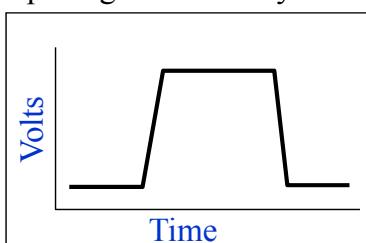
$$t_{10-90\%} = \frac{1.09}{\pi F_{3dB}} = \frac{0.35}{F_{3dB}}$$

$$t_{10-90\%} = \frac{0.35}{F_{3dB}}$$



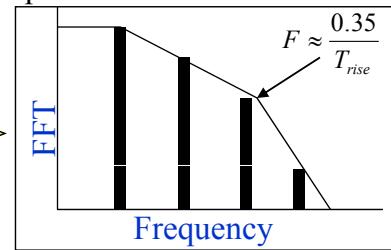
RF vs. Digital PCB: Sine vs. Square

Input signal into lossy T-line



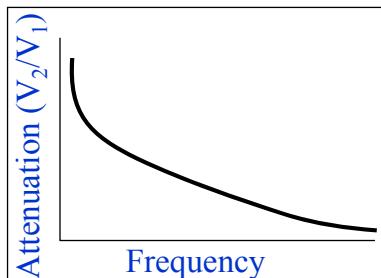
FT

Spectral content of waveform



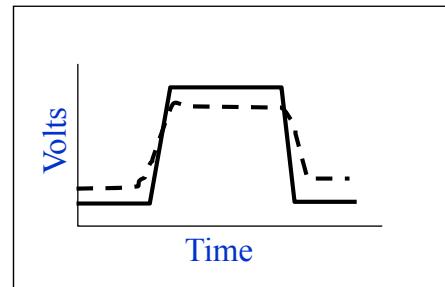
Time domain waveform with frequency dependent losses

Loss characteristics if T-line



Multiply

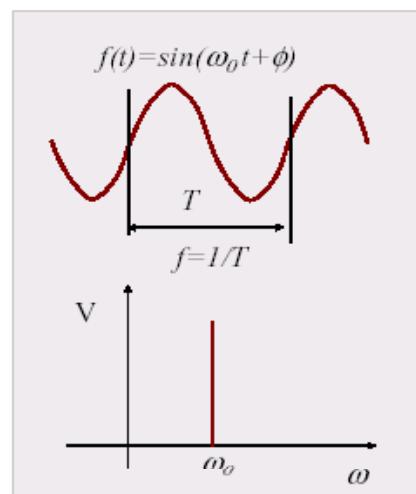
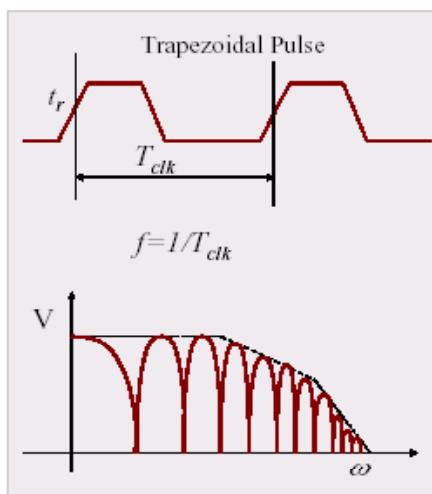
Inverse
FFT





RF vs. Digital PCB: Sine vs. Square

Digital
PCB
Tracks
Carriers
trapezoidal
waves



$$f_{3dB} = \frac{0.35}{t_{10-90\%}} = \frac{0.35}{t_r}$$

$$f_{BW} = \frac{4 \times 0.35}{t_r} = \frac{1.4}{t_r}$$

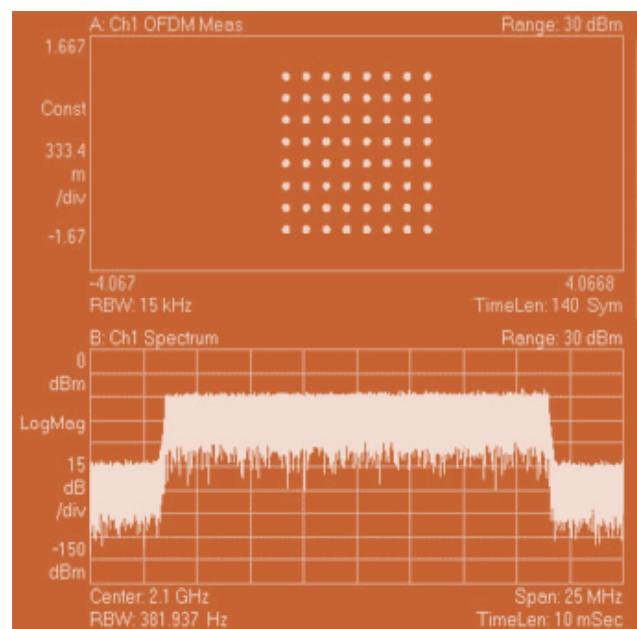
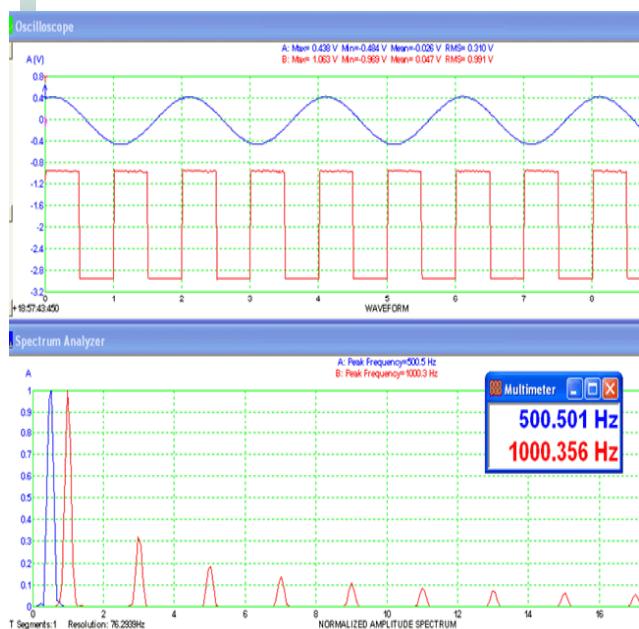
$$f_{BW} = f_o + \Delta f \quad \text{or}$$

$$\omega_{BW} = \omega_o + \Delta \omega$$

$\Delta \omega$ is BW in case of modulation



Real Sine vs. Square





PCB Material: Dielectric Constant

- Is measure of how much charge two conductors can hold at a certain fixed voltage. Low Dk hold less charge and high Dk more charge. Its also measure of the ratio of velocity in conductor and free space.
 - High Dk → Small width for same Zo
 - High Dk → Large propagation delay

$$Z_o = \left(\frac{79}{\sqrt{\epsilon_r + 1.41}} \right) \ln \left(\frac{5.98 H}{0.8 W + T} \right) \Omega \quad \text{Valid for } 5 < W < 15 \text{ mils}$$

$$C_o = \frac{0.67(\epsilon_r + 1.41)}{\ln \left(\frac{5.98 H}{0.8 W + T} \right)} \text{ pf/in.}$$

$$t_{pd} = 1.017 \sqrt{0.475 \epsilon_r + 0.67} \quad (\text{ns/ft})$$



PCB Material: Loss Tangent

$$\alpha = 2.3 f \tan(\delta) \cdot \sqrt{\epsilon_{eff}}$$

Where : α - Attenuation in dB / inch.

f - Frequency in GHz

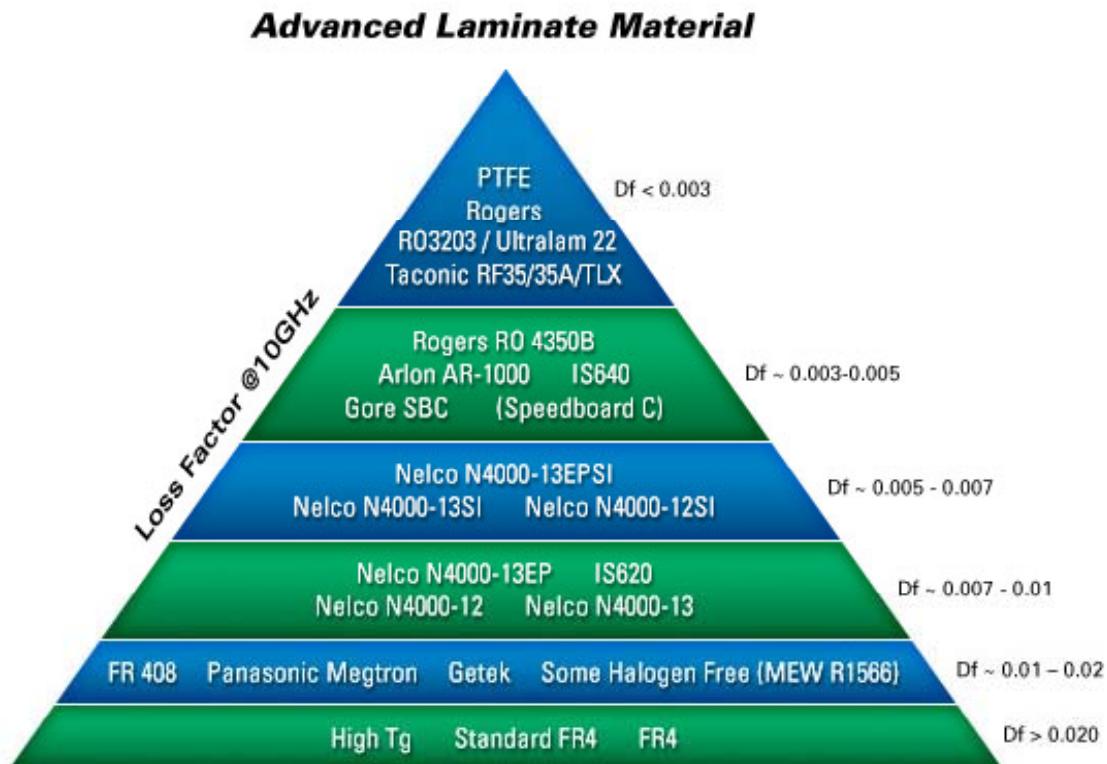
$\tan(\delta)$ - Loss tangent of material

ϵ_{eff} - Effective relative Er of material

- Is measure of how much electromagnetic energy is absorbed by dielectric material. Like microwave oven, things that heat up quickly has high loss tangent. Glass and ceramic are low Df materials.
 - Loss is frequency dependent, increases with frequency.
 - Low loss improves signal integrity--- Very Important for RF applications



RF vs. Digital: PCB Materials



RF vs. Digital: PCB Materials

Materials intended for digital applications

Material	Er (* at 1.0 MHz)	Thickness tolerance	Copper style	Multilayer compatible	Loss tangent
FR4	3.9 – 4.6*	+/- 1-2 mils	ED only	Yes	.02 - .03
FR408	3.4 – 4.1*	+/- 1-2 mils	ED only	Yes	.01 - .015
BT Epoxy	3.9 – 4.6*	+/- 1-2 mils	ED only	Yes	.015 - .02
Cyanate Ester	3.5 – 3.9*	+/- 1-2 mils	ED only	Yes	.009
Polyimide	4.0 – 4.5*	+/- 1-2 mils	ED only	Yes	.01
GETEK	3.5 – 4.2*	+/- 1-2 mils	ED only	Yes	.012
Nelco 4000-13	3.7 (1GHz)	+/- 1 mil	ED only	Yes	.01
Nelco 4000-13SI	3.5 (1GHz)	+/- 1 mil	ED only	Yes	.009
Nelco 6000	3.5 (1GHz)	+/- 1 mil	ED only	Yes	.008
Nelco 6000SI	3.2 (1GHz)	+/- 1 mil	ED only	Yes	.005
Speedboard N	3.0 *	+/- 1 mil	Prepreg	Yes	.02
Speedboard C	2.6 – 2.7*	+/- 1 mil	Prepreg	Yes	.004
Arlon 25 / Rogers 4003	3.4 (10GHz)	+/- 1 mil	ED only	Yes	.0027

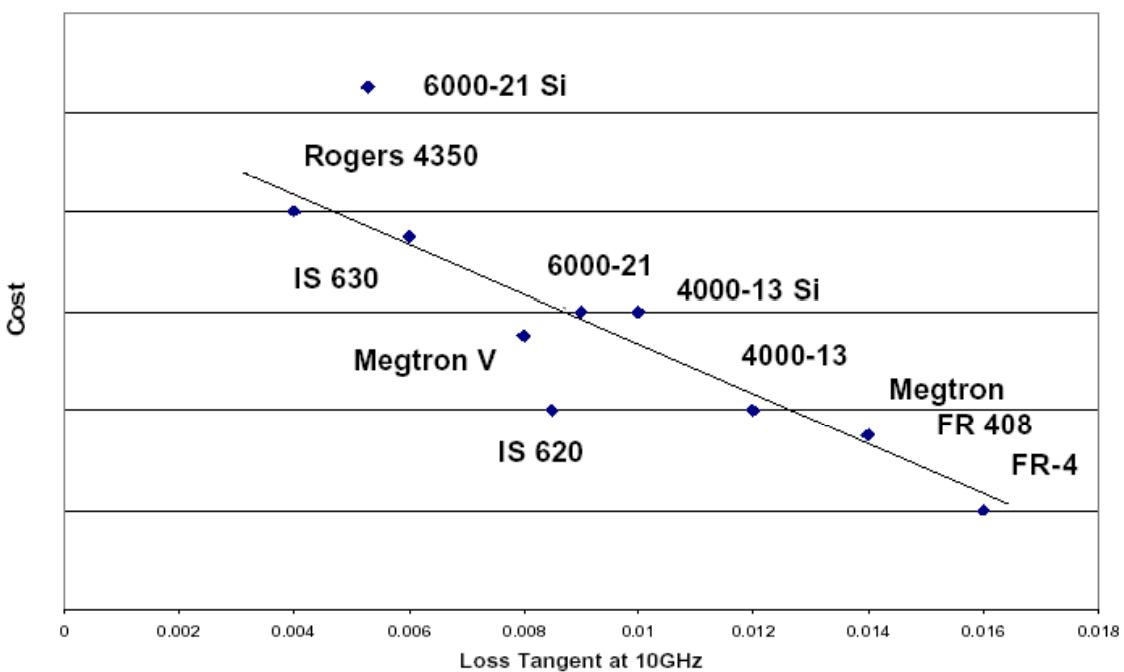


RF vs. Digital: PCB Materials

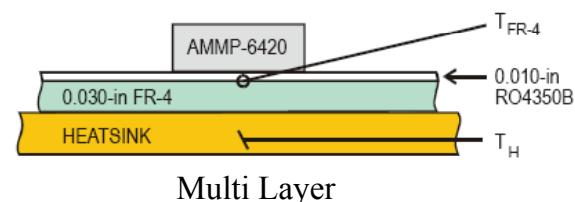
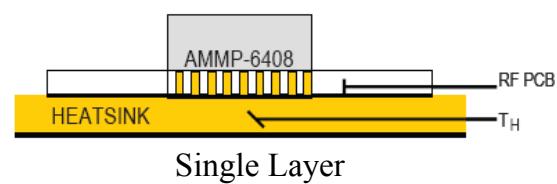
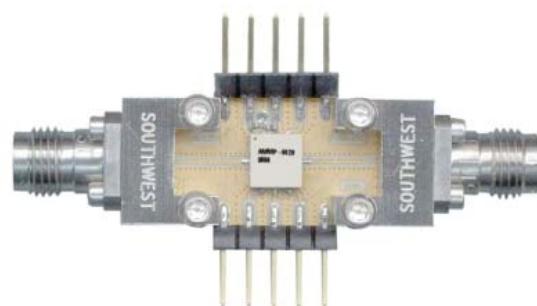
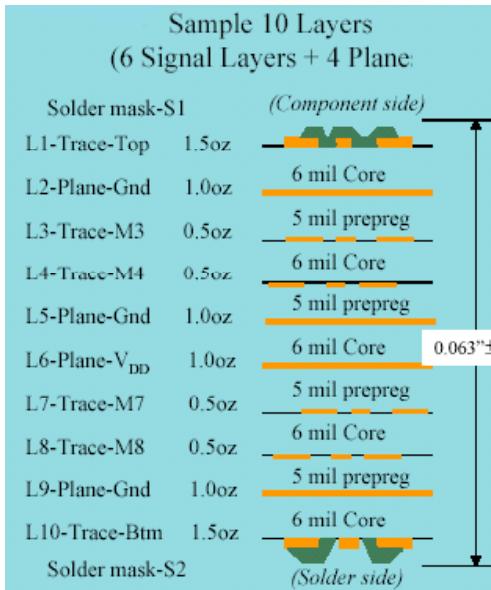
Materials for analog applications					
Material	Er (10.0 GHz)	Thickness tolerance	Copper style	Multilayer compatible	Loss tangent
Rogers Ultralam 2000	2.4 – 2.6	+/- .5 mil	ED / rolled	No	.0019
Rogers 5870	2.3	+/- .5 mil	ED / rolled	No	.0012
Rogers 5880	2.2	+/- .5 mil	ED / rolled	No	.0009
Rogers 6002	2.94	+/- .5 mil	ED / rolled	Yes	.0012
Rogers 3003	3.0	+/- 1 mil	ED / rolled	Yes	.0013
Rogers 6006	6.15	+/- .5 mil	ED / rolled	No	.0019
Rogers 6010	10.2	+/- .5 mil	ED / rolled	No	.0023
Rogers 3006	6.15	+/- 1 mil	ED / rolled	Yes	.0025
Rogers 3010	10.2	+/- 1 mil	ED / rolled	Yes	.0035



RF vs. Digital: PCB Materials



RF vs. Digital: Layer Stack



RF vs. Digital: 50Ω matching



- **Digital PCBs**

- Buses and CLK signals are laid out on controlled impedance traces of preferably **60 Ω** and above with no load matching.
 - Reason: although reflections occurs, still we want to use the **voltage divider rule** and control the reflection by **source series** terminations
 - We can make the digital IC $Z_{in} \geq$ (few) **KΩ** at frequencies a high as ~800MHz

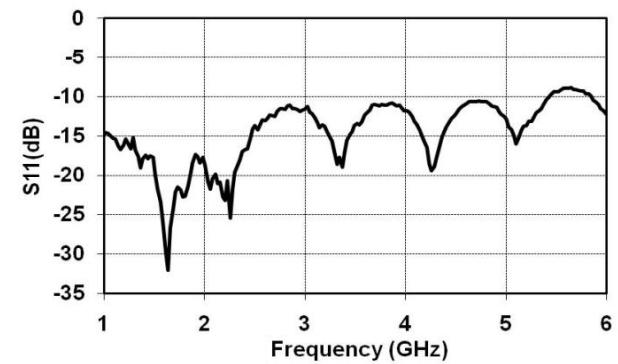
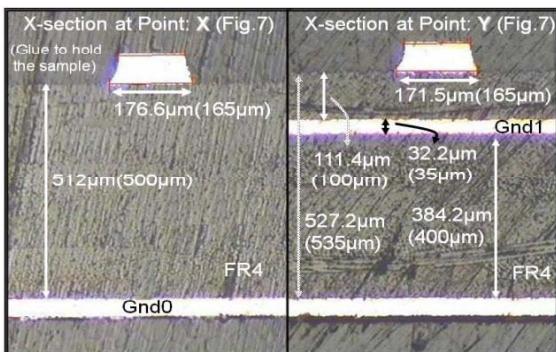
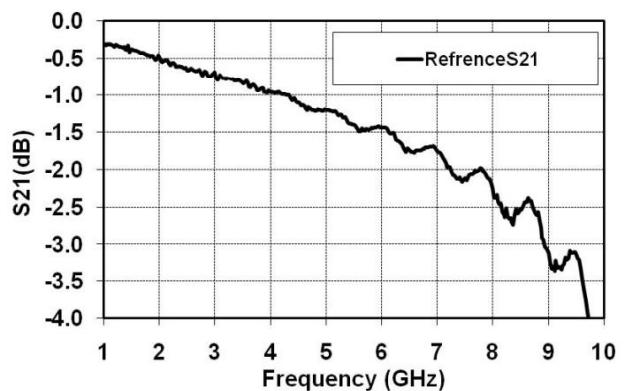
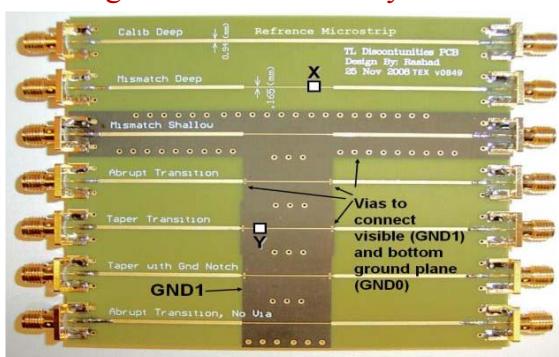
- **RF PCBs**

- Its always **50Ω** impedance matching
 - We can not make the RF IC $Z_{in} \geq$ (few)**KΩ** at GHz frequencies
 - **3dB** (Half RF Power) loss in every connection



Microstrips on FR4 PCB

Designed and measured by: Rashad

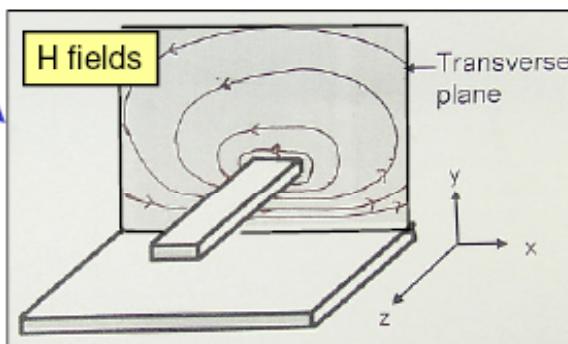
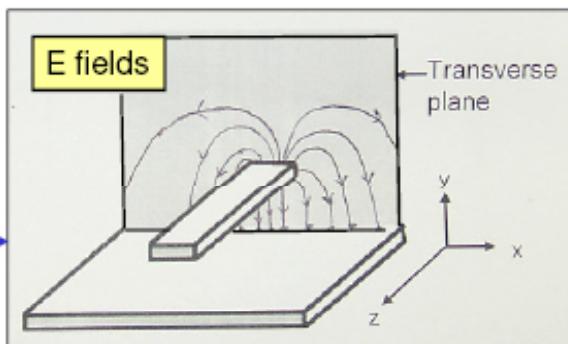
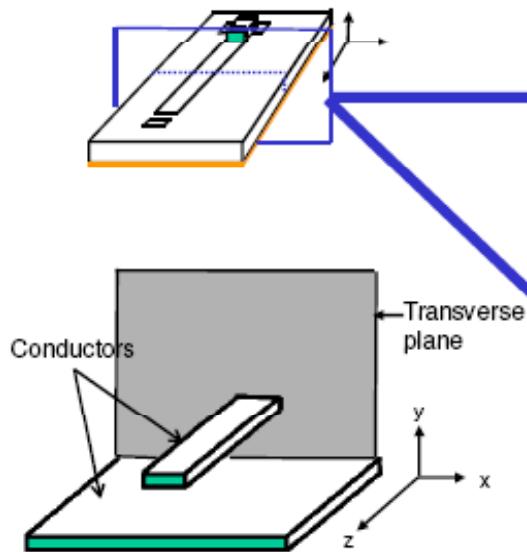


Propagation in Transmission Lines



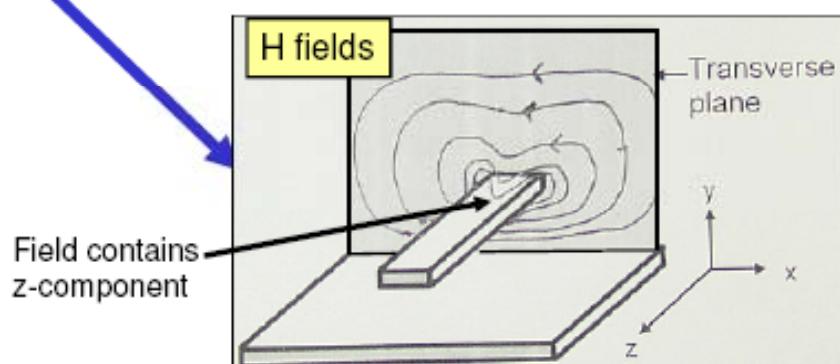
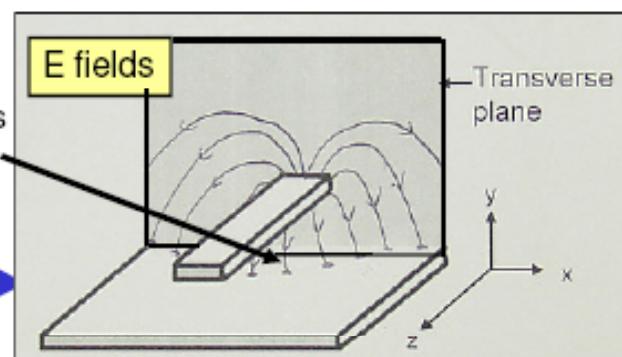
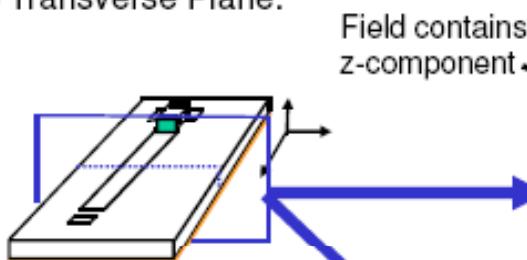
Transverse E & H Field Pattern

Field patterns that lie in the Transverse Plane.



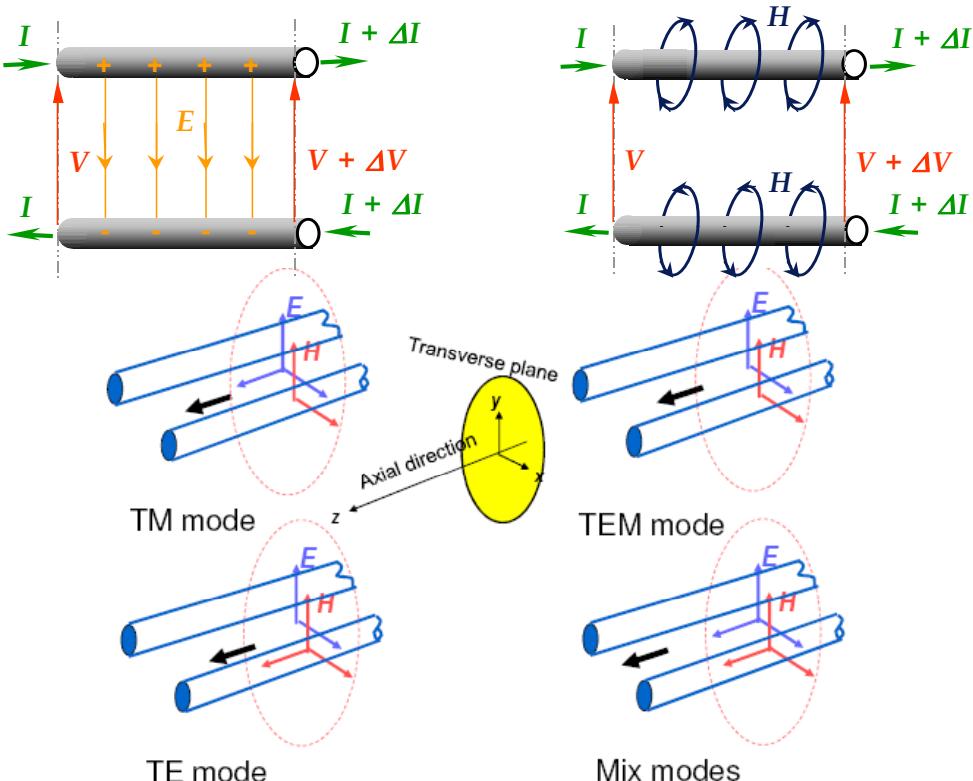
Non-transverse E & H Field Pattern

Field patterns that does not lie in the Transverse Plane.





Possible Propagation Modes



Why TEM is desirable?

- **Cutoff frequency is zero** – Therefore wideband transmission is possible like co-axial cables
- No dispersion, signals of different frequencies travel at the same speed, no distortion of signals
- Sometime we deliberately want to have a cutoff frequency so that a microwave filter can be designed

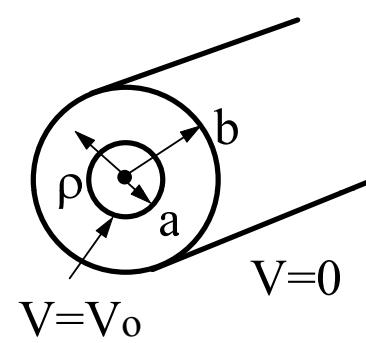


Coaxial Cables -TEM

- TEM exists in co-axial cable
- Higher-order modes exist in coaxial line but is usually suppressed
- Dimension of the coaxial line is controlled so that these higher-order modes are cutoff
- Dominate higher-order mode TE_{11} is mode, the **cutoff wavenumber (k_c)** can only be obtained by solving a transcendental equation, approximate $k_c = 2/(a+b)$ is often used in practice

$$H_y = \frac{-j}{k_c^2} (\omega \epsilon \frac{\partial E_z}{\partial x} + \beta \frac{\partial H_z}{\partial y})$$

$$E_x = \frac{-j}{k_c^2} (\beta \frac{\partial E_z}{\partial x} + \omega \mu \frac{\partial H_z}{\partial y})$$



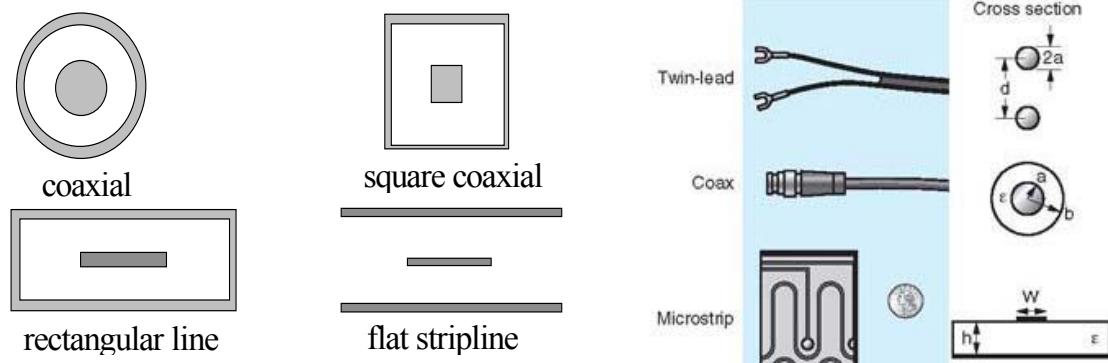
$$Z_{\text{TEM}} = \frac{E_x}{H_y} = \frac{\omega \mu}{\beta} = \sqrt{\frac{\mu}{\epsilon}} = \eta$$

$$Z_0 = \frac{V_0}{I_a} = \frac{\eta \ln(b/a)}{2\pi}$$

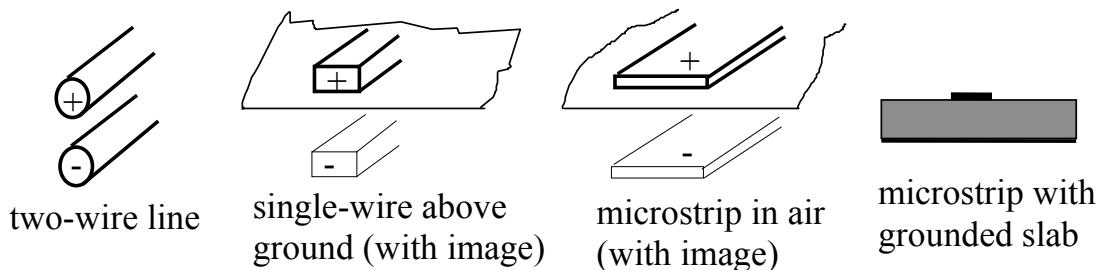


Microstrip and Striplines

Strip line was developed from the square coaxial



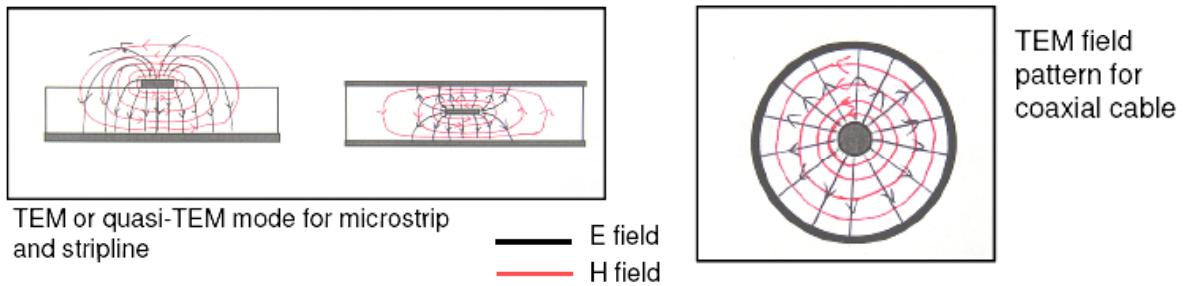
Evolution of microstrip



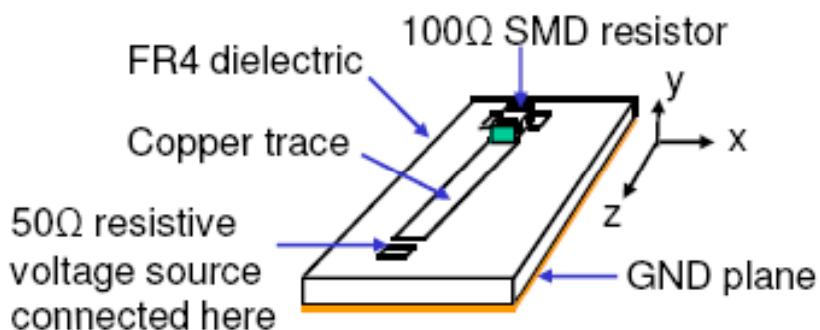


Modes in Microstrip

- A microstrip line suspended in air can support TEM wave
- A PCB microstrip does not support TEM wave
- A PCB microstrip fields constitute a hybrid TM-TE wave



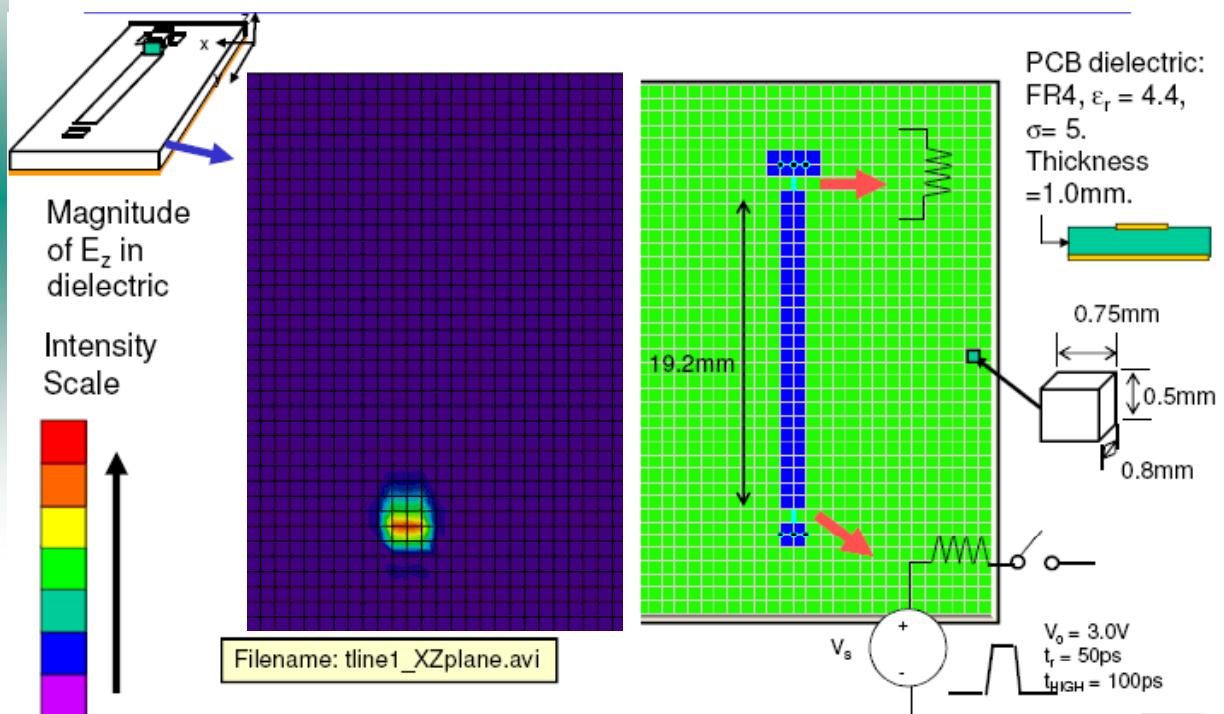
Demo EM Field in Microstrip



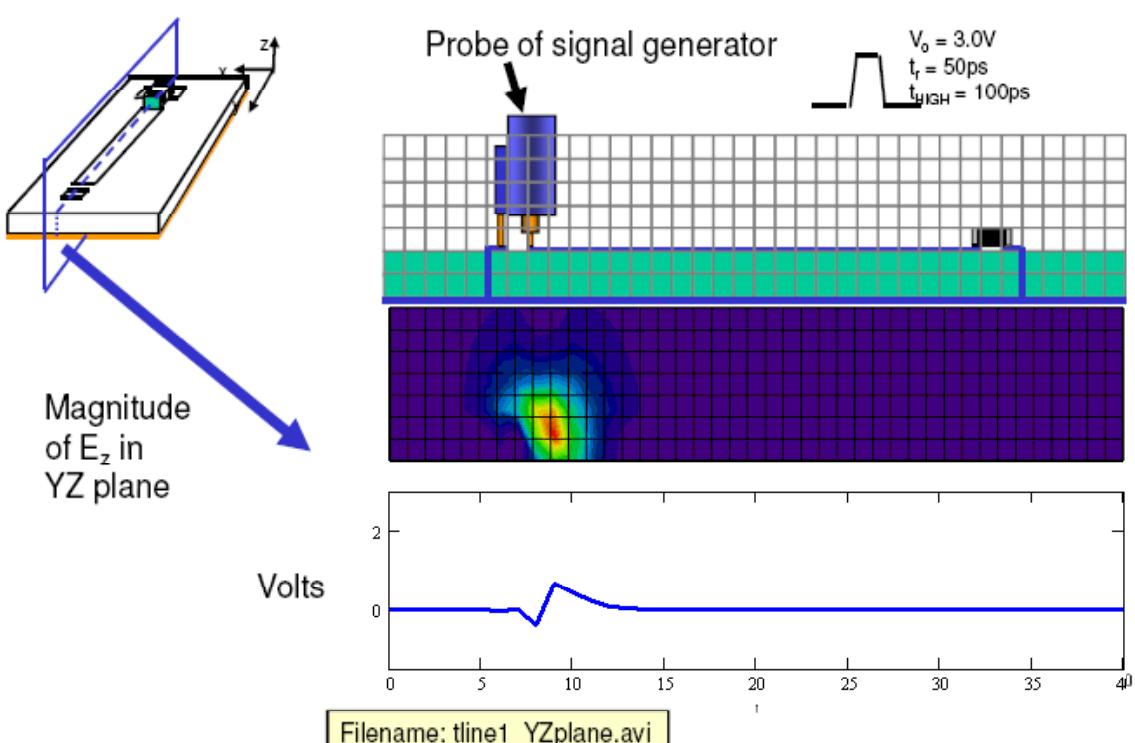
A numerical method, known as Finite-Difference Time-Domain (FDTD) is applied to Maxwell's Equations, to provide the approximate values of E and H fields at selected points on the model at every 1.0 picoseconds

Ref: <http://pesona.mmu.edu.my/~wlkung/Phd/phdthesis.htm>)

Magnitude of E_z in Dielectric



Magnitude of E_z in YZ Plane





Comparison: PCB Transmission Lines

Microstrip line	Stripline	Co-planar line
Suffers from dispersion and non-TEM modes	Pure TEM mode	Suffers from dispersion and non-TEM modes
Easy to fabricate	Difficult to fabricate	Fairly difficult to fabricate
High density trace	Mid density trace	Low density trace
Fair for coupled line structures	Good for coupled line structures	Not suitable for coupled line structures
Need through holes to connect to ground	Need through holes to connect to ground	No through hole required to connect to ground



Impedance Matching

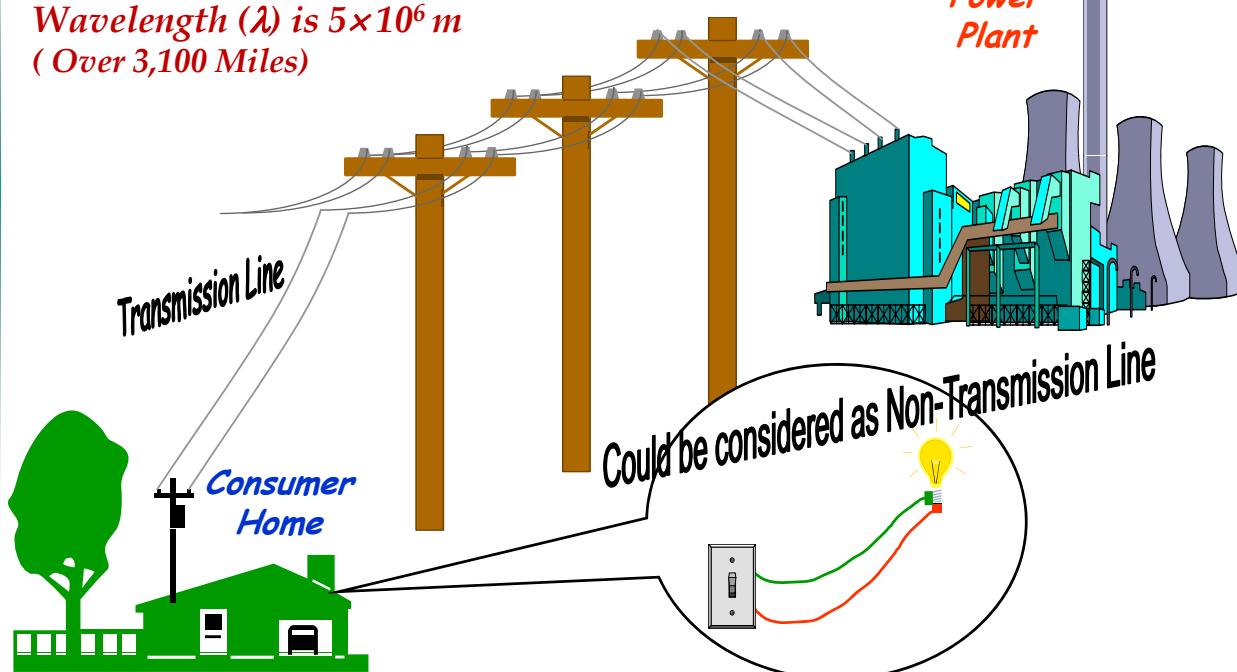
When Transmission Line?



Power Frequency (f) is @ 60 Hz

Wavelength (λ) is 5×10^6 m

(Over 3,100 Miles)



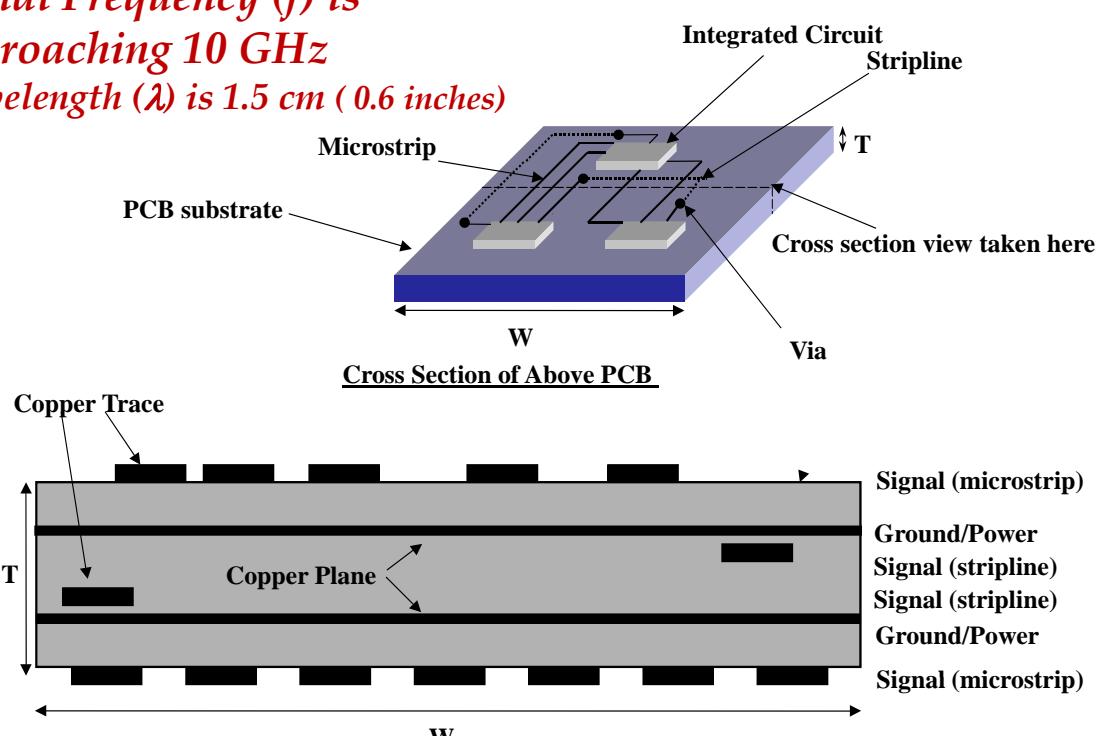
PCB Transmission Lines?



Signal Frequency (f) is

approaching 10 GHz

Wavelength (λ) is 1.5 cm (0.6 inches)





When Transmission Lines in RF PCB?

$t_{of} > 0.5t_{rise}$ For squarewave (Digital PCBs)

where $t_{of} = \frac{\text{length}}{\text{Phase Velocity}}$, called time of flight

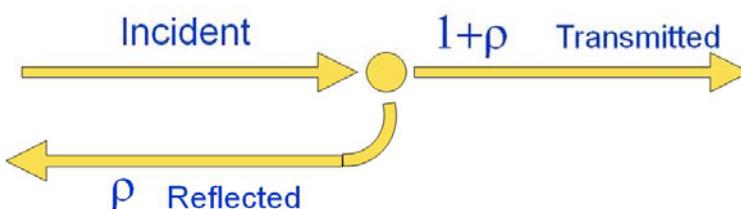
$l_{trans-line} > \lambda/10$ (For Sine in Digital PCBs)

$l_{trans-line} > \lambda/20$ (For RF PCBs)

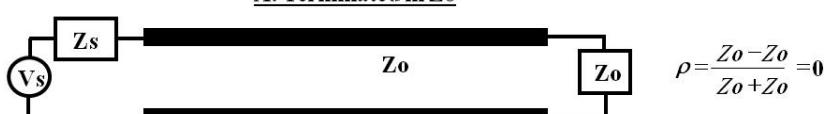
- In Digital PCBs we treat the signal in voltage & use voltage divisor rules
- In RF we deal with RF power, not the voltage & current, then the reflection becomes important



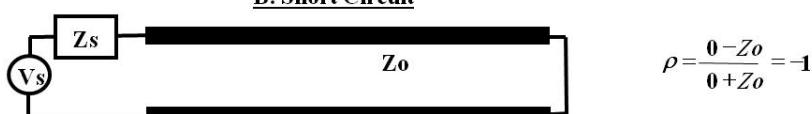
Reflection Constant



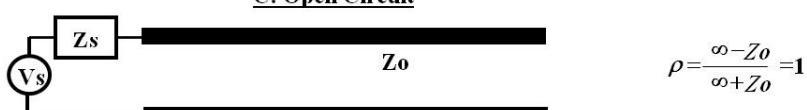
A: Terminated in Z_0



B: Short Circuit



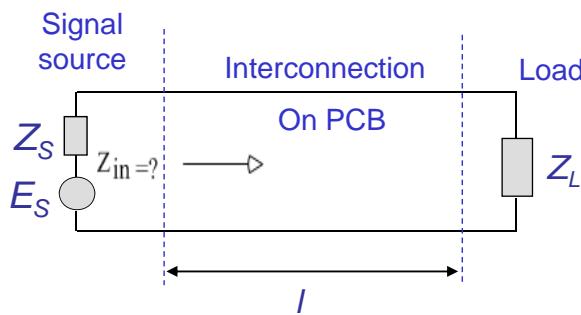
C: Open Circuit





Why Impedance Matching?

- Repetition of Lecture on Transmission Lines



$$Z = Z_0 \frac{Z_L + jZ_0 \tan \theta}{Z_0 + jZ_L \tan \theta} \quad (1)$$

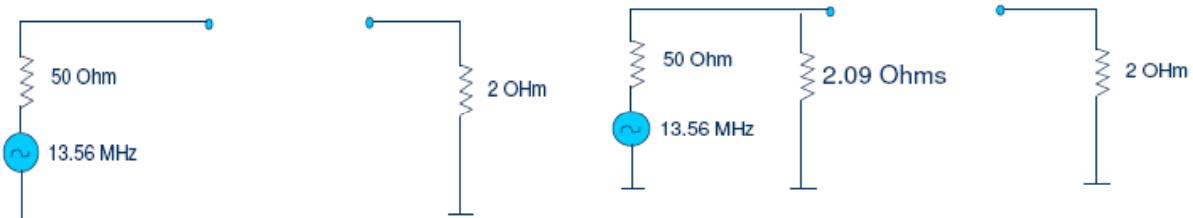
Z_L	ℓ/λ	θ	
∞	$1/6$	60°	$-28.87j$
0	$1/6$	60°	$86.6j$
∞	$1/4$	90°	0
0	$1/4$	90°	∞
∞	$1/2$	180°	∞
0	$1/2$	180°	0
∞	$1/3$	120°	$28.87j$
0	$1/3$	120°	$-86.60j$

- When matched $Z_0 = Z_L$ then Z_{in} is not dependent upon length of line.



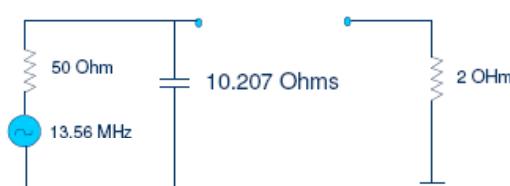
How Matching Works?

We want to match Z_s of 50Ω with 2Ω Load???



Now lets see what will happen if we replace the resistor with a capacitor of 1150 pF which has impedance $X = 1/2\pi f C = -j10.207 \text{ Ohm}$ @ 13.56 MHz

$$Z = \frac{R_1 \times R_2}{R_1 + R_2} = 2 \text{ Ohm}$$

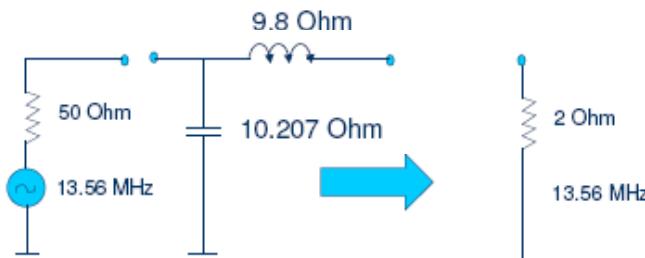


$$Z = \frac{R \cdot (-jX)}{R + (-jX)} = 2 - j 9.8 \text{ Ohms}$$

How Matching Works?

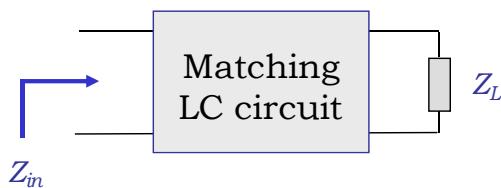


$$Z = \frac{R_{\parallel}(-jX)}{R_{\parallel} + (-jX)} = 2 - j 9.8 \text{ Ohms}$$



So if we add an inductor which impedance value @ 13.56 MHz is 9.8 Ohm
Inductor and capacitance will cancel each other and our match will be complete.
 $L=115 \text{ nH } @ 13.56 \text{ MHz}$

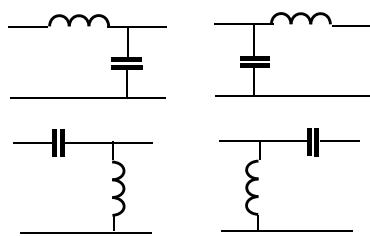
LC Impedance Matching



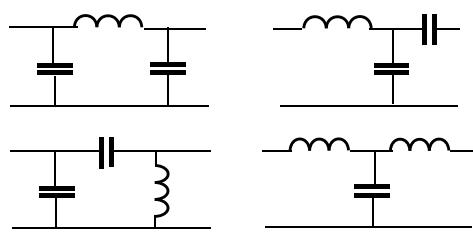
In most cases Z_{in} is expected to be real

Impedance must be matched:
 $X_{in} = -X_s$ and $R_{in} = R_s$

L-match circuits:



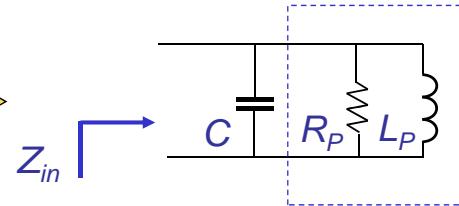
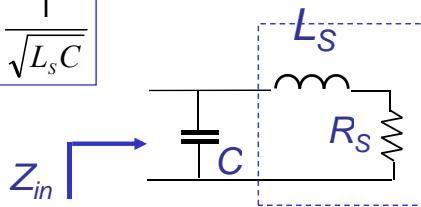
π -match and T-match circuits:



Narrow Band Transformation



$$\omega_0 \approx \frac{1}{\sqrt{L_s C}}$$



$$R_s + j\omega L_s = \frac{R_p j\omega L_p}{R_p + j\omega L_p}$$

$$R_s = \left. \frac{R_p}{R_p^2 / (\omega L_p)^2 + 1} \right|_{\omega \approx \omega_0} = \frac{R_p}{Q^2 + 1} \approx \frac{R_p}{Q^2}$$

$$L_s = \left. \frac{L_p \cdot R_p^2 / (\omega L_p)^2}{R_p^2 / (\omega L_p)^2 + 1} \right|_{\omega \approx \omega_0} = \frac{L_p \cdot Q^2}{Q^2 + 1} \approx L_p$$

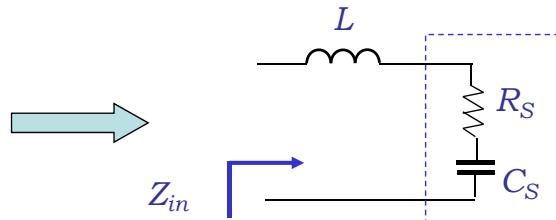
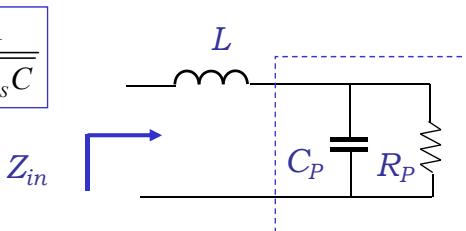
$$Z_{in} \Big|_{\omega \approx \omega_0} = R_p \approx Q^2 R_s$$

Upwards Resistance Transformer

Pi & T match Circuits



$$\omega_0 \approx \frac{1}{\sqrt{L_s C}}$$



$$\frac{1}{R_p} + j\omega C_p = \frac{j\omega C_s / R_s}{1/R_s + j\omega C_s}$$

$$R_s = \left. \frac{R_p}{R_p^2 (\omega C_p)^2 + 1} \right|_{\omega \approx \omega_0} = \frac{R_p}{Q^2 + 1} \approx \frac{R_p}{Q^2}$$

$$C_s = C_p \cdot \left. \frac{R_p^2 (\omega C_p)^2 + 1}{R_p^2 (\omega C_p)^2} \right|_{\omega \approx \omega_0} = C_p \cdot \frac{Q^2 + 1}{Q^2} \approx C_p$$

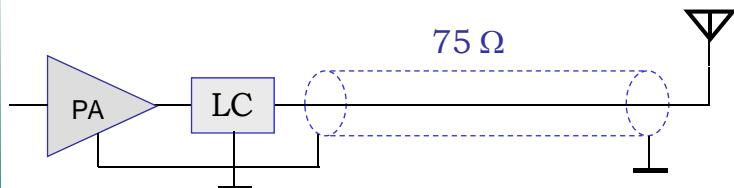
$$Z_{in} \Big|_{\omega \approx \omega_0} = R_s \approx \frac{R_p}{Q^2}$$

As $C_s \approx C$ the equivalent circuit has also a resonance at ω_0

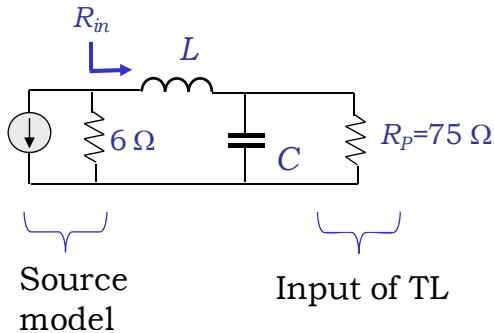
Note that once R_p and R_s are related, Q is defined and it cannot be improved by those simple L-match circuits



Example LC Matching



Match PA of output resistance 6Ω to 75Ω transmission line, also matched to antenna.
 $f=100\text{MHz}$



$$R_{in} = \frac{R_p}{Q^2 + 1} \rightarrow Q^2 = \frac{R_p}{R_{in}} - 1 = 11.5$$

$$Q^2 = \frac{(R_p)^2}{L/C} \rightarrow L/C = \frac{75^2}{11.5} = 489$$

$$(2\pi f)^2 = \frac{1}{LC} \rightarrow LC = \frac{1}{(2\pi \cdot 10^8)^2} = 254 \cdot 10^{-20}$$

$$L = 35.2 \text{ nH}, \quad C = 72 \text{ pF}$$

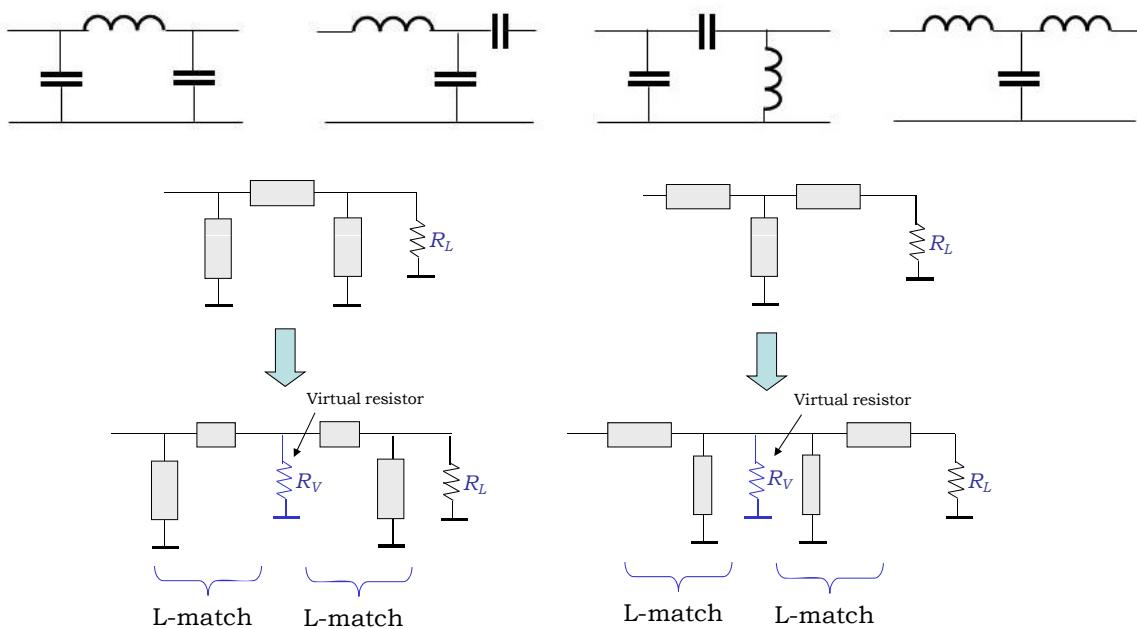
Observe that the matching circuit also works as LPF that attenuates higher harmonic components of PA

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Pi & T Match Circuits

π -match and T-match circuits:



There is one degree of freedom more, we can also choose Q



Smith charts revisited

$$\rho = \frac{Z_L - Z_0}{Z_L + Z_0} = \frac{Z_L/Z_0 - 1}{Z_L/Z_0 + 1} = \frac{Z - 1}{Z + 1}$$

$$Z = a + jb, \quad \rho = c + jd$$

Normalized loading impedance

$$\rho = c + jd = \frac{a^2 + b^2 - 1}{(a + 1)^2 + b^2} + j \left(\frac{2b}{(a + 1)^2 + b^2} \right)$$



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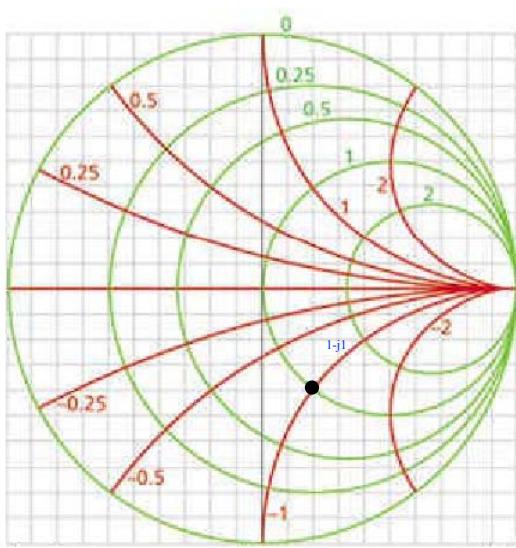
RF PCB Design:Lecture-1

© Rashad.M.Ramzan 2010-11



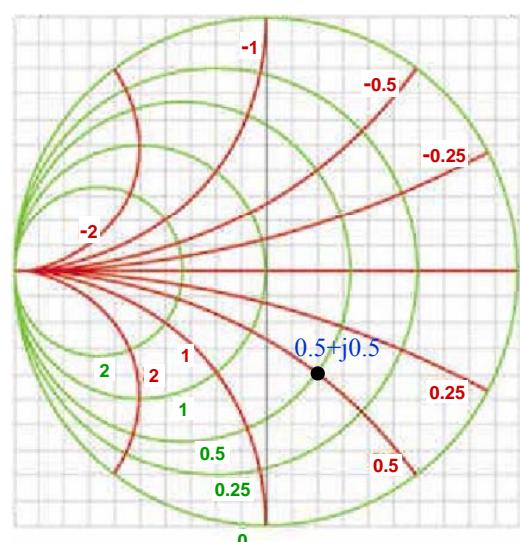
Impedance – Admittance Conversion

Constant R and X circles



$$Z = R+jX$$

Constant G and B circles



$$\text{Same load}$$

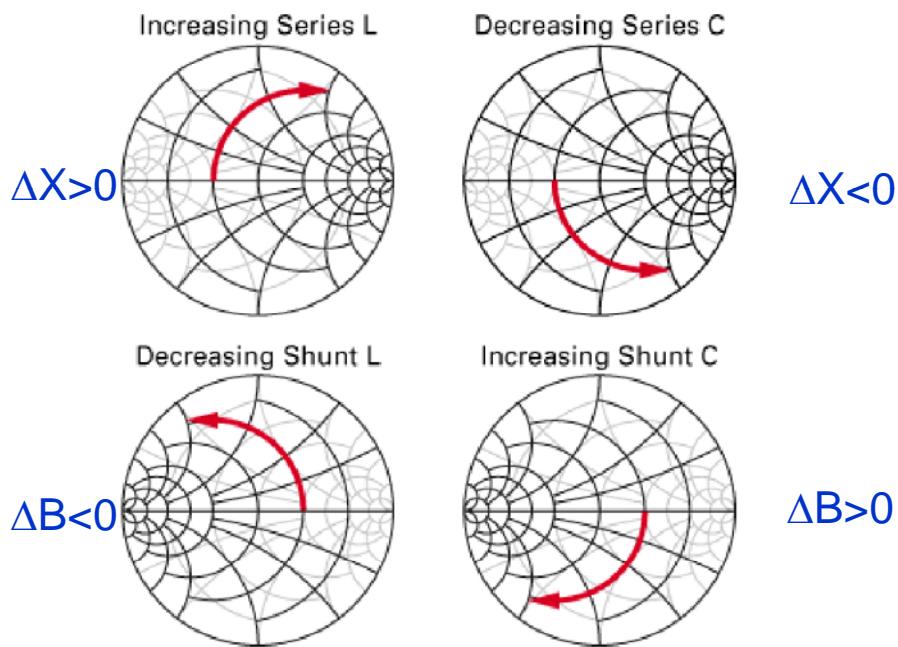
$$Y = 1/Z = G+jB$$

RF PCB Design:Lecture-1

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Modifying Admittance or Impedance



Impedance Matching: Smith Chart

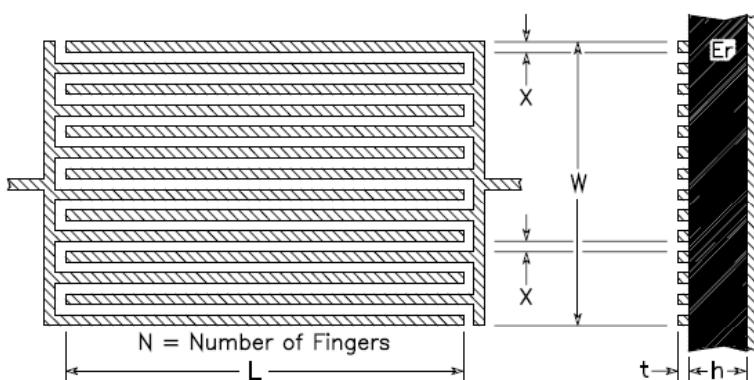
- Show the Simulation of the Impedance matching..... You can also see these simulation at....
- <http://www.amanogawa.com/archive/transmissionA.html>



μ-wave Components



μ-wave Capacitors



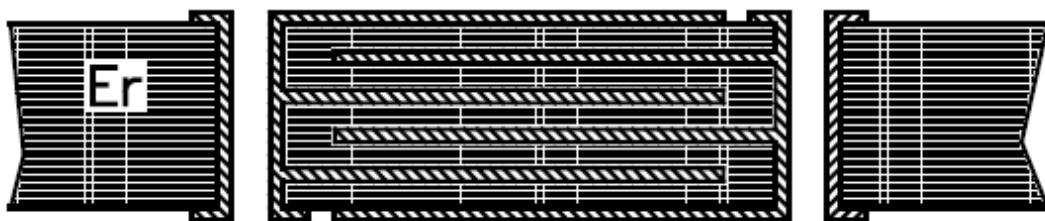
$$C_2 = \frac{\epsilon_r + 1.0}{w} L [(N - 3.0) A1 + A2] \text{ (pF / in)}$$

$$A1 = \left[0.3349057 - 0.15287116 \left(\frac{t}{X} \right) \right]^2$$

$$A2 = \left[0.50133101 - 0.22820444 \left(\frac{t}{X} \right) \right]^2$$

(Equation valid for $h > w/N$)

μ -wave Multilayer Capacitor



$$C = \frac{0.229\epsilon_r A(n-1.0)}{d} \text{ (pF)}$$

where:

A = area of planes in square inches

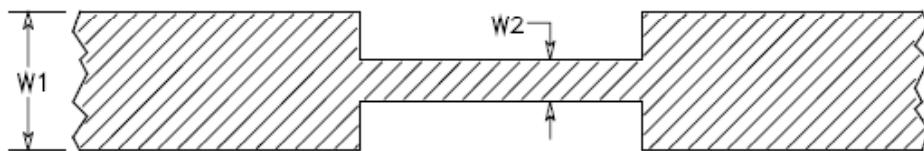
n = number of conductor layers

d = plate spacing

μ -wave Inductor



Inductor- Inline Inductor is formed by a Very Thin, High Impedance Trace.



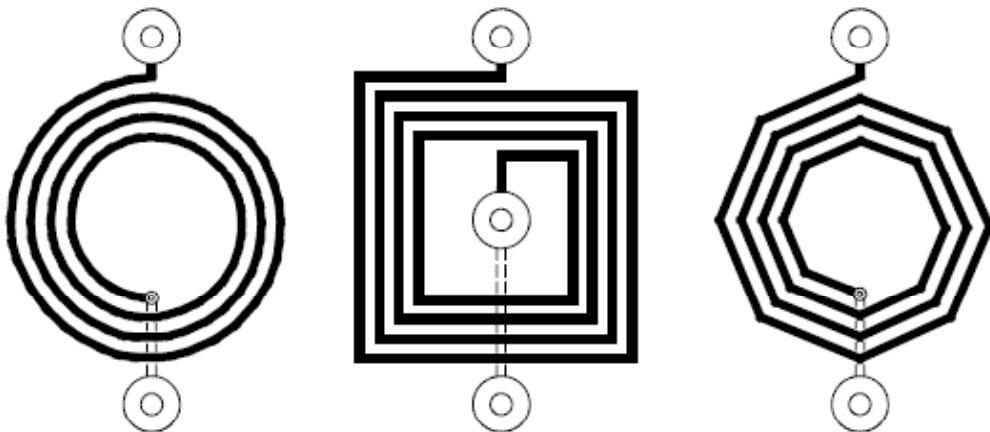
❖ Length Must be Shorter than Critical Length to Prevent Reflections. Can Remove Plane(s) to Boost Inductance.

❖ $L = Z_0^2 \times C$ or $T_{pd} \times Z_0$ (Many Equations available. This is Extremely Accurate.)



μ -wave Inductors

♦ Spiral Inductors -

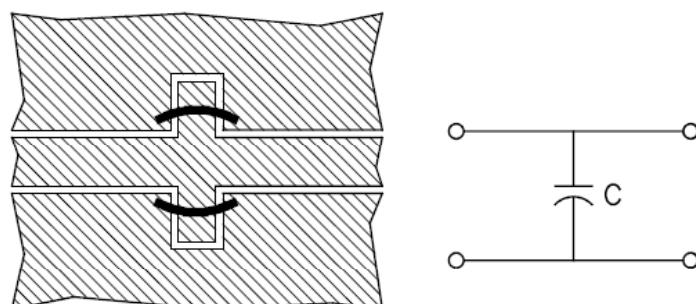


(Equations in Wadell- Pages 392-406)

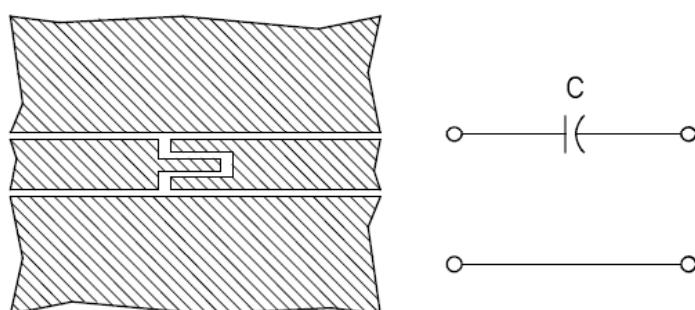


μ -wave Capacitors

CPW & CPWG Shunt Capacitor -



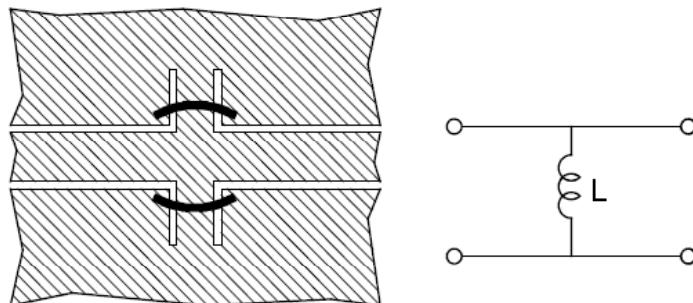
CPW & CPWG Series Capacitor -



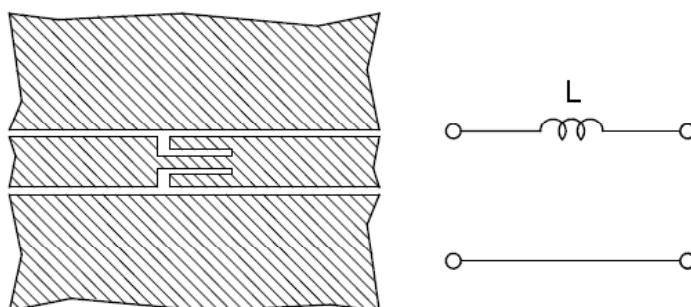


μ -wave Inductors

CPW & CPWG Shunt Inductor -

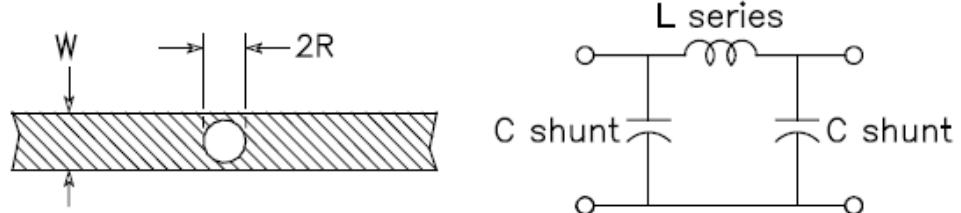
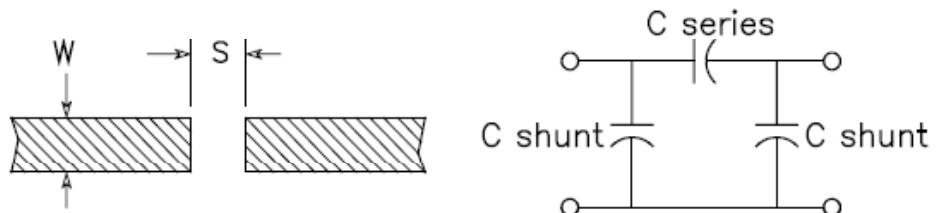


CPW & CPWG Series Inductor -



μ -wave Capacitors & Inductors

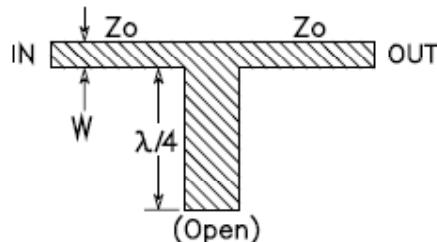
Gap in Centered Stripline Conductor-



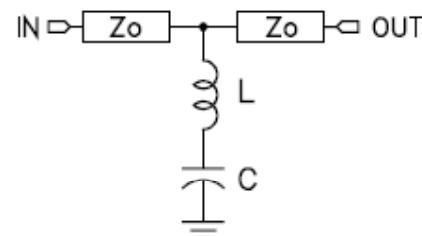


μ -wave Capacitors & Inductors

- ◆ $\lambda/4$ Stub is Series Resonant Circuit at Frequency.
- ◆ Circuit Shorts to Ground at $\lambda/4$, $3/4\lambda$, etc.
- ◆ Open Circuit at DC, $\lambda/2$, λ , etc.



Microstrip Open-Stub



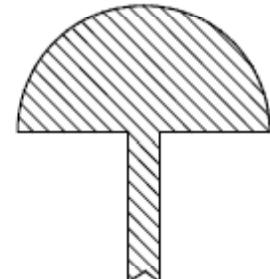
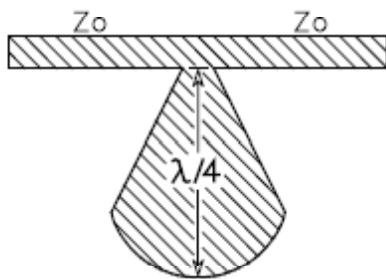
Microstrip Open-Stub Equivalent Circuit at Resonant Frequency

- ◆ 2W Wide for High Q and to Prevent Reflections.



μ -wave Capacitors & Inductors

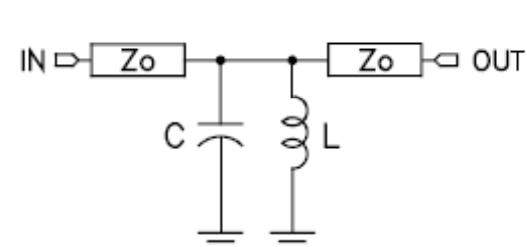
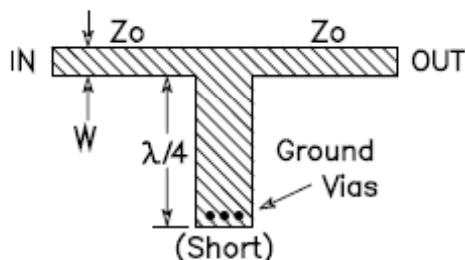
- ◆ Open Stubs (one just shown) have Narrow Frequency Over which they Short to Ground.
- ◆ Flaring the Stub Increases Frequency Response.





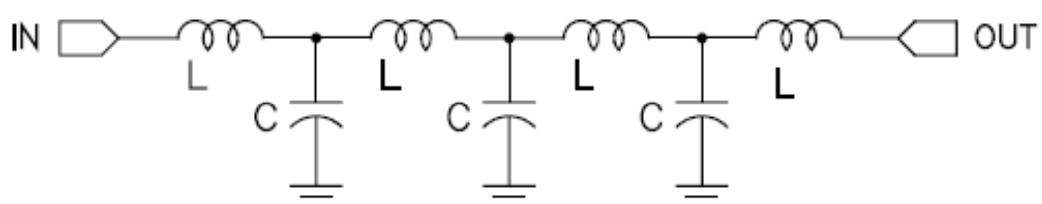
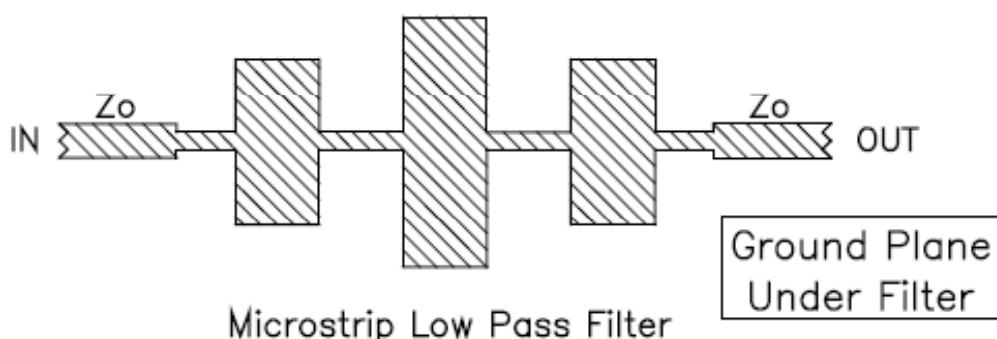
μ -wave Capacitors & Inductors

- ◆ $\lambda/4$ Stub, Shorted to Ground, is Parallel Resonant Filter at Frequency of Interest.
- ◆ Circuit Shorts to Ground at DC, $\lambda/2$, λ , etc.
- ◆ Open Circuit at $\lambda/4$, $3/4 \lambda$, etc.



μ -wave Low Pass Filter

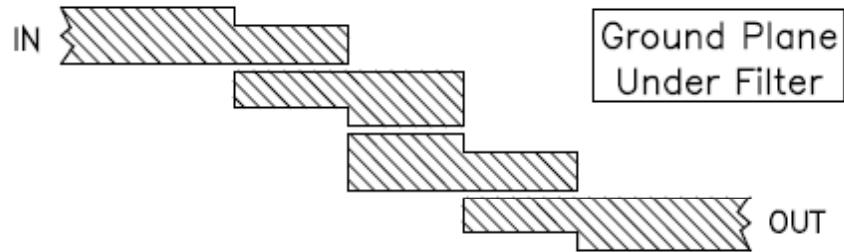
- ◆ Low Pass Filter -



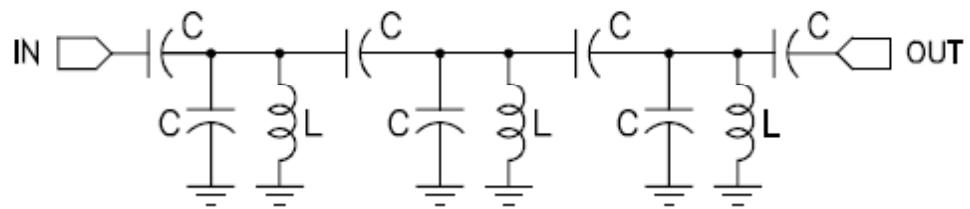


μ -wave Band Pass Filter

♦ Edge Coupled Band Pass Filter -

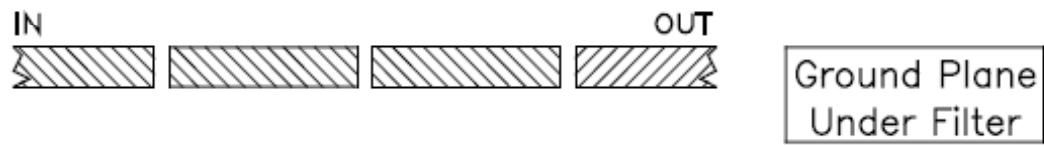


Microstrip Band Pass Filter (Edge Coupled)

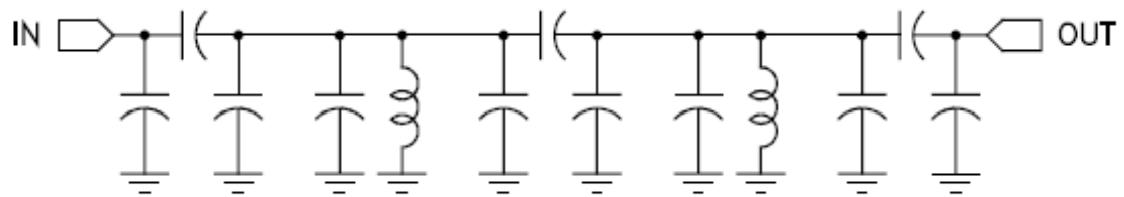


μ -wave Band Pass Filter

♦ End Coupled Band Pass Filter -



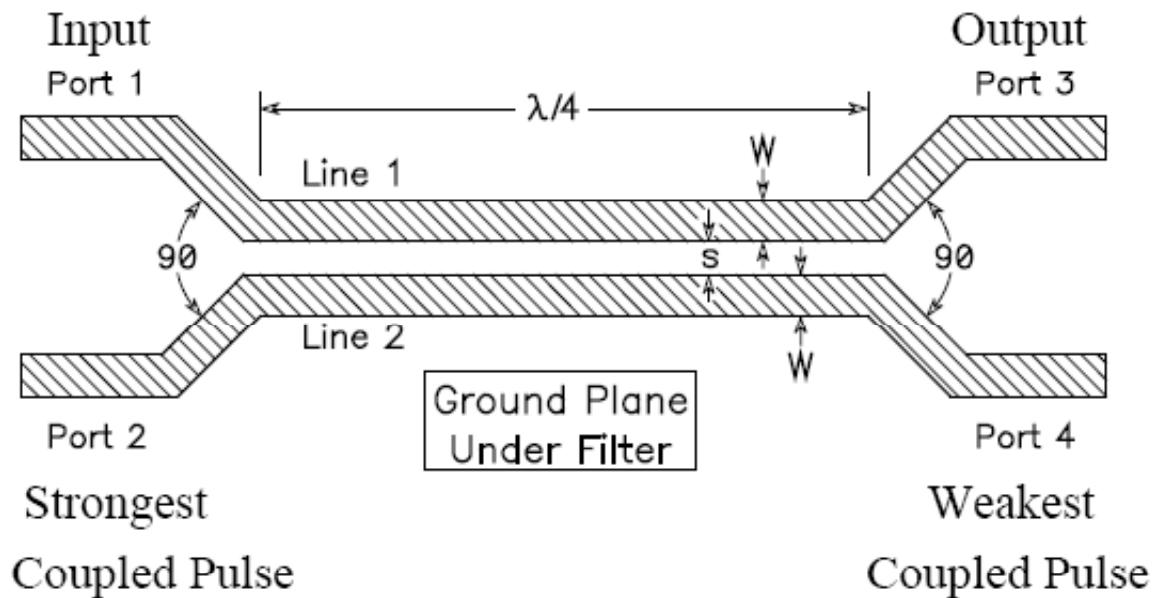
Microstrip Band Pass Filter (End Coupled)





Directional Coupler

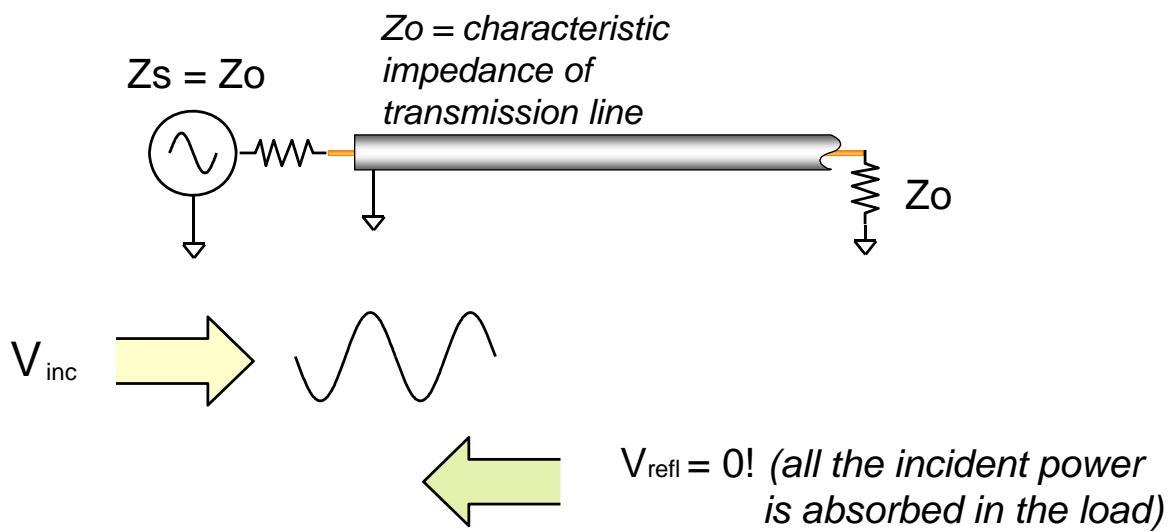
♦ Directional Coupler -



S-Parameters



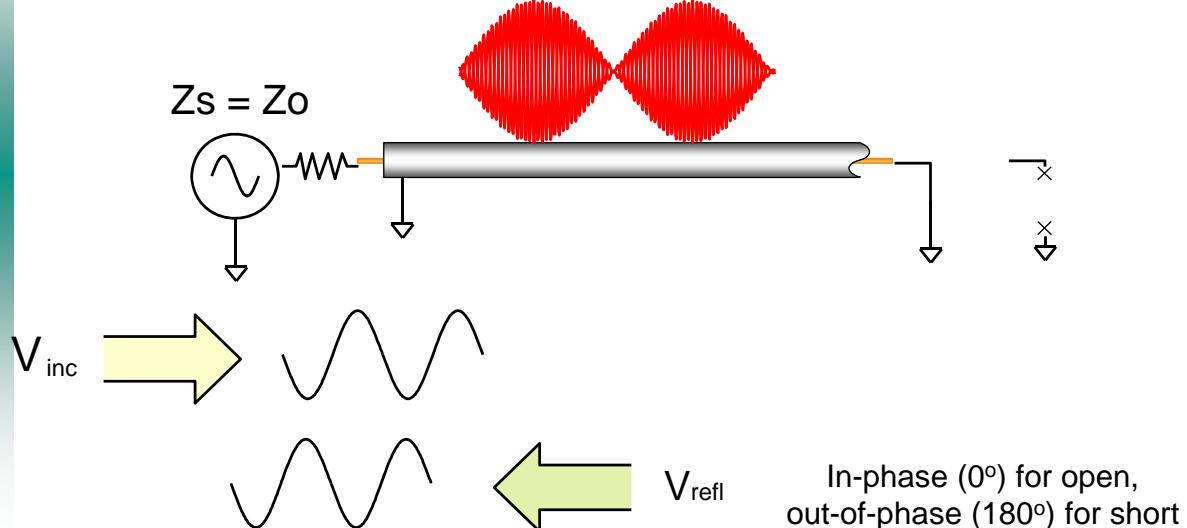
Transmission Line Terminated with Z_0



For reflection, a transmission line terminated in Z_0 behaves like an infinitely long transmission line



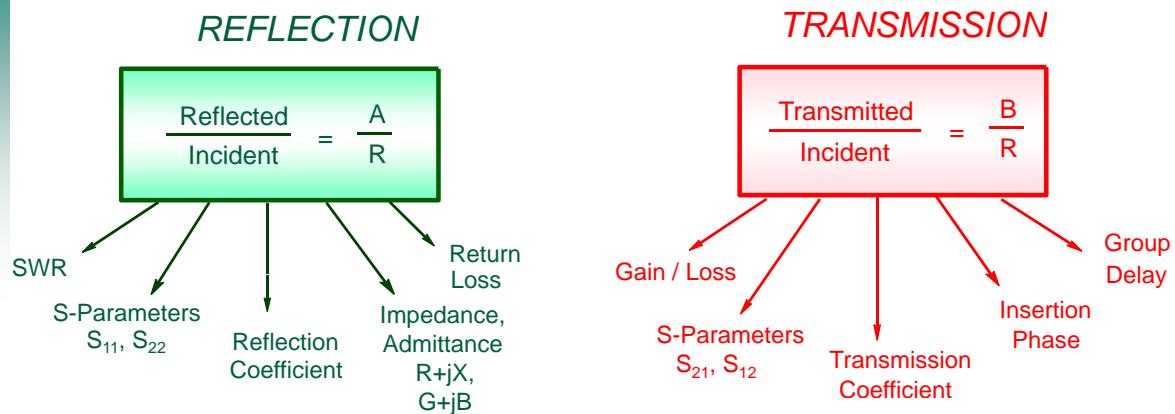
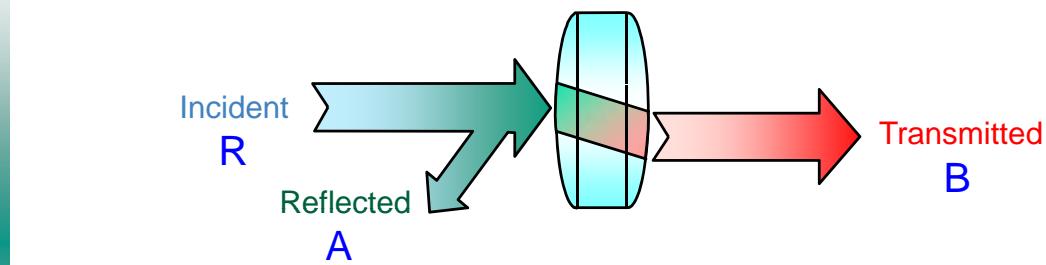
Transmission Line with Short & Open



For reflection, a transmission line terminated in a short or open reflects all power back to source



Reflection & Transmission

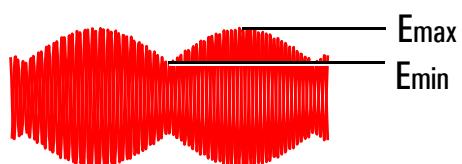


Reflection Parameters

Reflection Coefficient

$$\Gamma = \frac{V_{\text{reflected}}}{V_{\text{incident}}} = \rho \angle \Phi = \frac{Z_L - Z_0}{Z_L + Z_0}$$

$$\text{Return loss} = -20 \log(\rho), \quad \rho = |\Gamma|$$



Voltage Standing Wave Ratio

$$\text{VSWR} = \frac{E_{\text{max}}}{E_{\text{min}}} = \frac{1 + \rho}{1 - \rho}$$

No reflection
 $(Z_L = Z_0)$

0
 ∞ dB
 1

ρ
RL
VSWR

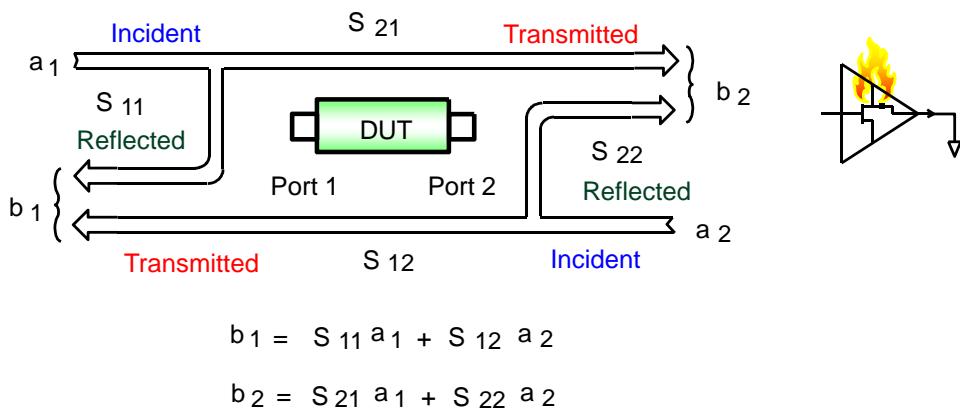
Full reflection
 $(Z_L = \text{open, short})$

1
 0 dB
 ∞



Why S-Parameters?

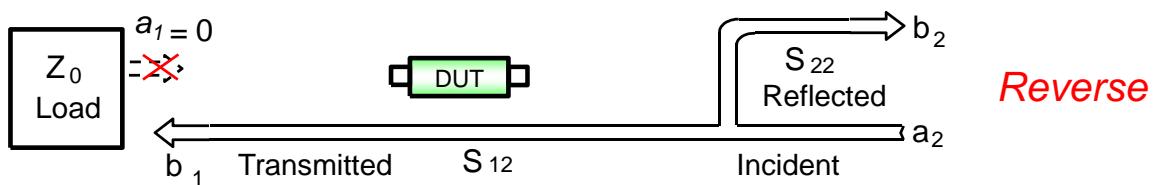
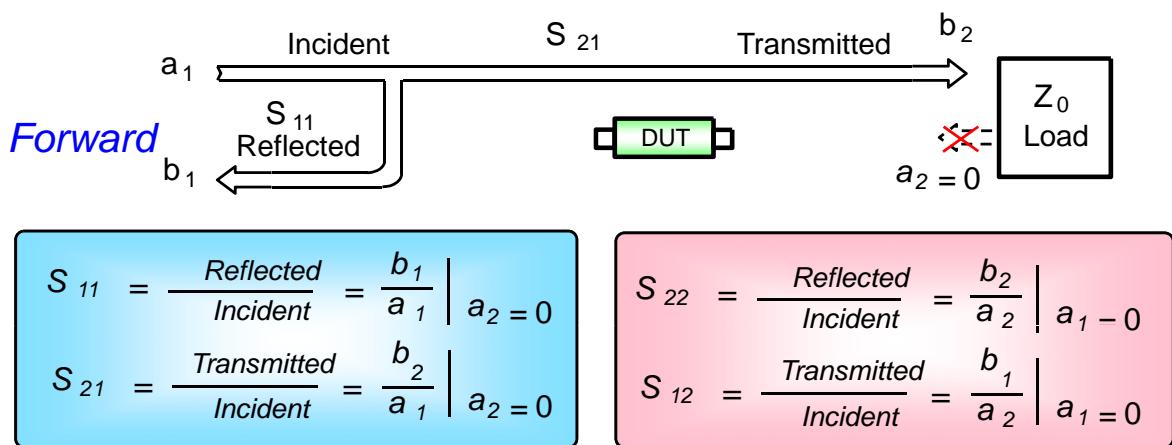
- Relatively easy to obtain at high frequencies
 - Measure voltage traveling waves with a vector network analyzer
 - Don't need shorts/opens which can cause active devices to oscillate or self-destruct
- Relate to familiar measurements (gain, loss, reflection coefficient ...)
- Can cascade S-parameters of multiple devices to predict system performance
- Can compute H, Y, or Z parameters from S-parameters if desired



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Measuring S-Parameters?





Summary

- Objective of Module-2
- Basic of Transmission Line
- Smith Chart
- Matching
- S-Parameters