

Agenda

- Intro: EM SCA
 - Background
 - Mind Map
 - Proposed Influence Diagram
 - Operational View / Activity Diagram



Background

What is Side Channel Attack?

- The Side-channel attack—SCA is a security exploitation technique that adversaries use to extract secret
 information/data from a chip/hardware/system by measuring or analyzing various physical parameters like
 power supply current, chip execution time, and electromagnetic radiation.
- The SCAs pose significant threats with the extensive and emerging use of the Internet of Things—loTs in users' private lives and almost every industry with different priorities and complex connectivity-generated vulnerabilities.

Why is this research?

- According to Gartner, the connected IoTs will rise to 75 billion by 2025 (Hodgdon, 2021). The convenience
 of IoTs comes with a price: falsely underestimated the vulnerability of almost any electronic device hardware
 against side-channel attacks.
- SCA causes multi-billion dollar incidents: In 2018, Spectre and Meltdown exploited speculative execution on a CPU to snoop on another and steal cryptographic keys, which caused to force redesign of all superscalar SPUs from 2020 to 2025 (Anderson, 2021).
- It is only possible to provide information security by securing the system's hardware (Bhunia & Tehranipoor, 2019).

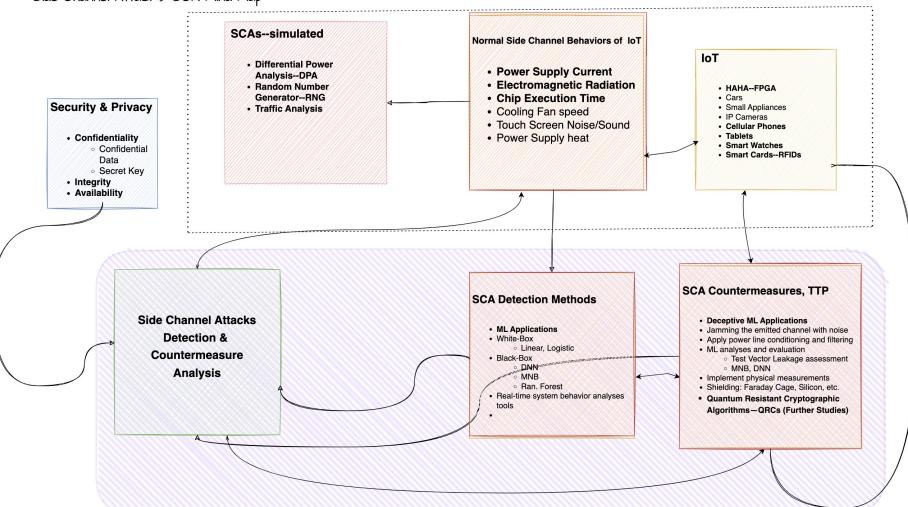
The focus of Research?

This research will explore commonly existing vulnerabilities in IoT devices. It will improve SCA detection accuracy and speed to provide countermeasures by using ML models and simulations against Electromagnetic—EM Side-channel Attacks.



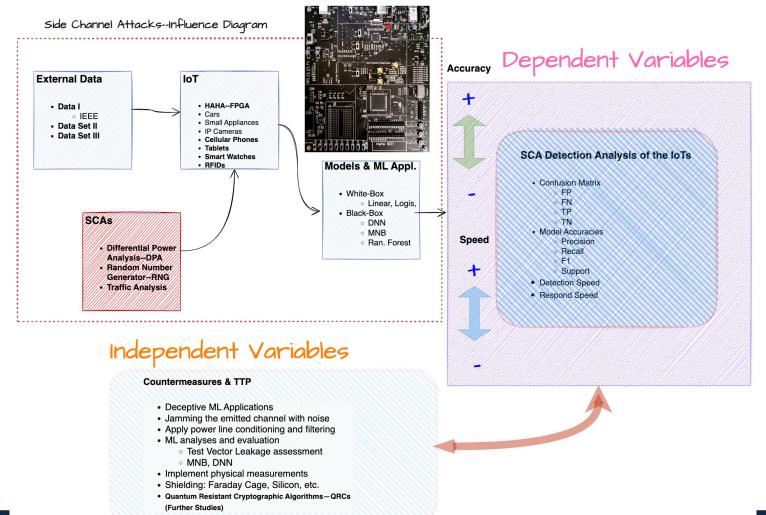
Mind Map

Side Channel Attacks--SCA Mind Map



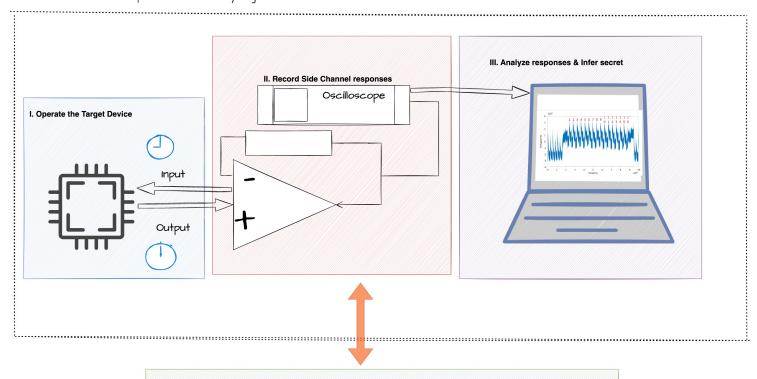


Proposed Influence Diagram



Operational View/Activity Diagram

Side Channel Attacks--Operational View/Activity Diagram



IV. Back propagate, Detect & Apply Countermeasures

Countermeasures & TTP

- · Deceptive ML Applications
- · Jamming the emitted channel with noise
- · Apply power line conditioning and filtering
- · ML analyses and evaluation
 - Test Vector Leakage assessment
 - MNB, DNN
- · Implement physical measurements
- · Shielding: Faraday Cage, Silicon, etc.
- Quantum Resistant Cryptographic Algorithms—QRCs (Further Studies)

