



SB70LC Platform Reference

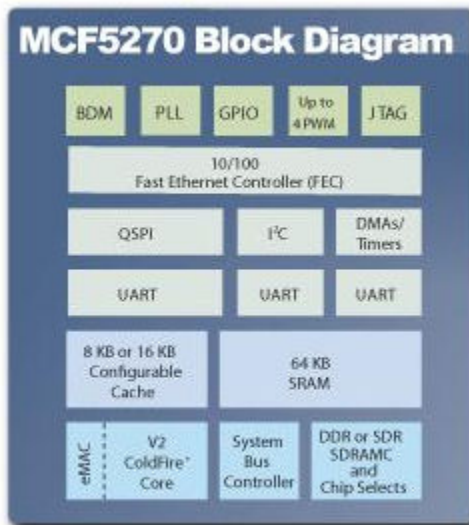
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Introduction

This document provides the memory map and locations of reference materials for those who wish to add additional hardware to their NetBurner device. Hardware dimensions, connectors and pinouts are described in the datasheet for your NetBurner device at www.netburner.com.

MCF5270 Processor Block Diagram

The block diagram of the 5270 processor is shown below. The Freescale reference manual provides in-depth information on the processor and is located in the \nburn\docs\Freescale directory of your NetBurner installation.



SB70LC Development Board Schematic

The SB70LC development board schematic is located in the \nburn\docs\platform directory. This schematic can be used for design ideas in your own hardware implementation for power, RS-232 conversion or SD Flash card implementation.

Memory Map

If you are adding peripherals to your NetBurner device address/data bus, you can choose unused memory locates from the table below. Once an area has been selected, you will need to configure the appropriate chip select address and option registers in the MCF5270 processor. Please refer to the chip select sections of the Freescale MCF5270 processor manual for details on the register configuration.

| Memory Region | Address Range | Region Description |
|------------------|---------------------------|--|
| Undefined | 0x00000000 to 0x01FFFFFF | Undefined area to catch NULL pointers |
| SDRAM | 0x02000000 to 0x027FFFFFF | 8Mbytes of SDRAM |
| Unused | 0x02200000 to 0x1FFFFFFF | Available to programmer |
| VBR | 0x20000000 to 0x200003FF | 5270 Vector Base Register |
| RAMBAR | 0x20000000 to 0x2000FFFF | 5270 internal SRAM |
| Unused | 0x20010000 to 0x3FFFFFFF | Available to programmer |
| IPSBAR | 0x40000000 to 0x7FFFFFFF | 5270 Internal device registers. These are accessible using the sim structure defined in sim5270.h. |
| Unused | 0x80000000 to 0xFFBFFFFFF | Available to programmer |
| Start of FLASH | 0xFFC00000 | Start of 512Kbytes Flash memory |
| Flash Monitor | 0xFFC00000 to 0xFFC03FFF | The Boot Monitor |
| Monitor Params | 0xFFC04000 to 0xFFC05FFF | Monitor parameter storage |
| User Params | 0xFFC06000 to 0xFFC07FFF | User parameter storage |
| Application Code | 0xFFC08000 to ... | Application code space |
| End of FLASH | 0xFFC7FFFF | End of Flash memory |