Ethernet Core Module

200 Version



DATASHEET

Key points

- Use as a high-performance single board computer or add Ethernet connectivity to a new or existing design
- Industrial temperature range (-40°C to 85°C)
- Customize with development kit

Device connectivity

- 10/100Mbps Ethernet
- 8 UARTs, 4 I²C, 2 CAN, 3 SPI, 1-Wire® and SSI support
- SD/MMC and MicroSD flash card ready
- 30 digital I/Os
- Six 12-bit analog-to-digital converters (ADC)
- Two 12-bit digital-to-analog converters (DAC)
- Eight pulse width modulators (PWM)

Performance and memory

• 32-bit 250 MHz processor

• 64MB DDR2 RAM and 8MB Flash

Companion development kit

The following is available with the development kit:

- · Customize any aspect of operation including web pages, data filtering, or custom network applications
- Development software: NB Eclipse IDE, graphical debugger, deployment tools, and examples
- Communication software: TCP/IP stack, HTTP web server, FTP, E-mail, and flash file system
- System software: uC/OS RTOS, ANSI C/C++ compiler and linker

The following optional software modules are not included with kit and are sold separately:

- Embedded SSL & SSH Security Suite (Module License Version)
- SNMP





Specifications

Processor and Memory

32-bit Freescale ColdFire 54415 CPU running at 250MHz with 64MB DDR2 RAM and 8MB Flash

Network Interface

10/100 BaseT

Data I/O Interface (P1)

- Up to 8 UARTs
- Up to 4 I2C
- Up to 2 CAN 2.0b controllers
- Up to 3 SPI
- Up to 30 digital I/O
- Up to six 12-bit analog-to-digital converters (ADC)
- Up to two 12-bit digital-to-analog converters (DAC)
- Up to 8 pulse width modulators (PWM)
- Up to 4 external timer in or outputs
- MicroSD flash card ready
- 1-Wire® interface
- Synchronous Serial Interface (SSI)

Flash Card Support

FAT32 support for SD Cards up to 8GB (requires exclusive use of SPI signals). Card types include SD/MMC (up to 2GB) and SDHC.

Serial Configurations

The UARTs can be configured in the following way:

- Up to 8 TTL ports
- Add external level shifter for RS-232
- Add external level shifter for RS-422/485 (up to three ports)

Note: UART 0/1/2 also provides RTS/CTS hardware handshaking signals.

Physical Characteristics

Dimensions (inches): 2.00" x 1.1875"

Weight: 1 oz.

Power

DC Input Voltage: 3.3V @ 500mA typical

Environmental Operating Temperature

-40° to 85° C

RoHS Compliance

The Restriction of Hazardous Substances guidelines ensure that electronics are manufactured with fewer environment harming materials.

Agency Approvals

UL, C/UL, CE, FCC









Part Numbers

NANO54415 Ethernet Core Module (200 Version, with PCleMini Connector)

Part Number: NANO54415-200IR

NANO Break out Board

Part Number: NANO-BOB-200IR

NANO54415 Development Kit

Part Number: NNDK-NANO54415-KIT

Kit includes all the hardware and software you need to customize the included platform hardware. See NetBurner Store product page for package contents.

Embedded SSL & SSH Security Suite (Module License Version)

Part Number: NBLIC-SSL-MODULE

Only required if you are using a development kit.

SNMP V1 (Module License Version)

Part Number: NBLIC-SNMP

Available as an option if you are using a development kit.

Ordering Information

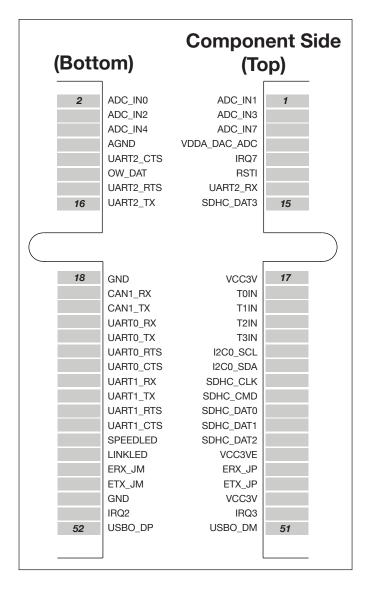
E-mail: sales@netburner.com Online Store: www.NetBurner.com Telephone: 1-800-695-6828



Pinout and Signal Description

The module has one 52-pin Mini PCI express connector that is designed to interface with a standard 52-pin mini PCI Express socket. Figure 1 shows the location of pin one and primary function. Table 1 provides a connector pinout and detailed primary and alternate function descriptions. Reference Freescale Manual for additional CPU pin function details.

Figure 1: Edge Connector Pinout for P1 Connector



Alternate Monitor Boot Jumper

The boot jumper is a pair of circular pads located near the middle line of the board near the connector. It can be used to recover from a software or configuration fault.

Table 1: Pinout and Signal Descriptions for P1 Connector (1)

| | Max Voltage | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | ı | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | 2 3.3VDC | 3.3VDC | 3.3VDC | it ² 3.3VDC | 3.3VDC | 1 | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | |
|--------------|---------------------------------|-------------------------------------|-------------------------------------|---|-------------------------------------|---|-------------------------------------|--|---|----------------------|--|-----------------------|---|---|--|--|---|---------------------|--------|--|--|--|--|---|--|
| P1 Connector | Description | Analog to Digital Converter 1 Input | Analog to Digital Converter 0 Input | Analog to Digital Converter 3 Input or Digital to Analog Converter 0 Output | Analog to Digital Converter 2 Input | Analog to Digital Converter 7 Input or Digital to Analog Converter 1 Output | Analog to Digital Converter 4 Input | ADC and DAC Reference Voltage (required when using ADC or DAC) | ADC and DAC Reference Ground (required when using ADC or DAC) | External Interrupt 7 | UART 2 Clear To Send or UART 6 Transmit or SSI 1 Serial Bit Clock ² | Processer Reset Input | 1-Wire Data Signal or DMA Acknowledge 0 | UART 2 Receive or PWM A3 Output Signal/Input Capture or SSI 1 Serial Receive ² | UART 2 Request To Send or UART 6 Receive or SSI 1 Serial Frame Sync ² | SDHC DAT3 Line / Card Detection or PWM A1 Output Signal/Input Capture or SPI 1 Chip Select 0 | UART 2 Transmit or PWM B3 Output Signal/Input Capture or SSI 1 Serial Transmit ² | Input power 3.3 VDC | Ground | Timer Input 0 or Timer Output 0 or USB On-The-Go VBUS Over-Current | CAN 1 Receive or UART 9 Receive or I ² C 1 Serial Data ^{2,3} | Timer Input 1 or Timer Output 1 or SDHC DAT1 Line / Interrupt Detect | CAN 1 Transmit or UART 9 Transmit or I ² C 1 Clock ^{2,3} | Timer Input 2 or Timer Output 2 or SDHC DAT2 Line / Read Wait | |
| à | General Purpose Description I/O | | | | | | | | | PC6 | PE6/RGPI014 | | PD3/RGPI00 | PE4 | PE5/RGPI015 | PF2 | PE3 | | | PE7/RGPI04 | PC7 | PD0/RGPI03 | PB0 | PD1/RGPI02 | |
| | Fuction 3 | | | | | | | | | | SSI1_BCLK | | | SSI1_RX | SSI1_FS | SPI1_PCS0 | SSI1_TX | | | USBO_VBUS_ OC | I2C1_SDA | SDHC_DAT1 | I2C1_SCL | SDHC_DAT2 | |
| | Function 2 | | | DAC0_OUT | | DAC1_OUT | | | | | UART6_TX | | <u>DACK0</u> | PWM_A3 | UART6_RX | PWM_A1 | PWM_B3 | | | TUOOT | UART9_RX | T10UT | UART9_TX | TZOUT | |
| | Function 1 | ADC_IN1 | ADC_IN0 | ADC_IN3 | ADC_IN2 | ADC_IN7 | ADC_IN4 | VDDA_DAC_ADC | AGND | IRQ7 | UART2_CTS | RSTI | OW_DAT | UART2_RX | UART2_RTS | SDHC_DAT3 | UART2_TX | VCC3V | GND | NIOT | CAN1_RX | T1IN | CAN1_TX | TZIN | |
| | CPU Pin | 두 | Ξ | Х | J2 | χ 33 | G4 | √ 4C | 원 왕 | F12 | Α | K15 | N11 | L L | M3 | B13 | NZ | | | H15 | D15 | H13 | D14 | H14 | |
| | Pin | - | 2 | က | 4 | 2 | 9 | 7 | ∞ | တ | 10 | Ξ | 12 | 13 | 4 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | |

Active low signals, such as RESET, are indicated with an overbar. Each UART can be clocked by the corresponding Tn_IN vnput pin. If using I2C, pull-up resistors must be added to open drain SDA/SCL signals.

| | Max | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | In 3.3VDC | 3.3VDC | 911 Se- 3.3VDC | 3.3VDC | Chip 3.3VDC | 3.3VDC | Select 2 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC | 1 | 3.3VDC | 3.3VDC | 3.3VDC | 3.3VDC |
|--------------------------|------------------------|---|--|--|---|--|--|--|--|---|---|--|---|--|--|--|---|--------------------|---------------------------------------|-------------------|--------------------|--------------------|---------------------|---------------------|---------------------|--------|---|---|-------------------------|-------------------------|
| P1 Connector (continued) | Description | UART 0 Receive or I ² C 4 Serial Data or SPI 2 Serial Data In ^{2,3} | Timer Input 3 or Timer Output 3 or USB On-The-Go VBUS Enable | UART 0 Transmit or I ² C 4 Serial Clock or SPI 2 Serial Data Out ^{2,3} | PC 0 Serial Clock or UART 8 Transmit or CAN 0 Transmit ^{2,3} | UART 0 Request To Send or UART 4 Receive or SPI 2 Chip Select 02 | PC 0 Serial Data or UART 8 Receive or CAN 0 Receive ^{2,3} | UART 0 Clear To Send or UART 4 Transmit or SPI 2 Serial Clock ² | SDHC Clock or PWM A0 Output Signal/Input Capture or SPI 1 Serial Clock | UART 1 Receive or I ² C 5 Serial Data or SPI 1 Serial Data In ^{2,3} | SDHC Command Line or PWM B0 Output Signal/Input Capture or SPI 1 Serial Data In | UART 1 Transmit or I ² C 5 Serial Clock or SPI 3 Serial Data Out ^{2,3} | SDHC DAT0 Line / Busy-State Detect or PWM B2 Output Signal/Input Capture or SPI 1 Serial Data Out | UART 1 Request to Send or UART 5 Recieve or SPI 3 Chip Select 02 | SDHC DAT1 Line / Interrupt Detect or PWM A2 Output Signal/Input Capture or SPI 1 Chip Select 1 | UART 1 Clear To Send or UART 5 Transmit or SPI 3 Serial Clock ² | SDHC DAT2 Line / Read Wait or PWM B1 Output Signal/Input Capture or SPI 1 Chip Select 2 | Ethernet Speed LED | Ethernet magnetics provided by module | Ethernet Link LED | Ethernet Recieve + | Ethernet Receive - | Ethernet Transmit + | Ethernet Transmit - | Input power 3.3 VDC | Ground | External Interrupt 3 or SPI 0 Chip Select 3 or USB Host VBUS Enable | External Interrupt 2 or SPI 0 Chip Select 2 or USB Host VBUS Over-Current | USB On-the-Go D- output | USB On-the-Go D+ output |
| | General Purnose I/O | PF4 | PD2/RGPI01 | PF3 | PB2 | PF5/RGPIO6 | PB1 | PF6/RGPI05 | PG5 | PEO | PG6 | PF7 | PG7 | PE1/RGPI08 | PF0 | PE2/RGPI07 | PF1 | | | | | | | | | | PC3 | PC2 | | |
| | Fuction 3 | SPI2_SIN | USBO_VBUS_EN | SPI2_SOUT | CAN0_TX | SPI2_PCS0 | CAN0_RX | SPI2_SCK | SPI1_SCK | SPI3_SIN | SPI1_SIN | SPI3_SOUT | SPI1_SOUT | SPI3_CS0 | SPI1_CS1 | SPI3_SCK | SPI1_CS2 | | | | | | | | | | USBH_VBUS_EN | USBH_VBUS_OC | | |
| | Function 2 | I2C4_SDA | T30UT | I2C4_SCL | UART8_TX | UART4_RX | UART8_RX | UART4_TX | PWM_A0 | I2C5_SDA | PWM_B0 | I2C5_SCL | PWM_B2 | UART5_RX | PWM_A2 | UART5_TX | PWM_B1 | | | | | | | | | | SPI0_CS3 | SPI0_CS2 | | |
| | Function 1 | UART0_RX | NIST | UARTO_TX | I2C0_SCL | UARTO_RTS | I2C0_SDA | UARTO_CTS | SDHC_CLK | UART1_RX | SDHC_CMD | UART1_TX | SDHC_DAT0 | UART1_RTS | SDHC_DAT1 | UART1_CTS | SDHC_DAT2 | SPEEDLED | VCC3VE | LINKLED | ERX_JP | ERX_JM | ETX_JP | ETX_JM | VCC3V | GND | <u>IRQ3</u> | IRQ2 | USBO_DM | USBO_DP |
| | CPU | B10 | G13 | D11 | G15 | B11 | G14 | E13 | A10 | 60 | 5 | 60 | B12 | D10 | D12 | C10 | E14 | | | | | | | | | | Σ | M2 | A14 | B14 |
| | Pin | 24 | 25 | 26 | 27 | 28 | 59 | 30 | 31 | 32 | 33 | 34 | 35 | 36 | 37 | 38 | 39 | 40 | 41 | 42 | 43 | 44 | 45 | 46 | 47 | 48 | 49 | 20 | 51 | 52 |

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