

Computer Science PhD Student
National University of Singapore (NUS)

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Systems Security, Machine Learning Privacy and Security, Hardware/Software Co-design, Optimizing Compilers

AUGUST 2019 - Present Doctor of Philosophy in Computer Science
National University of Singapore (NUS), Singapore
 Advisor: Dr. Trevor E. CARLSON

2014 - 2019 Bachelor of Science in Computer Engineering
Sharif University of Technology, Tehran, Iran
 Thesis: “High Concurrency Latency Tolerant Register Files for GPUs”
 Advisor: Prof. Hamid SARBAZI-AZAD

2009 - 2013 Diploma in Physics and Mathematics Discipline
Shahid Beheshti High School, Birjand, Iran
Affiliated with the National Organization for the Development of Exceptional Talents (NODET)

AUGUST 2019 - PRESENT	<p>Graduate Research Assistant at NATIONAL UNIVERSITY OF SINGAPORE, Singapore <i>NUS Computer Architecture Group</i> <i>Supervisor: Prof. Trevor E. CARLSON</i></p> <p>My current research spans around HW/SW co-design to build secure and efficient general-purpose processors. My focus is on mitigating Speculative Execution attacks and Power Analysis attacks.</p>
JULY 2016 - JUNE 2019	<p>Research Assistant at SHARIF UNIVERSITY OF TECHNOLOGY, Tehran <i>High Performance Computer Architectures and Networks (HPCAN) Lab</i> <i>Supervisor: Prof. Hamid SARBAZI-AZAD</i></p> <p>Focus of my research has been on latency tolerant register files for GPUs through HW/SW cooperative register prefetching. I contributed to an ASPLOS paper (acknowledged) and an ACM TOCS paper. In collaboration with <i>Institute for Research in Fundamental Sciences, EPFL</i>, and <i>ETH Zürich</i>.</p>
SUMMER 2018	<p>Research Intern at NATIONAL UNIVERSITY OF SINGAPORE (NUS) <i>Supervisor: Prof. Trevor E. CARLSON</i></p> <p>As a visiting research assistant, I investigated the potentials of out-of-order commit and how to implement an efficient system to enable out-of-order commit.</p>

SPRING 2020 & SPRING 2021 **Teaching Assistant**, NATIONAL UNIVERSITY OF SINGAPORE, Singapore
Course: CS2106 Introduction to Operating Systems
Instructor: Prof. Djordje JEVDJIC

PUBLICATIONS

- [1] **Ali Hajiabadi**, Andreas Diavastos, Trevor E. Carlson
NOREBA: A Compiler-Informed Non-speculative Out-of-Order Commit Processor. Proceedings of 26th International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS 2021). April 2021. Acceptance rate: 75/398 = 18.8%
[\[Paper\]](#)[\[Extended Abstract\]](#)[\[Short Slides\]](#)[\[Short Talk\]](#)[\[Slides\]](#)[\[Full Talk\]](#)
- [2] Harish Patil, Alexander Isaev, Wim Heirman, Alen Sabu, **Ali Hajiabadi**, Trevor E. Carlson
ELFies: Executable Region Checkpoints for Performance Analysis and Simulation. Proceedings of 19th International Symposium on Code Generation and Optimization (CGO 2021), March 2021. Acceptance rate: 31/89 = 34.8%
[\[Paper\]](#)
- [3] Mohammad Sadrosadati, Amirhossein Mirhosseini, **Ali Hajiabadi**, Seyed Borna Ehsani, Hajar Falahati, Hamid Sarbazi-Azad, Mario Drumond, Babak Falsafi, Rachata Ausavarungnirun, Onur Mutlu
Highly Concurrent Latency-tolerant Register Files for GPUs. In ACM Transactions on Computer Systems (TOCS), 2021.
[\[arXiv Paper\]](#)
- [4] Yun Chen*, **Ali Hajiabadi***, Romain Poussier, Andreas Diavastos, Shivam Bhasin, Trevor E. Carlson
Mitigating Power Attacks through Fine-Grained Instruction Reordering. arXiv (unpublished), 2021.
* Authors with equal contribution.
[\[arXiv Paper\]](#)

HONORS & AWARDS

AUGUST 2021	Recipient of RESEARCH ACHIEVEMENT AWARD from School of Computing, NUS.
MARCH 2020	Invited talk and travel grant for the 2 nd Young Architect Workshop at ASPLOS'20, Switzerland.
FEBRUARY 2019	Recipient of PRESIDENT'S GRADUATE FELLOWSHIP, the most prestigious doctoral fellowship at National University of Singapore (NUS).
SEPTEMBER 2014	Ranked 164 th in Iranian National University Entrance Exam among more than 250,000 students.
2006 & 2009	Recognized as talented student in entry exam of NODET among Birjand students for middle school and high school.

TALKS

AUGUST 2021	NOREBA: A Compiler-Informed Non-speculative Out-of-Order Commit Processor <i>Computing Research Week, School of Computing (NUS)</i> , Virtual.
APRIL 2021	NOREBA: A Compiler-Informed Non-speculative Out-of-Order Commit Processor <i>International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS 2021)</i> , Virtual.
FEBRUARY 2021	Accelerating HPC applications with Out-of-Order Commit Processors <i>Free and Open source Software Developers' European Meeting (FOSDEM 2021)</i> , HPC, Big Data, and Data Science track, Virtual.
MARCH 2020	Speculation-Free Out-of-Order Commit <i>2nd Young Architect Workshop at the 25th International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS 2020)</i> , Virtual.

SERVICES

JUNE 2021	Student Volunteer at 42 nd International Conference on Programming Language Design and Implementation (PLDI 2021), Virtual.
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SKILLS

PROGRAMMING LANGUAGES:	C/C++, Python, and familiar with Java, Scala, Matlab
SCIENTIFIC TOOLS:	LLVM Compiler Infrastructure, gem5 Simulator, Sniper Simulator, GPGPU-Sim, BookSim, GPU-Ocelot, Pin
OPERATING SYSTEMS:	Linux, Mac OS, Windows
TYPESETTING:	L ^A T _E X, Microsoft Word