

Ali HAJIABADI

Computer Science PhD Candidate
National University of Singapore (NUS)

CONTACT INFORMATION

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RESEARCH INTERESTS

Systems Security, Hardware/Software Co-design, Computer Architecture, Optimizing Compilers, Secure Architectures and Software, Microarchitectural Attacks, Machine Learning Security and Privacy

EDUCATION

- AUG. 2019 - Present Doctor of Philosophy in Computer Science
National University of Singapore (NUS), Singapore
Thesis: *“Non-speculative and Non-deterministic Processing for Efficient and Secure Modern CPUs”*
Advisor: Dr. Trevor E. CARLSON
- 2014 - 2019 Bachelor of Science in Computer Engineering
Sharif University of Technology, Tehran, Iran
Thesis: *“High Concurrency Latency Tolerant Register Files for GPUs”*
Advisor: Prof. Hamid SARBAZI-AZAD
- 2009 - 2013 Diploma in Physics and Mathematics Discipline
Shahid Beheshti High School, Birjand, Iran
Affiliated with the National Organization for the Development of Exceptional Talents (NODET)

HONORS & AWARDS

- JAN. 2022 Recipient of STUDENT TRAVEL AWARD from ASPLOS’22 conference.
- AUG. 2021 Recipient of RESEARCH ACHIEVEMENT AWARD from School of Computing, NUS.
- MAR. 2020 Invited talk and travel grant for the 2nd Young Architect Workshop at ASPLOS’20, Switzerland.
- FEB. 2019 Recipient of PRESIDENT’S GRADUATE FELLOWSHIP, the most prestigious doctoral fellowship at National University of Singapore (NUS).
- SEP. 2014 Ranked 164th in Iranian National University Entrance Exam among more than 250,000 students.
- 2006/2009 Recognized as talented student in entry exam of NODET for middle school and high school.

RESEARCH EXPERIENCE

- AUG. 2019 - PRESENT Graduate Research Assistant at NATIONAL UNIVERSITY OF SINGAPORE, Singapore
NUS Computer Architecture Group
Advisor: Prof. Trevor E. CARLSON
My current research spans around HW/SW co-design to build secure and efficient general-purpose processors. My focus is on microarchitectural attacks, including speculation-based attacks and power analysis attacks.
- JUL. 2016 - JUN. 2019 Research Assistant at SHARIF UNIVERSITY OF TECHNOLOGY, Tehran, Iran
High Performance Computer Architectures and Networks (HPCAN) Lab
Advisor: Prof. Hamid SARBAZI-AZAD
Focus of my research has been on latency tolerant register files for GPUs through HW/SW cooperative register prefetching. I contributed to an [ASPLOS’18 paper](#) and an ACM TOCS paper. In collaboration with *Institute for Research in Fundamental Sciences, EPFL*, and *ETH Zürich*.
- SUMMER 2018 Research Intern at NATIONAL UNIVERSITY OF SINGAPORE, Singapore
Advisor: Prof. Trevor E. CARLSON
As a visiting research assistant, I investigated the potentials of out-of-order commit in modern processors and explored implementations (simulation+compiler) to enable out-of-order commit.

PEER-REVIEWED PUBLICATIONS

- ICCAD'23 | Arash Pashrashid, **Ali Hajiabadi**, Trevor E. Carlson
HIDFix: Efficient Mitigation of Cache-based Spectre Attacks through Hidden Rollbacks.
To appear in Proceedings of 42nd IEEE/ACM International Conference on Computer-Aided Design (ICCAD 2023), November 2023. Acceptance rate: 172/768 = 22.4%
- ICCAD'22 | Arash Pashrashid, **Ali Hajiabadi**, Trevor E. Carlson
Fast, Robust and Accurate Detection of Cache-based Spectre Attack Phases.
Proceedings of 41st IEEE/ACM International Conference on Computer-Aided Design (ICCAD 2022), November 2022. Acceptance rate: 132/586 = 22.5%
[Paper](#) | [Github Project](#)
- ASPLOS'21 | **Ali Hajiabadi**, Andreas Diavastos, Trevor E. Carlson
NOREBA: A Compiler-Informed Non-speculative Out-of-Order Commit Processor.
Proceedings of 26th ACM International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS 2021). April 2021. Acceptance rate: 75/398 = 18.8%
[Paper](#) | [Extended Abstract](#) | [Short Slides](#) | [Short Talk](#) | [Slides](#) | [Full Talk](#)
- TOCS'21 | Mohammad Sadrosadati, Amirhossein Mirhosseini, **Ali Hajiabadi**, Seyed Borna Ehsani, Hajar Falahati, Hamid Sarbazi-Azad, Mario Drumond, Babak Falsafi, Rachata Ausavarungnirun, Onur Mutlu
Highly Concurrent Latency-tolerant Register Files for GPUs.
In ACM Transactions on Computer Systems (TOCS), 2021.
[arXiv Paper](#)
- CGO'21 | Harish Patil, Alexander Isaev, Wim Heirman, Alen Sabu, **Ali Hajiabadi**, Trevor E. Carlson
ELFies: Executable Region Checkpoints for Performance Analysis and Simulation.
Proceedings of 19th IEEE International Symposium on Code Generation and Optimization (CGO 2021), March 2021. Acceptance rate: 31/89 = 34.8%
[Paper](#)

IN-PROGRESS WORK

- Ali Hajiabadi**, Archit Agarwal, Andreas Diavastos, Trevor E. Carlson
Mitigating Speculation-based Attacks through Configurable Hardware/Software Co-design.
[arXiv Paper](#), 2023
- Yun Chen, **Ali Hajiabadi**, Lingfeng Pei, Trevor E. Carlson
New Cross-Core Cache-Agnostic and Prefetcher-based Side-Channels and Covert-Channels.
[arXiv Paper](#), 2023
- Yun Chen*, **Ali Hajiabadi***, Romain Poussier, Andreas Diavastos, Shivam Bhasin, Trevor E. Carlson
Mitigating Power Attacks through Fine-Grained Instruction Reordering.
*Authors with equal contribution.
[arXiv Paper](#), 2021

TEACHING EXPERIENCE

➤ National University of Singapore, Singapore

- SPRING 2020 and SPRING 2021 | **Teaching Assistant**, Tutorial Instructor
Course: CS2106 Introduction to Operating Systems
Instructor: Prof. Djordje Jevdjic

➤ Sharif University of Technology, Tehran, Iran

- SPRING 2017 | **Teaching Assistant**, Assignments/Projects Assistant
Course: CE323 Computer Architecture
Instructor: Prof. Hamid Sarbazi-Azad
- FALL 2017 and FALL 2018 | **Teaching Assistant**, Tutorial Instructor, Assignments/Projects Assistant
Course: CE453 Real-Time Systems
Instructor: Prof. Amirhossein Jahangir

SERVICES

- OCT. 2022 **Shadow PC member** at 18th European Conference on Computer Systems (EuroSys 2023), Rome.
- MAR. 2022 **Mentor in the Meet-a-Senior-Student program** at 27th International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS 2022), Lausanne.
- JUN. 2021 **Student Volunteer** at 42nd International Conference on Programming Language Design and Implementation (PLDI 2021), Virtual.

RESEARCH MENTORING

- 2021 - PRESENT Arash Pashrashid, PhD Student Advised by Trevor E. Carlson
- 2020 - PRESENT Yun Chen, PhD Student Advised by Trevor E. Carlson
- 2021 - 2023 Archit Agarwal, Research Assistant at NUS
- 2020 - 2021 Vernon Pang, Undergraduate Student at NUS

TALKS

- AUG. 2021 **NOREBA: A Compiler-Informed Non-speculative Out-of-Order Commit Processor**
Computing Research Week, School of Computing (NUS), Virtual.
- APR. 2021 **NOREBA: A Compiler-Informed Non-speculative Out-of-Order Commit Processor**
International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS 2021), Virtual.
- FEB. 2021 **Accelerating HPC applications with Out-of-Order Commit Processors**
Free and Open source Software Developers' European Meeting (FOSDEM 2021), HPC, Big Data, and Data Science track, Virtual.
- MAR. 2020 **Speculation-Free Out-of-Order Commit**
2nd Young Architect Workshop at the 25th International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS 2020), Virtual.

SKILLS

- PROGRAMMING LANGUAGES: C, C++, Python, bash, and familiar with Java, Matlab, Scala
- INSTRUCTION SET ARCHITECTURES: x86, ARM, RISC-V
- SCIENTIFIC TOOLS: LLVM Compiler Infrastructure, gem5 Simulator, Sniper Simulator, Intel Pin, DynamoRIO
- OPERATING SYSTEMS: Linux, Mac OS, Windows
- TYPESETTING: \LaTeX , Microsoft Word