

13th Feb, 2024

lecture 1

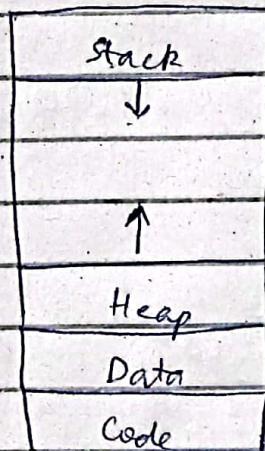
Process ?

→ Steps

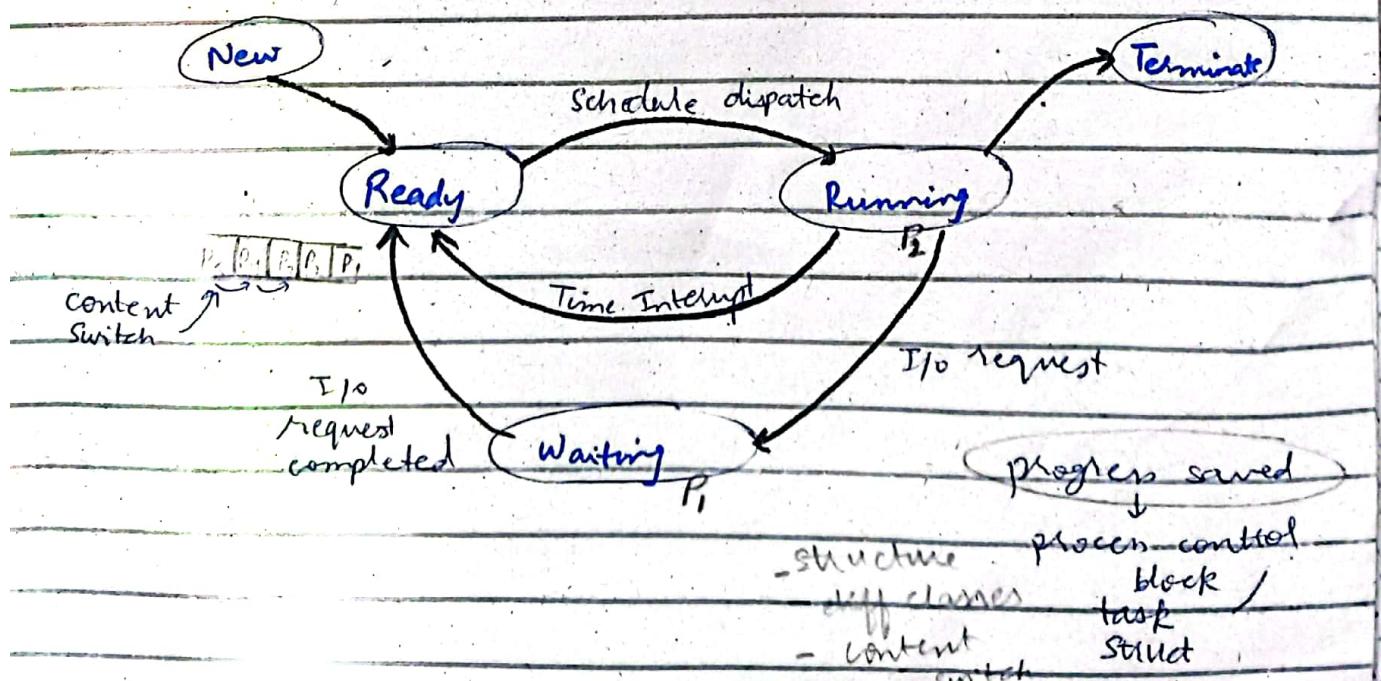
Chapter 3 Process

15th Feb, 2024

lecture 2



Process State Diagram



21st Feb, 2024

lecture 3

Fork()?
for creating
process

void main()

{

int pid, n, y;

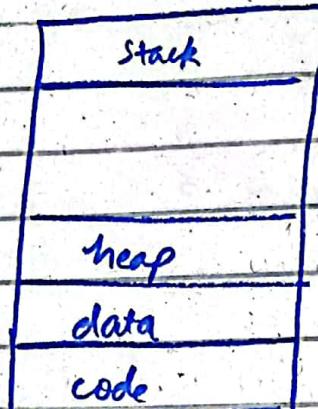
pid = fork();

printf("pid = ", pid);

}

P - pid = 3276

C - pid = 0



wait(NULL);

↳ child 1st

Parent 2nd

a, y, a+y

enclp

Process Scheduling
Queues

Scheduler

Inter Process Communication

Ways for Inter Process communication:

- Pipes.

- Named pipe ✓

- Shared memory (fastest)

- Msg queues ✓

- Sockets. (fast)

→ logical identifier

Stack
heap
data
code

```
int main() [combo : ipaddress &
{                                port #]
```

```
int n = 10, y = 0, pid;
```

```
pid = fork();
```

```
if (pid == 0)
```

```
{
```

```
n = 20;
```

```
y = y + 1;
```

```
}
```

```
else
```

```
{
```

```
Wait(NULL);
```

```
n = n + 10;
```

```
y = y + 1;
```

```
}
```

```
cout << "n = " << n << endl;
```

```
cout << "y = " << y << endl;
```

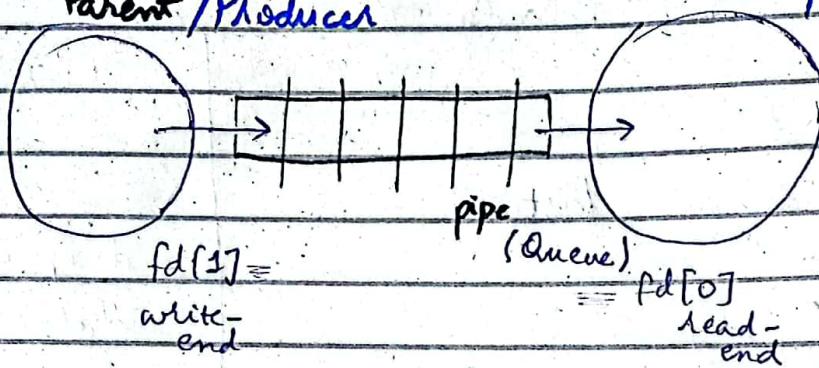
```
}
```

Pipe (X duplicate)

(descriptive of pipes ✓ duplicate)

Parent / Producer

Child / Consumer



file descriptor

↳ fd

int fd[2];

int (pid == 0)
{

char buffer [10];

Read (fd[0], &buffer,
Size of &buffer);

pipe (fd);

→ Write (fd[1],

buffer, ^{size} of buffer)

→ Read (fd[0],
&buffer, ^{size} of &buffer)

}

else

{

char buffer [10];

Write (fd[1], buffer,
Size of buffer);

}

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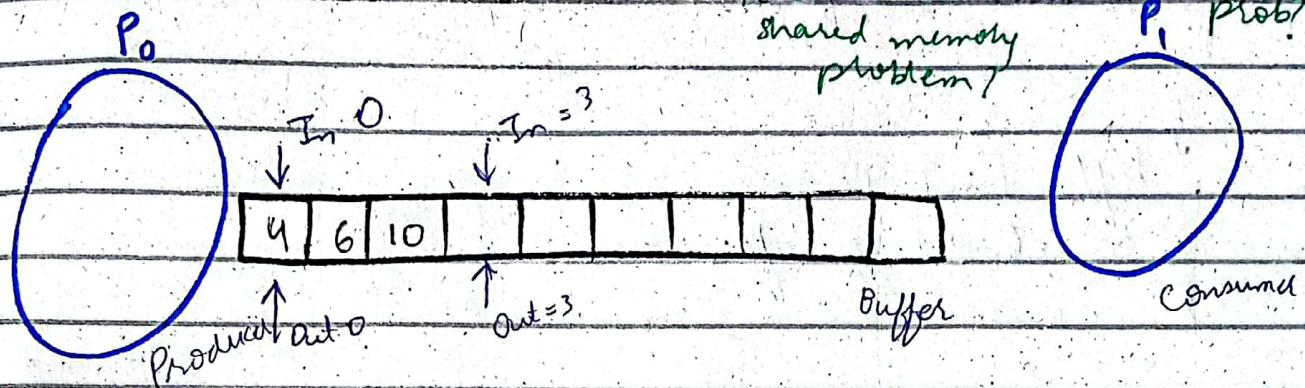
lecture 5

Producers - Consumer Problem

What is consumer

How to solve producer
shared memory
problem?

P₁ Prob?



```
while (In == out)  
{
```

// busy loop

}

pointing to buffer [in] = item
item to in = in + 1
be produced buffer [in] = item

in = in + 1

item = buffer [out]

out = out + 1

in = (in + 1) % buffersize

→ queue ended / circular queue

```
While ((in + 1) % buffersize == out)  
{
```

// busy loop

}

while (1)

{

 while ($\lfloor \text{in} + 1 \rfloor \% \text{buffersize} == \text{out}$)

{

}

 buffer [$\text{in}] = \text{item}$

$\text{in} = (\text{in} + 1) \% \text{buffersize}$

}

In

while (1)

{

 while ($\text{In} == \text{out}$)

{

}

$\text{item} = \text{buffer} [\text{out}]$

$\text{out} = \text{out} + 1$

}

Out

Shared Memory



29th Feb, 2024

lecture 6

1 queue must
from threads

Multithreading

Stack

heap

data

Code

Stack different
for every
thread.

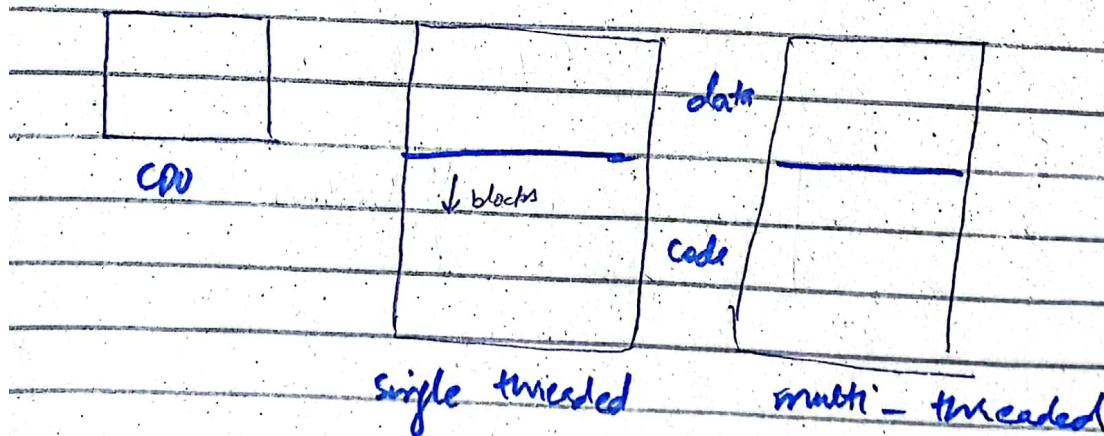
Why threads?

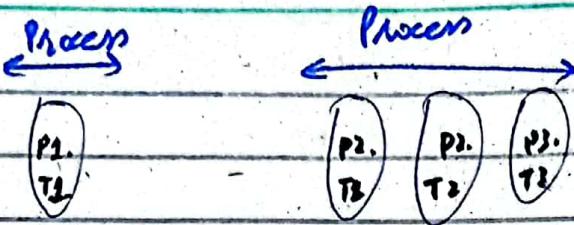
- easier.
- light weight.
- can be managed under single process.
- can be created easily.
- communication easy.
- no need for pipe (can be used but without
pipes easier to handle)

7th March, 2024

lecture 8

Threads and Thread usage





Schedulable Entities

We can select one of them & run.

function 1()

{

}

function 2()

{

}

main()

{

thread_create(function 1)

thread_create(function 2)

thread_create(function 2)

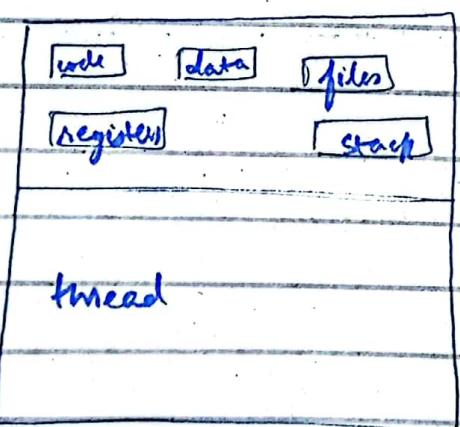
}

thread 1

thread 2

thread 3

thread

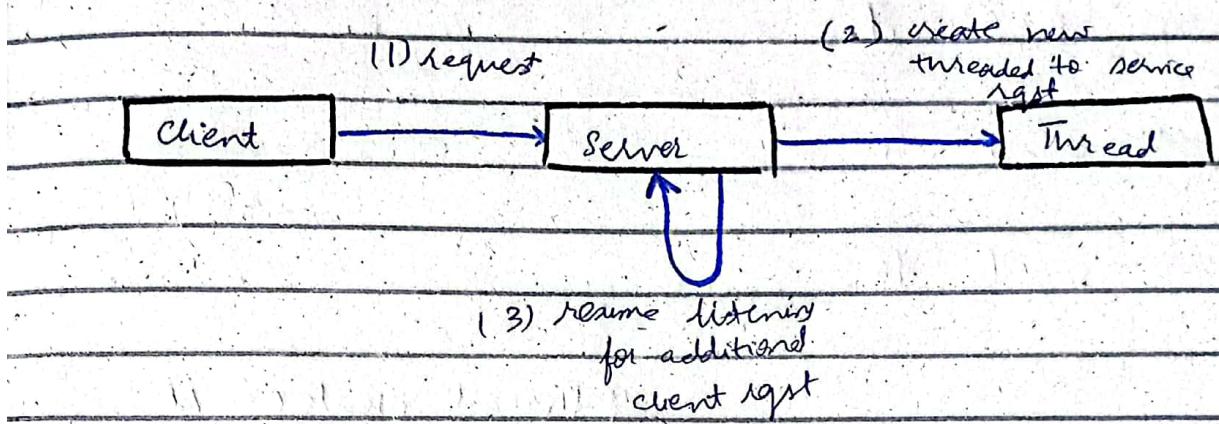


thread → separate stack

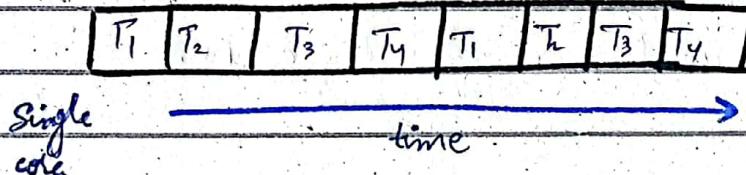
code, data, files, heap

shared
in multithreaded
process.

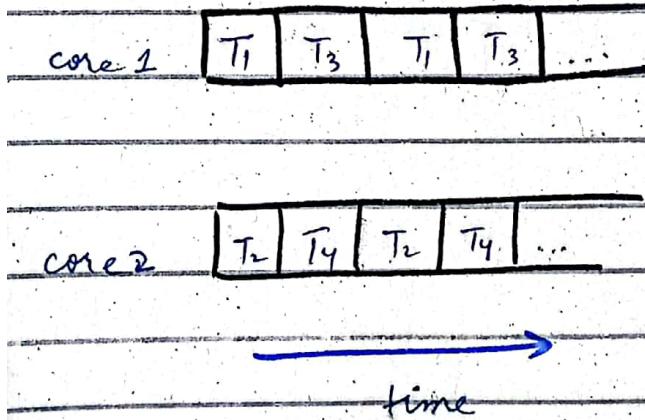
Multithreaded Server Architecture



Concurrent Execution on a single-core system



Multi-core



13th March, 2024 lecture 9

am [2 | 0 | 7]

sun [3 | 1 | 9]

am [5 | 1 | 16]

pthread_create(&tid 1,
NULL, thread 1, NULL);

pthread_join(tid 1, NULL);

Threading support

User level libraries

library creates and manages threads.

User level implementation

Kernel

kernel creates and manages threads

Kernel level implementation

- Many-to-one model. *
- One-to-one model.
- Many-to-Many model.

14th March, 2024 Lecture 10

Chapter 5 Synchronization

Threads / Program

```
Pthread_create(&tid1, NULL, int, NULL);  
Pthread_create(&tid2, NULL, dec, NULL);  
Pthread_join(tid1, NULL);  
Pthread_join(tid2, NULL);
```

void *inc(void *arg)

{

```
for (int n=0; n<1000; n++)  
    count = count + 1;
```

}

Running

in parallel

simultaneously

void *dec(void *arg)

{

```
for (int n=0; n<1000; n--)  
    count = count - 1;
```

}

21st March, 2024 lecture 12

Synchronization Hardware

→ Test and Set.

→ Swap

XCHG

Test and Set(bool *target)

{

bool rv = *target;

*target = TRUE;

return(rv);

}

lock

T..

false

do {



do {

while (Test&set (& lock))

{

//busy loop

}

U.C.S

lock = FALSE;

} while (1);

while (Test&set (& lock))

{

//busy loop entry section

}

U.C.S

lock = FALSE; exit section

} while (1);

Swap

Swap (bool *a, bool *b)

{

bool temp;

temp = *a;

*a = *b;

*b = temp;

}

T₀ a=1

T₁ T&S()

a=1

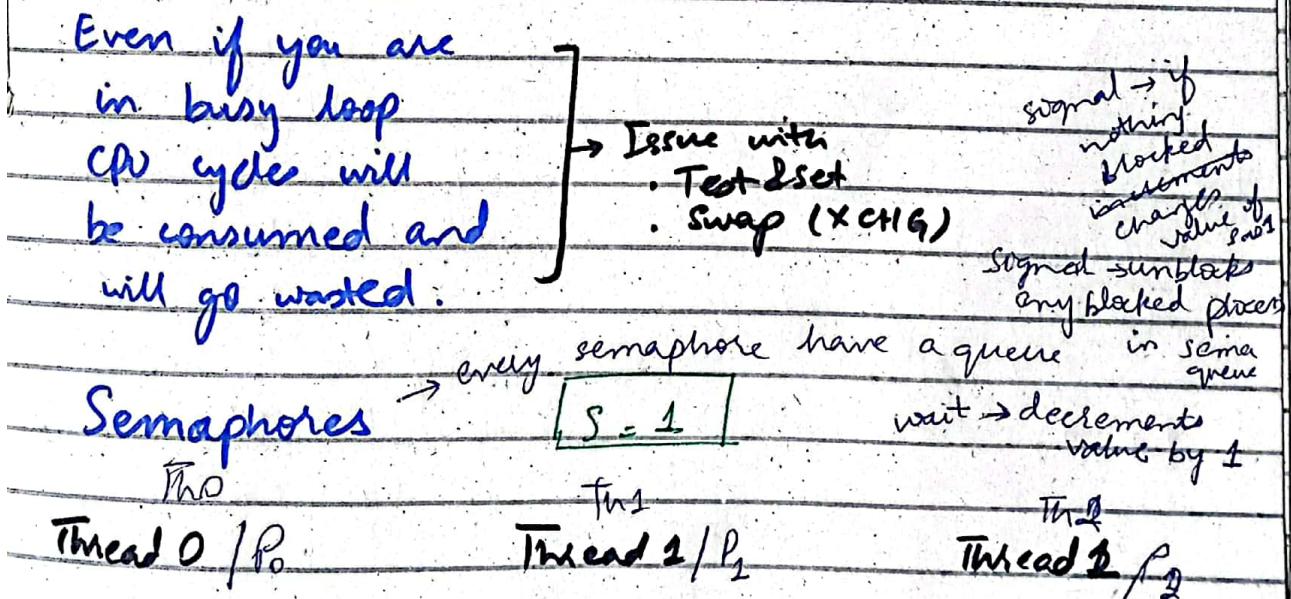
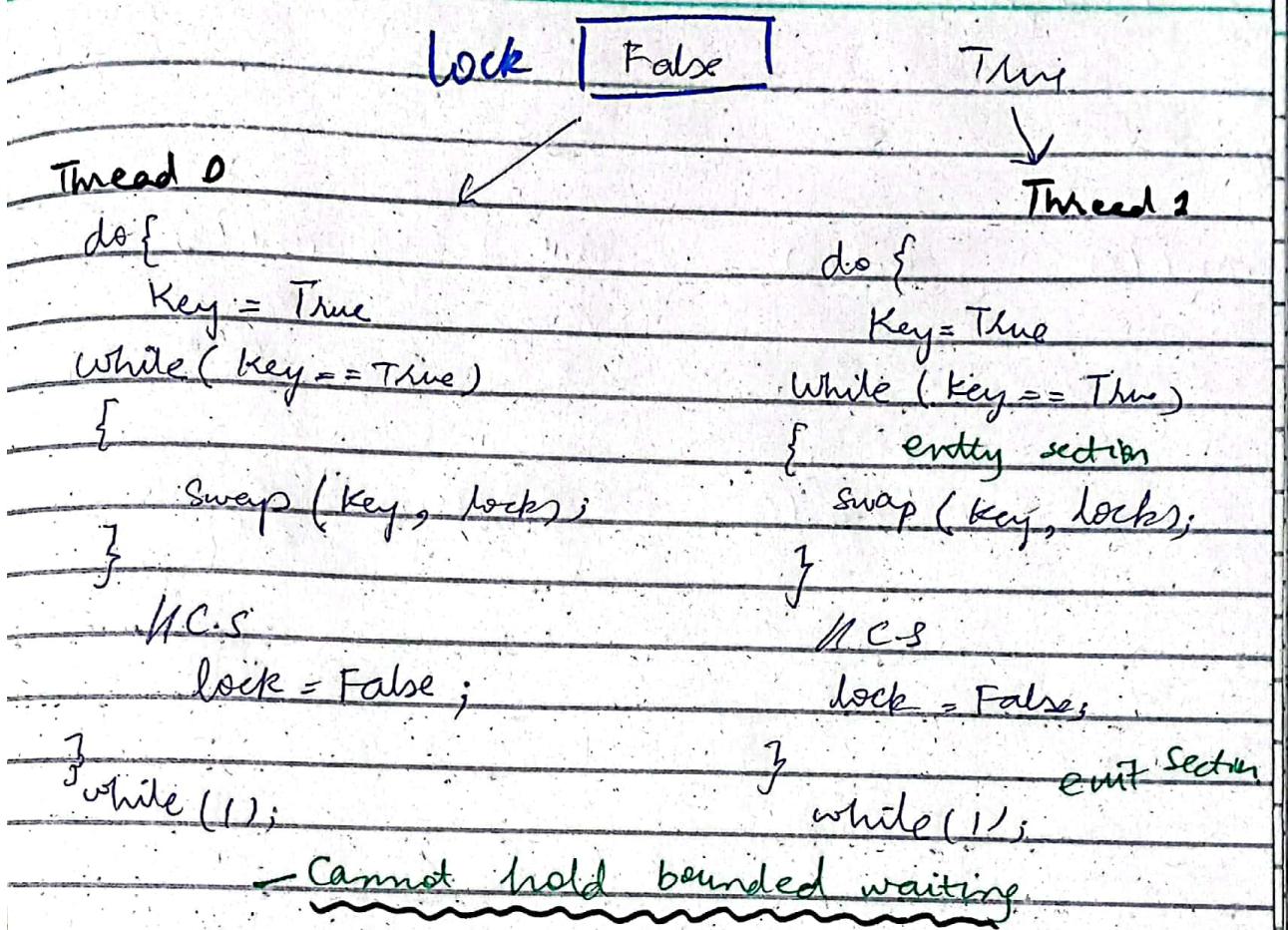
T&S()

a=1

T&S()

a=1

T&S()



wait (S);	wait (S);	wait (S);
// C.S.	// C.S.	// C.S.
Signal (S);	Signal (S);	Signal (S);

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Lecture 13

P₁

P₁

P₂

P₃

wait (S)

wait (S)

wait (S)

wait (S)

Access
Printer

Access
Printer

Access
Printer

Access
Printer

signal (S)

signal (S)

signal (S)

signal (S)

Writer 2

Reader ✓

→ any
Writer

Writer

DATA SET

Reader 1

come when

any reader

reading block

witch

→ when any
writer

writing don't

allow any

reader or

Semaphore Mutex = 1 int readcount = 0 writer

wrt = 1

do {

 wait (wrt);

 // writing is performed

 signal (wrt);

} while (1);

↓ writer

do {

 wait (Mutex);

 Read count ++;

 if (read count == 1)

 wait (wrt);

 signal (mutex);

 reader // reading performed

 wait (Mutex);

 readcount --;

 if (read count == 0)

 signal (wrt);

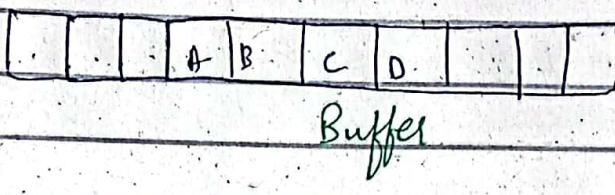
 signal (Mutex);

} while (1);

Bounded buffer problem

Empty = 6

Full = 4

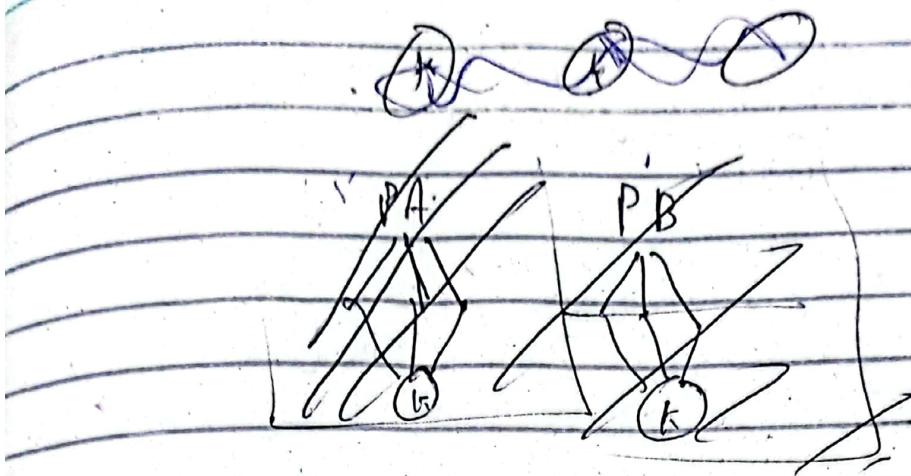


Producer thread

```
do {  
    wait (empty);  
    wait (Muten);  
    //  
    signal (Mutent);  
    signal (Full);  
} while (1)
```

Consumer thread

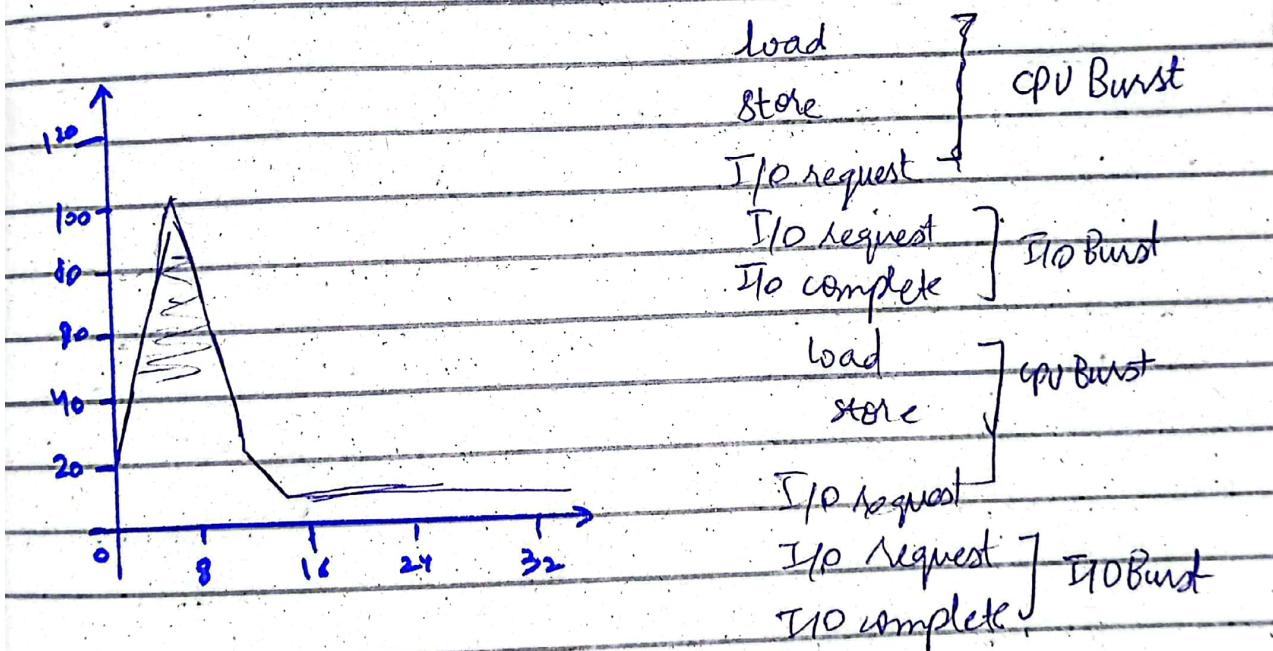
```
do {  
    wait (full);  
    wait (Muten);  
    //  
    signal (Mutent);  
    signal (empty);  
} while (1)
```

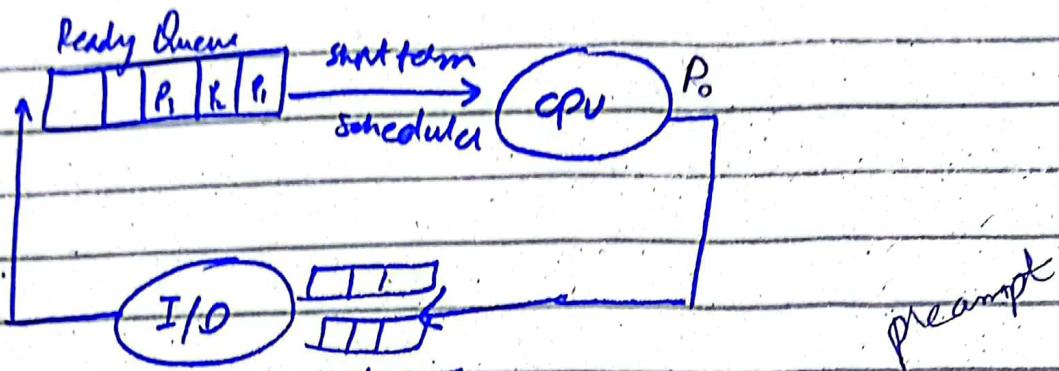


17th April, 2024

Lecture 15

Chapter 6 "CPU Scheduling"



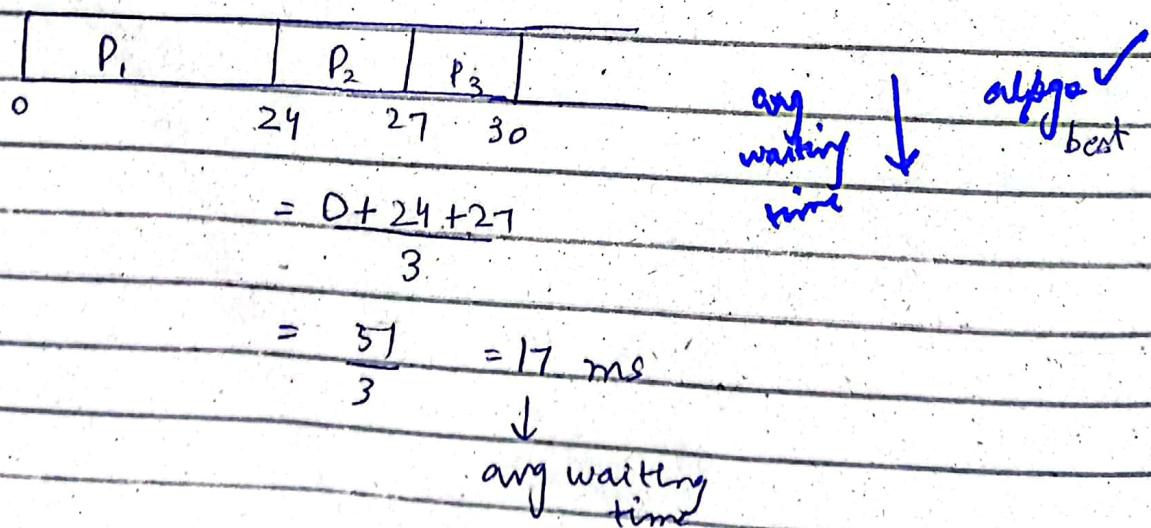


- ③ Running to Ready state
- ① Running to waiting state
Scheduler needs to take decision
- ② Waiting to Ready state
needs b/c of (to take decisions) priority

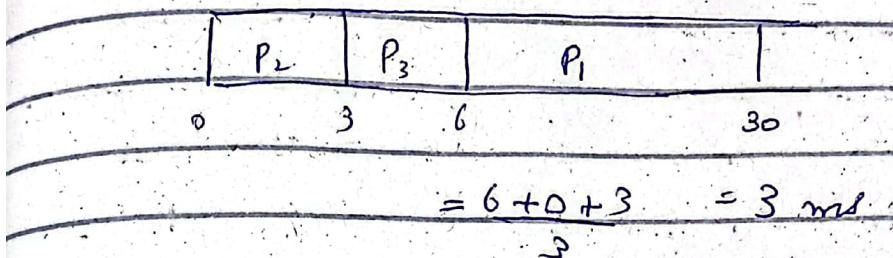
FCFS (First come First Served) scheme

Process CPU Burst

P ₁	24	time
P ₂	3	
P ₃	3	



SJF (Shortest Job First)

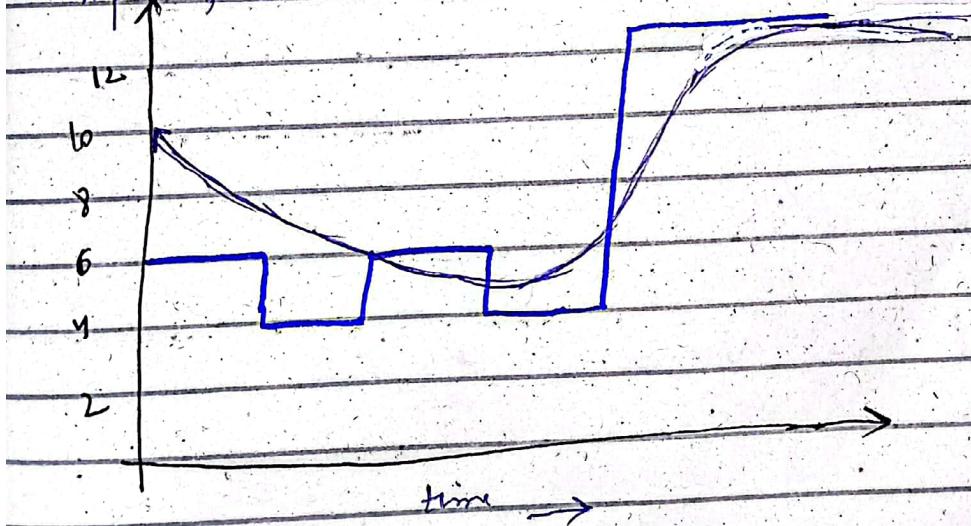


Exponential averaging
(to predict next CPU
Burst of Process).

$$\underbrace{T_{n+1}}_{\text{Predicted value}} = \alpha \underbrace{T_n}_{\text{current CPU burst}} + (1 - \alpha) \underbrace{T_n}_{\text{History values}}$$

18th April, 2024

Lecture 16



$$= 0.5 \times 6 + (0.5) \times 10 \\ = 3 + 5$$

$$T_{n+1} = 8$$

$$T'_{n+2} = 0.5 \times 4 + (0.5) \times 8 \\ = 2 + 4$$

$$T'_{n+1} = 6$$

$$T_{n+3} = 0.5 \times 6 + 0.5 \times 6 \\ = 3 + 3 = 6$$

$$T_{n+4} = 0.5 \times 4 + 0.5 \times 6 \\ = 2 + 3 = 5$$

$$T_{n+5} = 0.5 \times 13 + 0.5 \times 5 \\ = 6.5 + 2.5 \\ = 9$$

Exponential Averaging

SRJF (Shortest Remaining Job First)

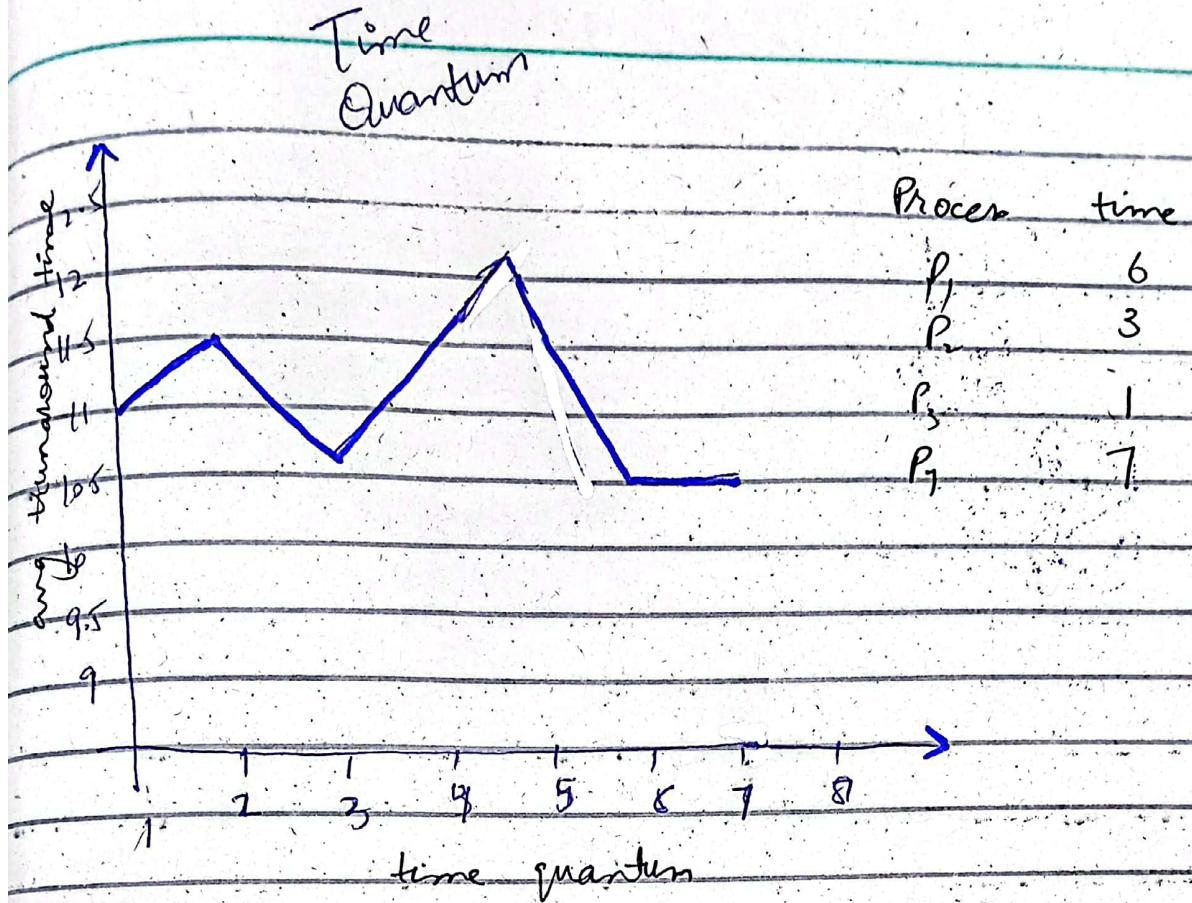
Priority Scheduling

Round Robin (RR)

- CPU scheduling is based on RR.
- Time Quantum given to RR by CPU
- q^{\leftarrow} Large = FIFO
- q^{\leftarrow} small =

Context Switch Time

Turnaround time varies with the Time Quantum



$Q=1$	P ₁	P ₂	P ₃	P ₄	P ₁	P ₂	P ₃	P ₄	P ₁	P ₂	P ₃
	0	1	2	3	4	5	6	7	8	9	10

P ₁	P ₄	P ₁	P ₂	P ₃
13	11	15	16	17

$$Q_s = 2$$

P ₁	P ₂	P ₃	P ₄	P ₁	P ₂	P ₃	P ₄	P ₁
0	2	4	5	7	9	10	12	14

$$P_1 = (15 - 0) = 15$$

$$= \frac{15 + 9 + 3 + 17}{4}$$

$$= \frac{44}{4} = 11.0$$

$$P_2 = (9 - 0) = 9$$

$$P_3 = (3 - 0) = 3$$

$$P_4 = (17 - 0) = 17$$