

MIDDLE EAST TECHNICAL UNIVERSITY

EE464 POWER ELECTRONICS



*”Turning Coffee into Electricity”*

PROJECT 2 REPORT

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## 1 Introduction

As Fiero Converters, we manufacture 230 V AC to 9 V DC, 10 W Flyback Converters. This documents shows the detailed pre-manufacturing process of designing, simulating of the converter and the controller of the product. The design to be used in the simulations is provided in figure 1.

## 2 Isolated Converter Simulation

The design is made using []. There is snubber in order to reduce the current and voltage stress the MOSFET endures during operation. The resultant effect of the snubber is clearly observed in the simulations as well. The simulations were done synchronously with the implementation and hence the design parameters for the switch, diode and the transformer are taken from the implemented products.

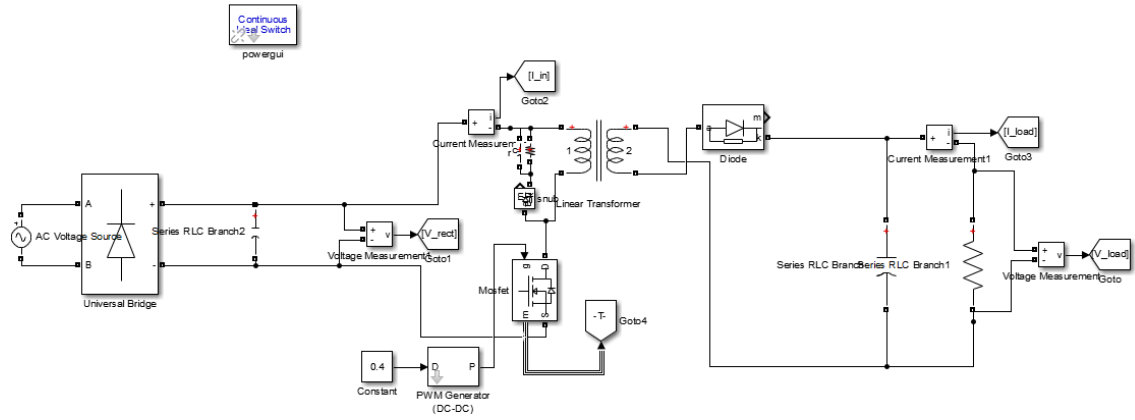


Figure 1: Flyback Design

## 2.1 Part I. Steady State Operation of the Converter

The figures 2, 3, 4, 5 show the simulation waveforms and the resultant voltage, current and power outputs for the calculated values of maximum duty cycle, turns ratio, reasonable capacitor values and resistance for the ideal case.

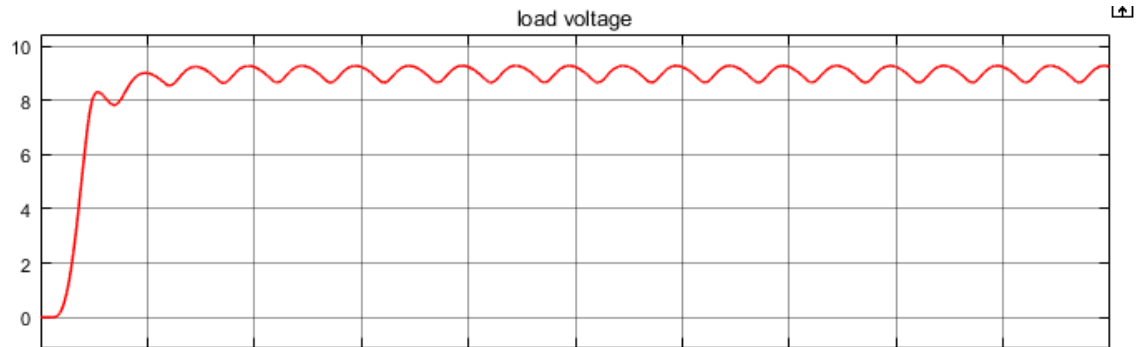


Figure 2: Load Steady State Voltage

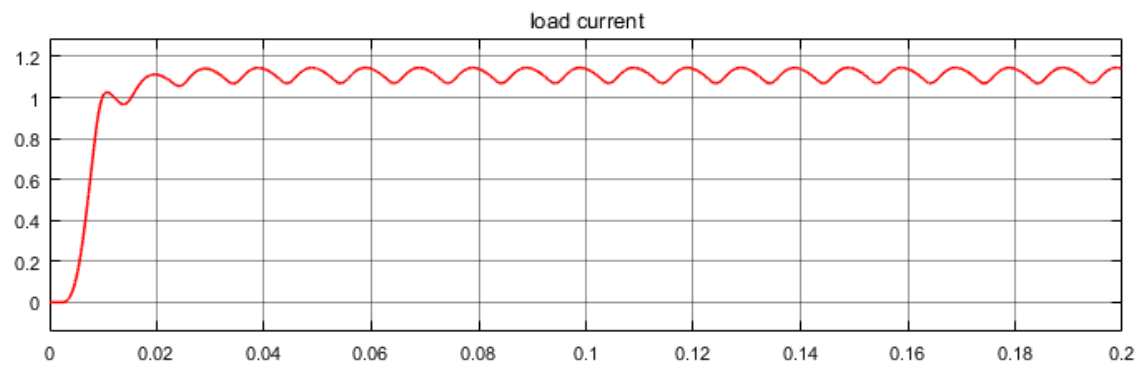


Figure 3: Load Steady State Current

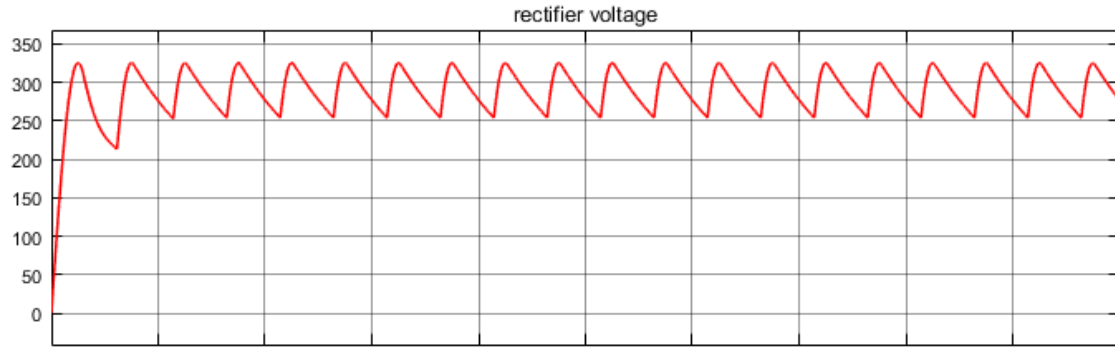


Figure 4: Rectifier Output Voltage at Steady State

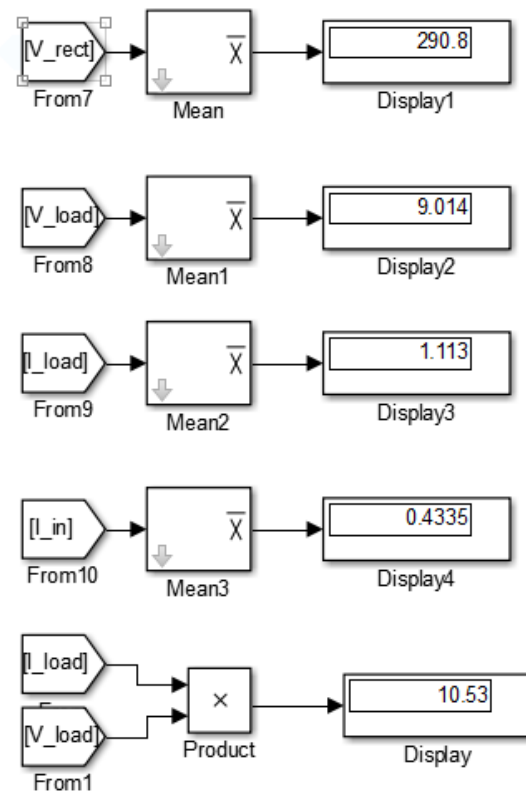


Figure 5: All voltage current display values

Moreover, we have also checked that the switch voltage and current remains in the tolerable range. As expected, the voltage across the switch will be twice the input voltage when it turns off. This can be seen in figure6.

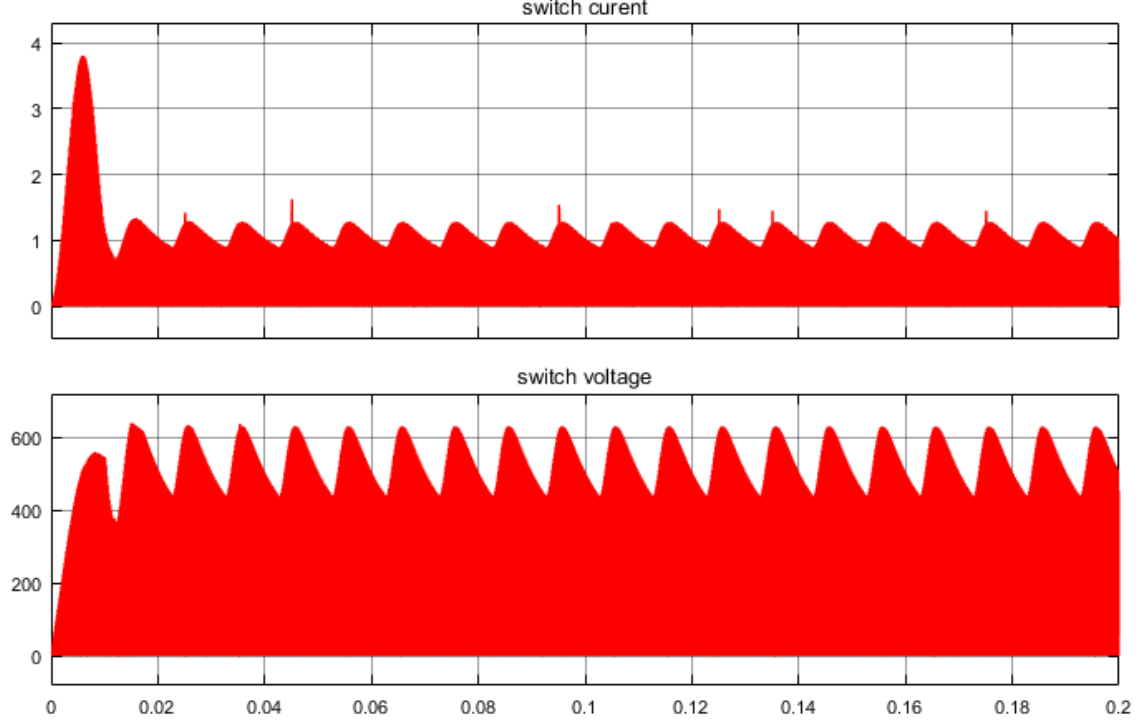


Figure 6: Current and voltage of the MOSFET during operation

## 2.2 Transformer Design

For flyback topology design, there are many different considerations like loss, area, saturation etc. We started the topology knowing that the duty cycle can not exceed 0.5. Following the steps in [?], we started by putting a safety margin to the duty cycle and selecting it to be 0.4. Once we have decided on this, using the flyback input output relation  $N_{sec}/N_{prim} \cdot D/(1 - D) \cdot V_{in} = V_{out}$ , since flyback is a buck boost topology, we found the turns ratio to be 19. Following this, we need the primary and secondary rms and average currents in order to decide on the core inductance and the exact turns ratio. We know that ideally we need around 1.2 A in secondary and .....

## 3 Controller Design

This part contains the desired control design for our flyback converter. The controller is chosen to be type 2 controller with a schematic shown in figure 7.

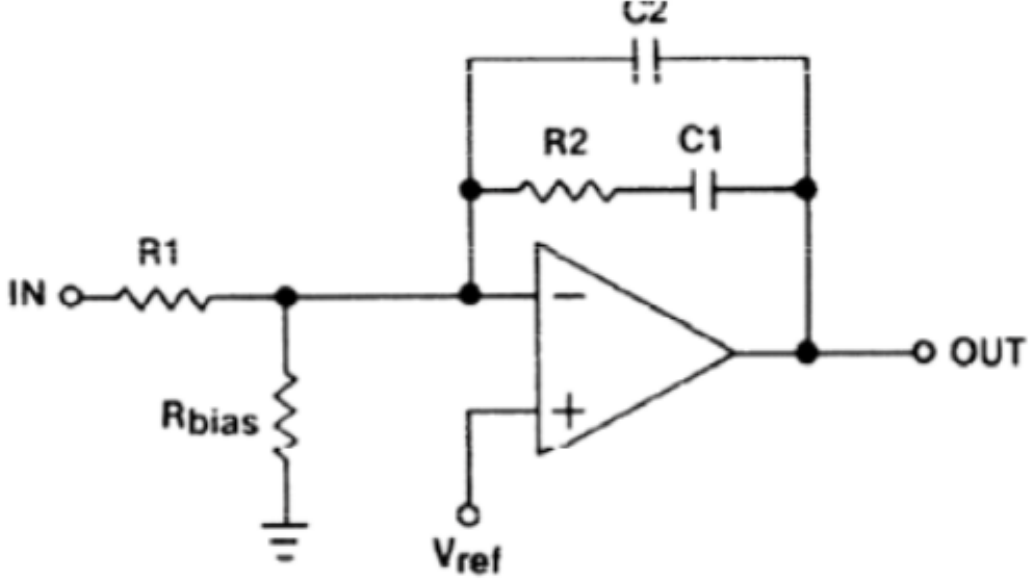


Figure 7: Type 2 Controller

### 3.1 Part 1. Controller Transfer Function

The transfer function for the controller is:

$$\tilde{v}_c / \tilde{v}_o = Z_{feedback} / Z_{input}$$

Consider that the input and output voltages contain DC and AC components, while the reference is DC. In order to derive the transfer function, consider the AC components:

$$Z_{feedback} = 1/sC_2 // (R_2 + 1/sC_1)$$

$$Z_{input} = R_1 R_{bias} / (R_1 + R_{bias})$$

This yields the following transfer function:

$$T_c(s) = \frac{R_1 + R_{bias}}{R_1 R_{bias}} \cdot \frac{1}{C_2} \cdot \frac{s + 1/(R_1 C_1)}{s(s + (C_1 + C_2)/R_2 C_1 C_2)}$$

Here, we observe that the controller is a compensator function. It is in our control to make it either a lead or lag compensator. For stability, we need an increase in the phase margin of our close loop system. Hence, we desire the controller to add a phase lead (boost). Therefore, we need to place a zero before a pole so that first an incline in the phase occurs. The general bode plot for the controller transfer function is provided

in figure 8, we see that the zero is placed before the pole.

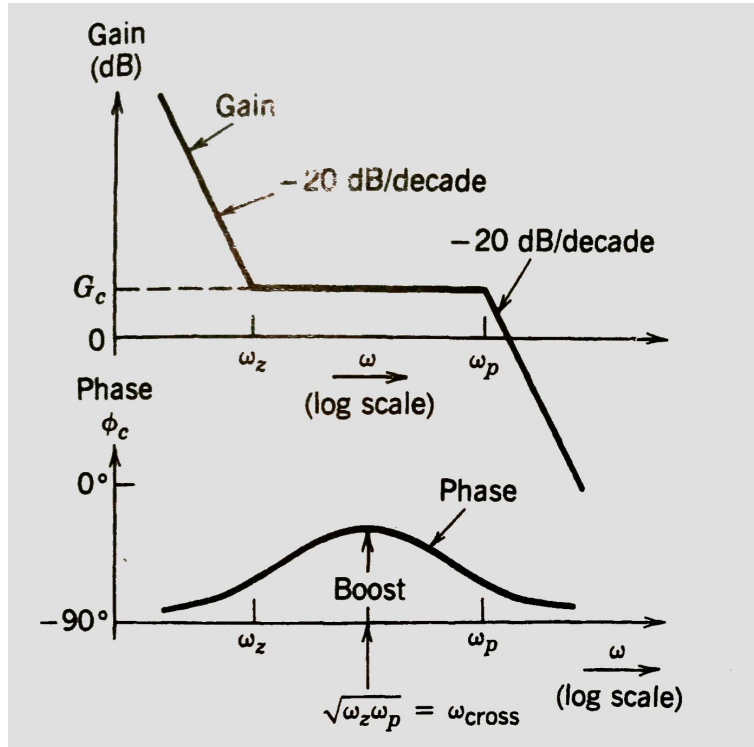


Figure 8: Type 2 Controller Bode Plot

## 3.2 Part 2. Controller Computer Simulation