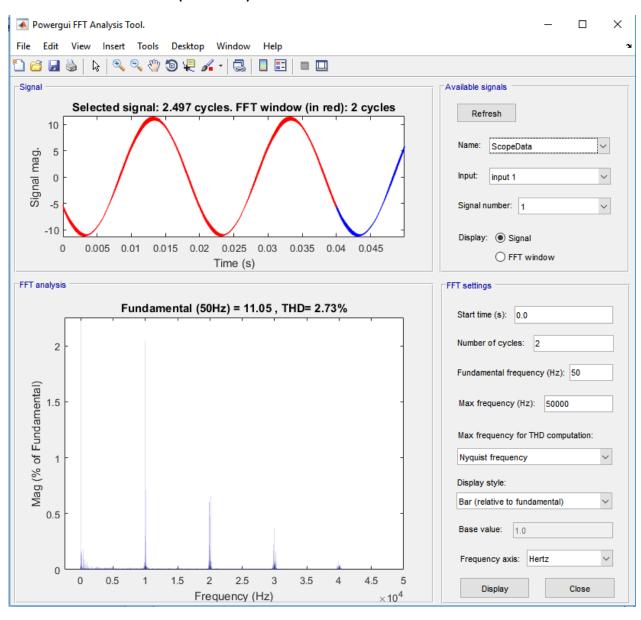
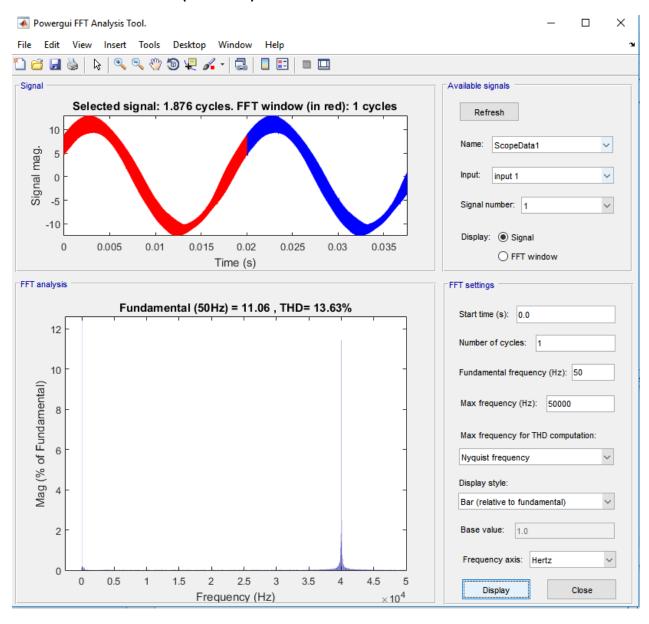
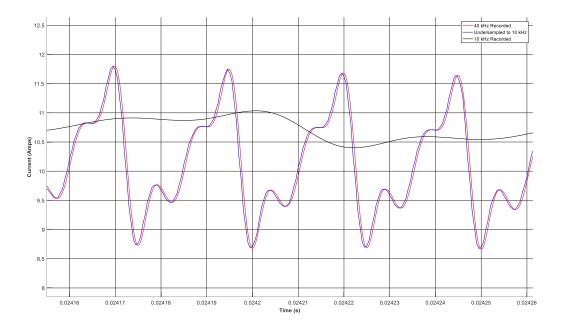
Measured current at 10kHz (Ts = 500ns):



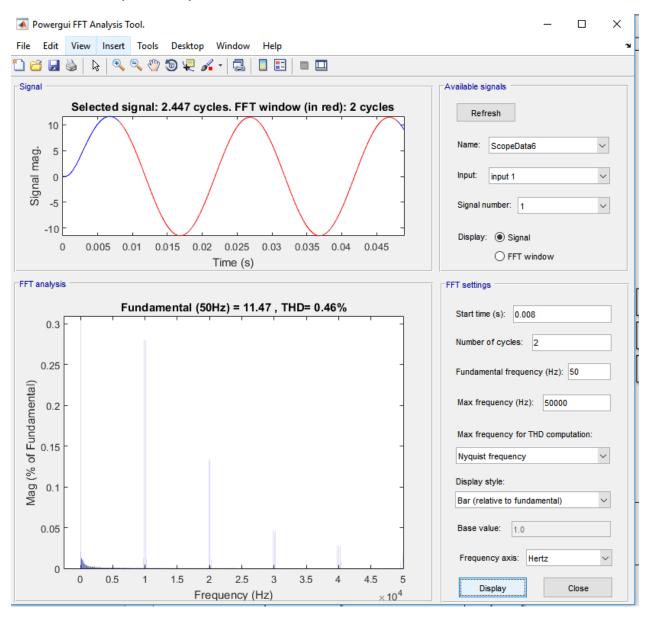
Measured current at 40kHz (Ts = 200ns)



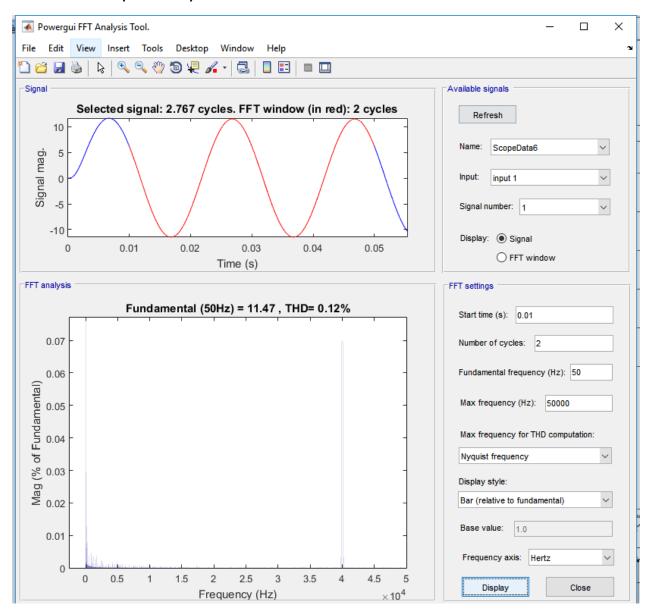
Downsampling:



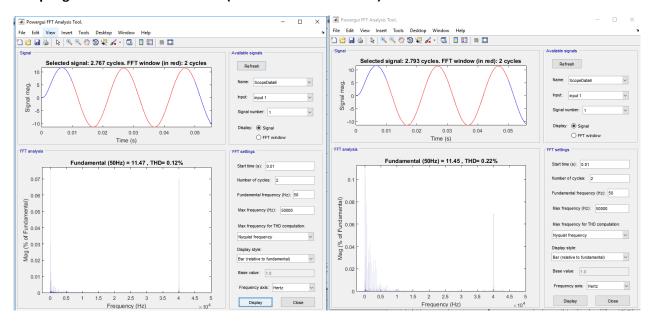
10kHz simulation (Ts = 100ns):



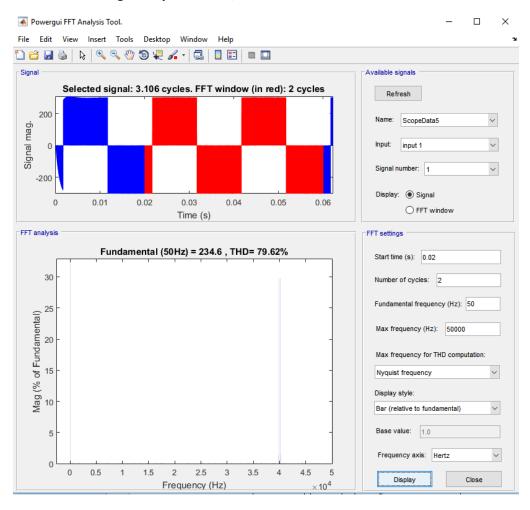
40kHz simulation (Ts = 100ns):



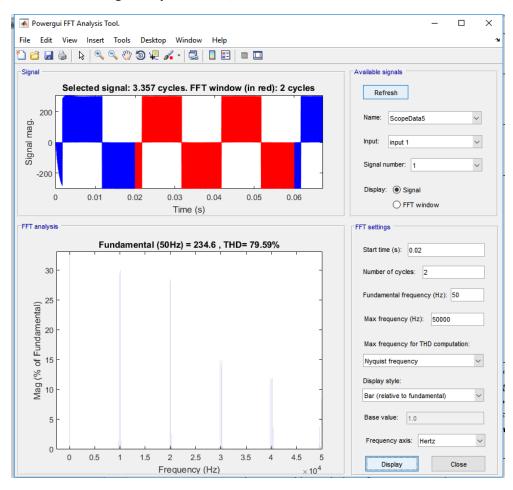
Sampling rate effect on harmonics (Ts = 100ns vs Ts = 500ns):



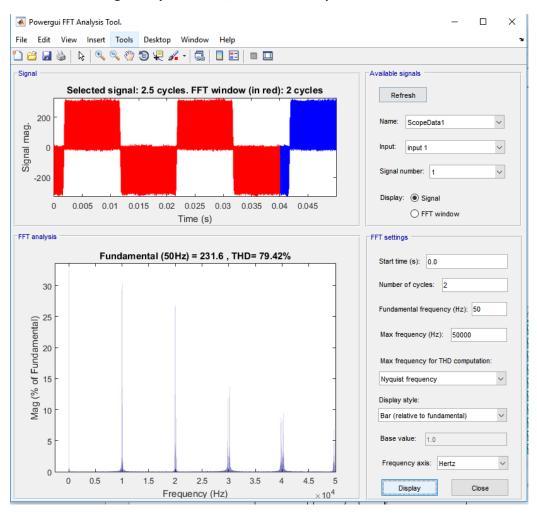
Line-to-line voltage analysis - 40kHz, Ts = 100ns - Simulation



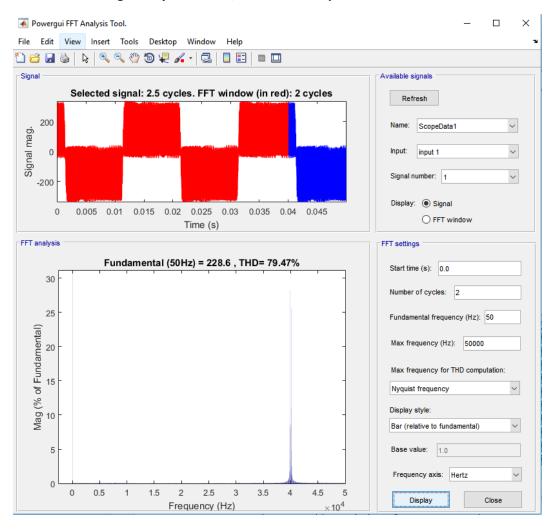
Line-to-line voltage analysis - 10kHz, Ts = 100ns - Simulation



Line-to-line voltage analysis - 10kHz, Ts = 500ns - Experiment



Line-to-line voltage analysis - 40kHz, Ts = 200ns - Experiment



Comparison:

	fsw	VII-rms	VII-THD	ls-rms	Is-THD
Simulation	10kHz	165,9	79,59%	8,11 A	0,46%
	40kHz	165,9	79,62%	8,11 A	0,12%
Experiment	10kHz	163,8	79,42%	7,81 A	2.73%
	40kHz	161,7	79,47%	7,82 A	13.63%