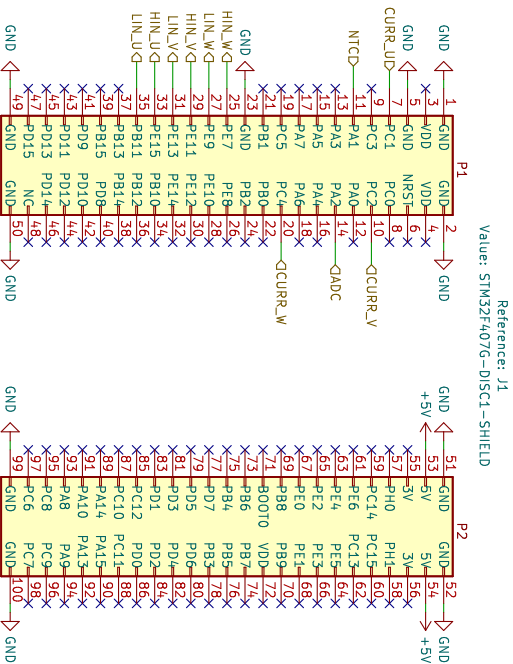


Sheet: /			
File: Main.kicad_sch			
Title:			
Size: A4	Date:	Rev:	
KiCad E.D.A. KiCad 7.0.6		Id: 1/4	



Sheet: /MCU/			
File: MCU.kicad.sch			
Title:			
Size: A4	Date:	Rev:	
KiCad E.D.A.	KiCad 7.0.6	Id: 2/4	

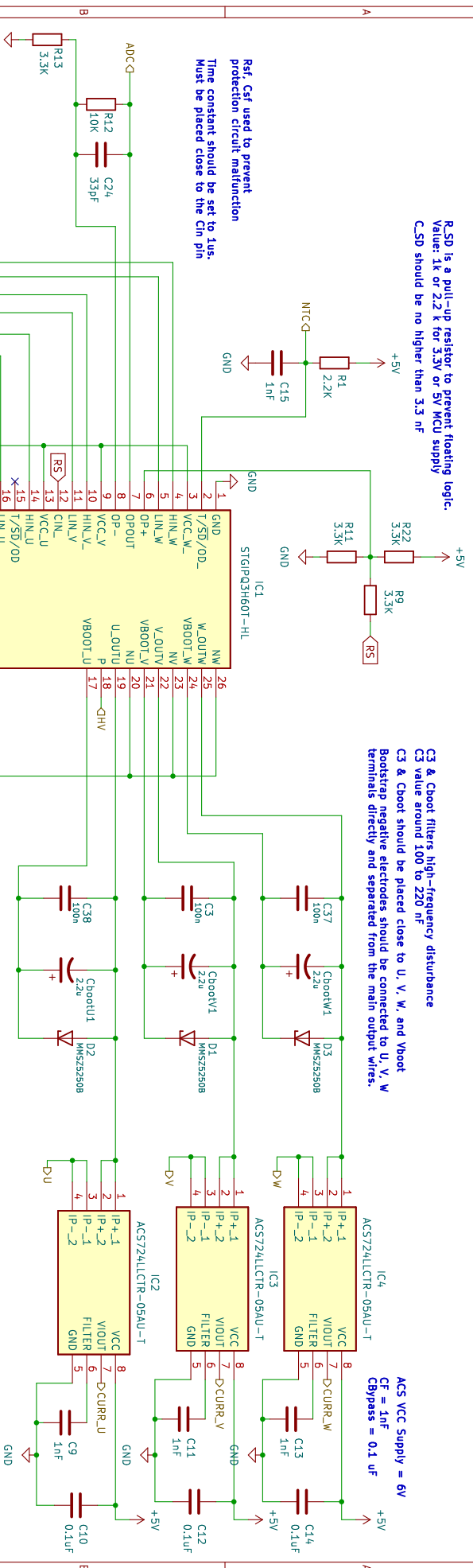
R_{SD} is a pull-up resistor to prevent floating logic.
Value: 1k or 2.2 k for 3.3V or 5V MCU supply
C_{SD} should be no higher than 3.3 nF

Rsf, Csf used to prevent
protection circuit malfunction
Time constant should be set to 1us.
Must be placed close to the Cin pin

C3 & Cboot filters high-frequency disturbance
C3 value around 100 to 220 nF
C3 & Cboot should be placed close to U, V, W, and Vboot
Bootstrap negative electrodes should be connected to U, V, W
terminals directly and separated from the main output wires.

ACS VCC Supply = 6V

CF = 1nF
CBypass = 0.1 uF



RC time constant about 100 ns

To avoid input signal oscillation,
the wiring of each input should be
as short as possible

Direct coupling to the MCU terminals
without opto-couplers is possible

Low-inductance shunt resistors used for
phase leg current sensing

Shunt calculation:
 $R_S = 1.5A \times R_S = 0.54$
 $R_S = 0.36$
 $- 3x 1R$ in parallel

Sheet: /inverter/
File: Inverter.kicad_sch

Title:

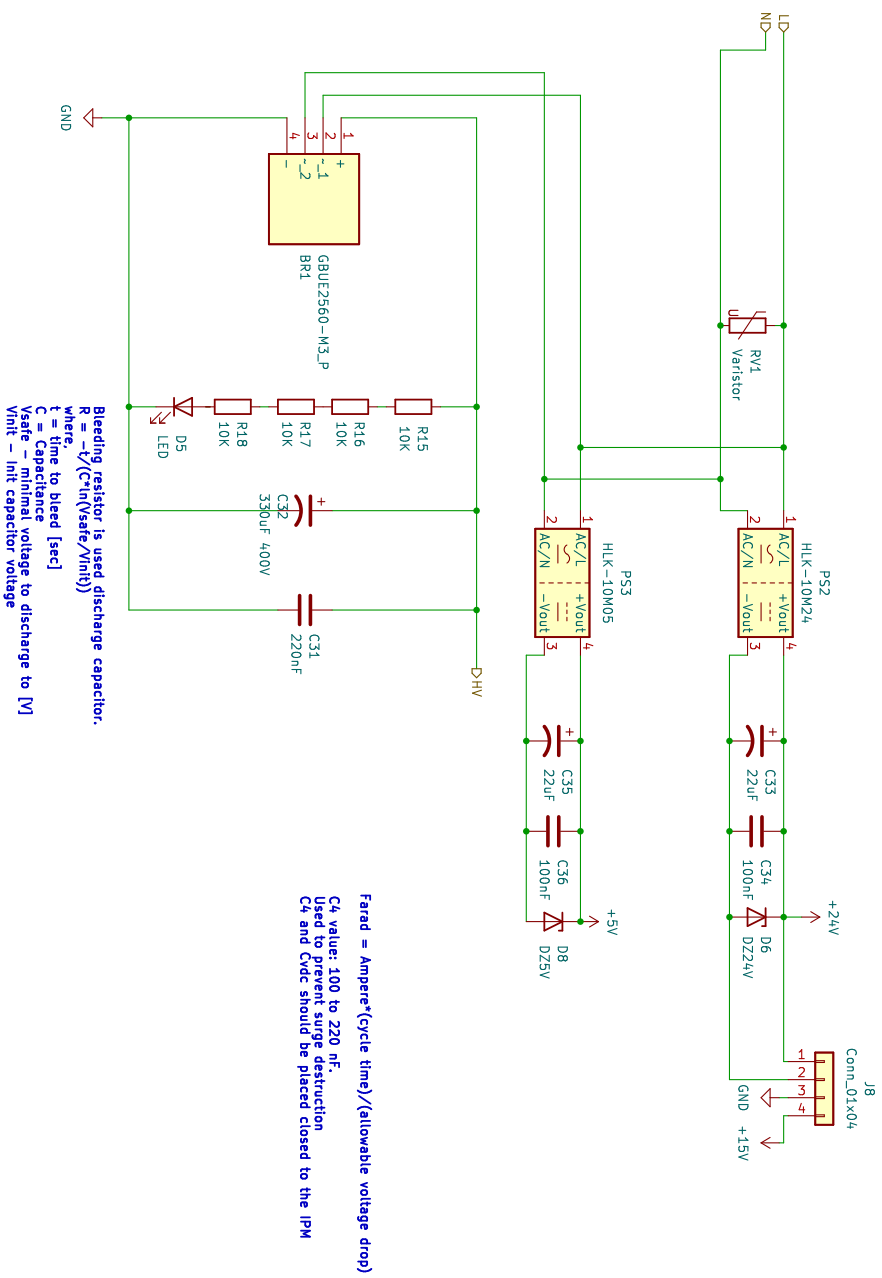
Size: A4

Date:

KiCad E.D.A. kicad 7.0.6

Rev:

Id: 3/4



Sheet: /Rectifier & Power supply/
File: Rectifier.kicad.sch

Title:

Size: A4

Date:

KiCad E.D.A. kicad 7.0.6

Rev:

Id: 4/4