

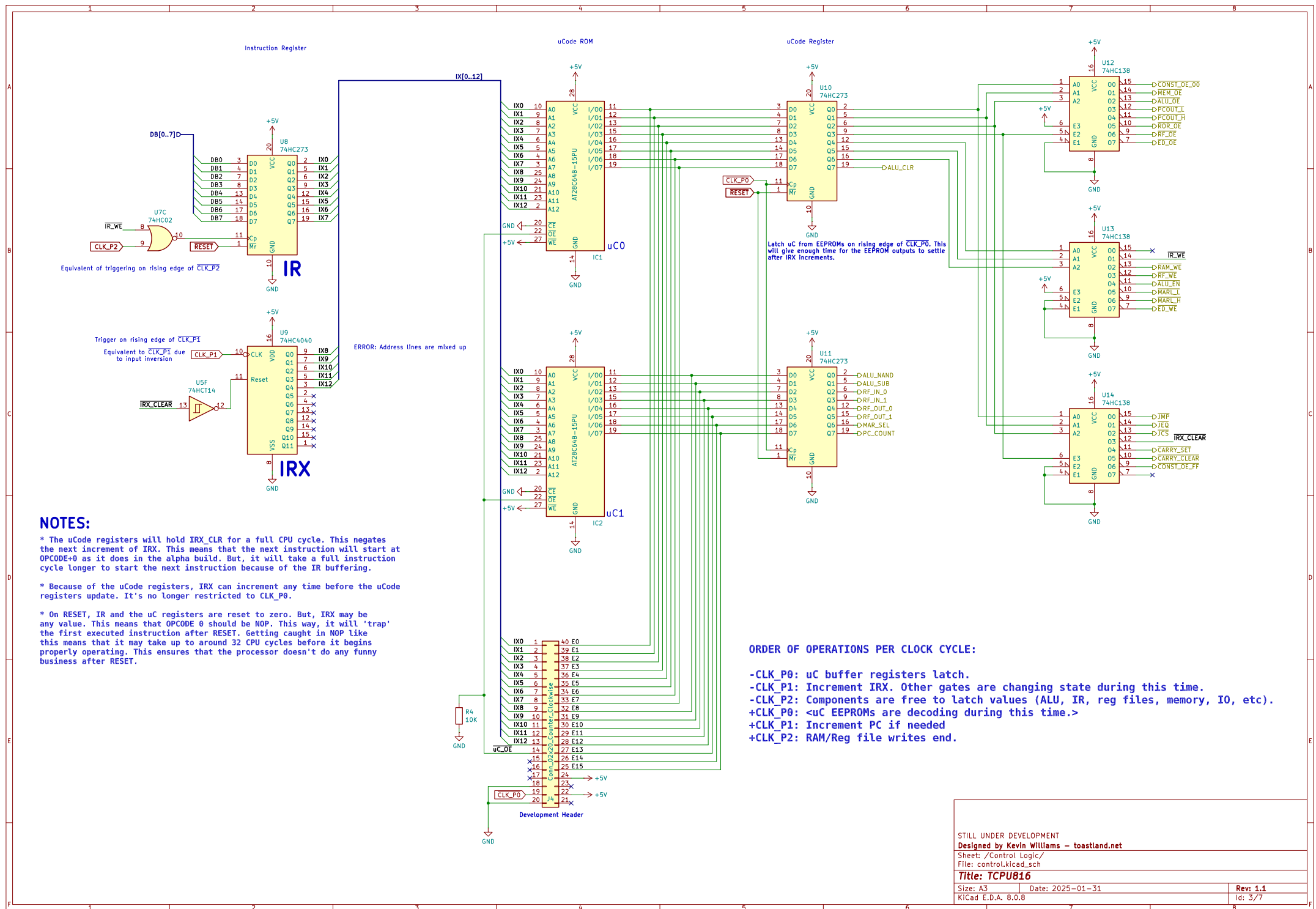
Standard Pierce oscillator
Rs to be determined (if needed)

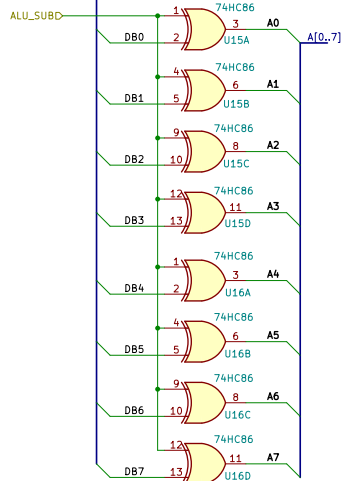
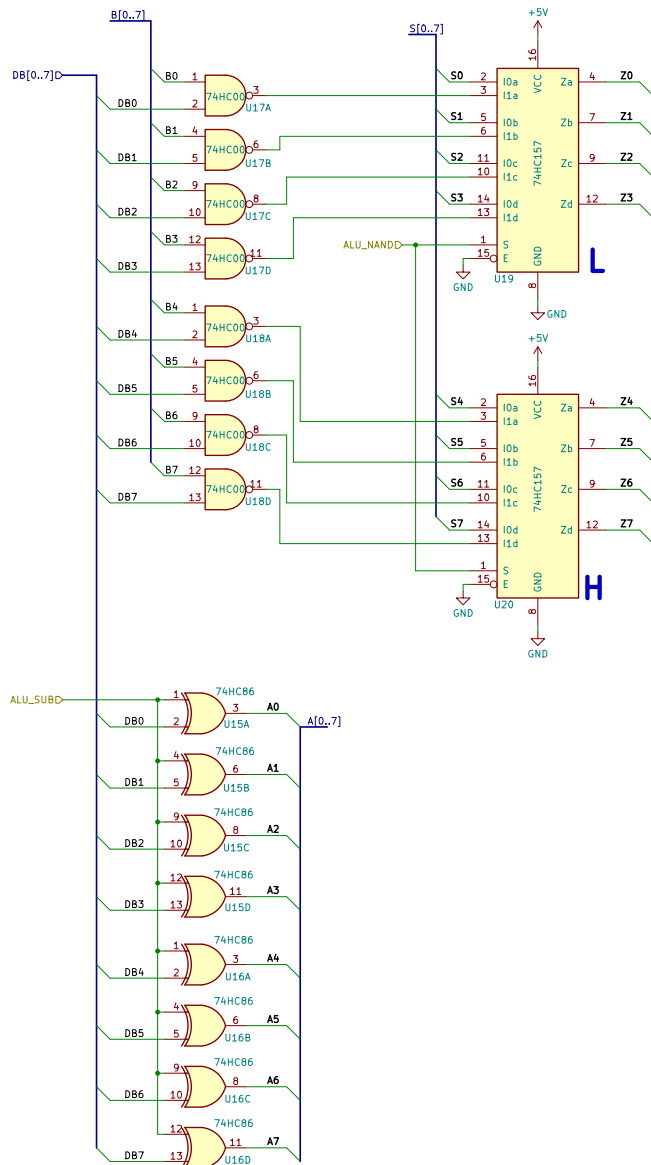
Here to (hopefully) reduce the
parasitic effects of J3. May
not be needed.

Clock Override
Allows you to attach an external clock source/stepper.

2 MHz output
Each phase is shifted ~84ns

STILL UNDER DEVELOPMENT		
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Sheet: /Clock Generator/		
File: clock_gen.kicad_sch		
Title: TCPU816		
Size: A5	Date: 2025-01-31	Rev: 1.1
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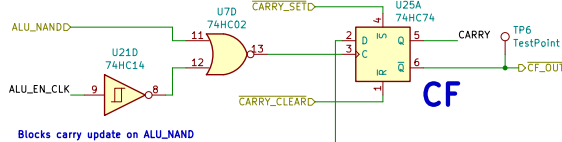




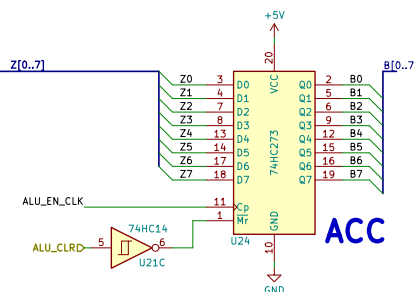
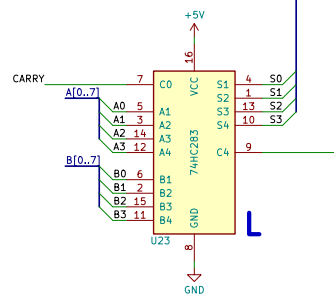
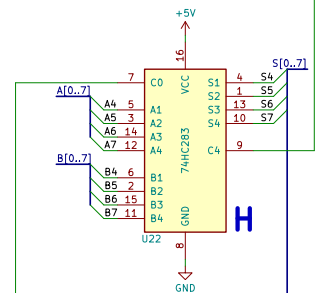
Trigger on rising edge of $\overline{\text{CLK_P2}}$



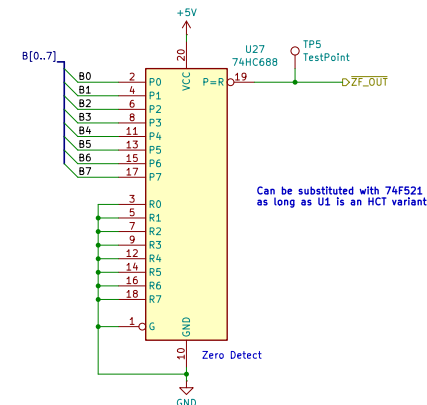
Equivalent of triggering on $\overline{\text{CLK_P2}}$



Blocks carry update on ALU_NAND

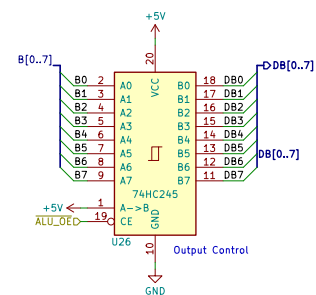


ACC

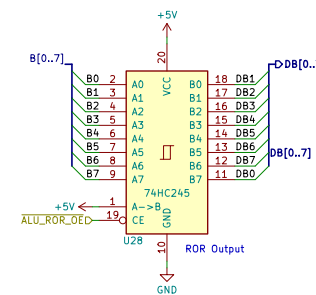


Can be substituted with 74F521 as long as U1 is an HCT variant.

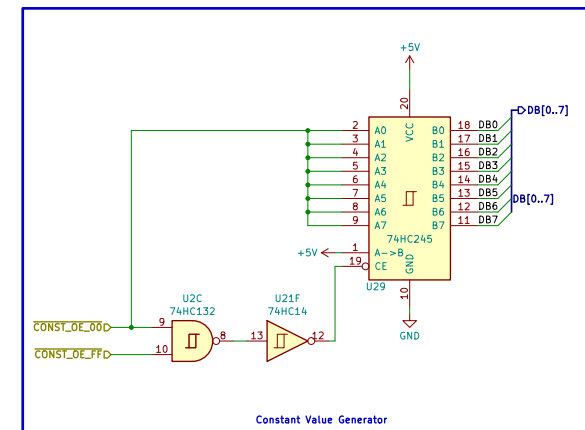
Zero Detect



Output Control



ROR Output



Constant Value Generator

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Sheet: /ALU/
File: ALU.kicad_sch

Title: TCPUB16

Size: A3 Date: 2025-01-31

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Rev: 1.1

Id: 4/7

