

3 nand, 1 four and

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2 nand, 1 four and

inverter

|  |  |  |
| --- | --- | --- |
| a1 | a0' | c0 |
| a1 | a3' | c1 |
| a2 | a3' | c2 |
| a2' | a3' | c3 |
| a1 | a0 | c4 |
| a1' | a0 | c5 |
| a1' | a3 | c6 |
| a2' | a3 | c7 |