

ストロブ命令をたしこみ、ひきかえの制御信号
 (r, adr) (r, adr)

12 8 本

o STR

1. $\text{MAR} \leftarrow \text{MDR} + \text{GRB}$
 $(\text{MDR} \rightarrow A, \text{GRB} \rightarrow B) \rightarrow \text{ALU}(A+B) \rightarrow C \rightarrow \text{MAR}$

1. bus A on MDR-latch

2. GRB out

3. ALU(001)

4. MAR-latch

2. $\text{MDR} \leftarrow \text{GRH}$ / $\text{GRH} \rightarrow A \rightarrow \text{ALU}(A) \rightarrow C \rightarrow \text{MDR}$

1. GRA out

2. ALU(101)

3. MDR-latch

3. $\text{mem}(\text{MAR}) \leftarrow \text{MDR}$ / $\text{MDR} \rightarrow (\text{MAR} \rightarrow \text{メモリアドレス})$
 1. write $\rightarrow \text{メモリアドレス}$

o SUBadr

1. $\text{MAR} \leftarrow \text{MDR} + \text{GRB}$
 $(\text{MDR} \rightarrow A, \text{GRB} \rightarrow B) \rightarrow \text{ALU}(A+B) \rightarrow C \rightarrow \text{MAR}$

1. bus A on MDR-latch

2. GRB out

3. ALU(001)

4. MAR-latch

2. $\text{MDR} \leftarrow \text{mem}(\text{MAR})$

1. read

2. MDR-latch

3. MDR s-mdi

3. $\text{GRA} \leftarrow \text{GRA} - \text{MDR}$

1. GRA out

2. bus B on MDR-latch

3. ALU(010) \rightarrow ALU(001) = 23 = ADDR

4. GRA latch

(557)

° SUBr.

$$1. \text{GRA} \leftarrow \text{GRA} - \text{GRB}$$

1. GRAout

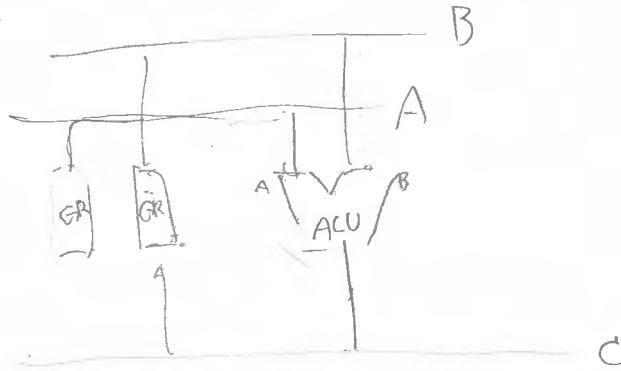
2. GRBout

3. $\text{ALU}(010) \leftarrow \{ \text{ALU}(001) \}$
I = 73 = ADDR

4. GRA latch

AND

- ALU (111)
 - GRA-latch
 - GRB-latch
 - GR-latch
- 157-



DIV

- MPU (100)
- GR-latch
- GRA-latch
- GRB-latch

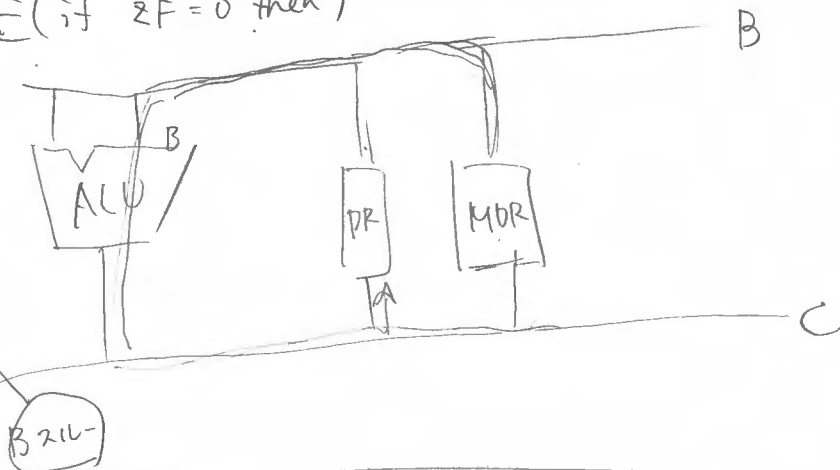
MLT

- MPU (011)
- GR-latch
- GRA-latch
- GRB-latch

JMP, JZE (if ZF=0 then)

~~MDR~~

- ALU (110)
- MDR-B-latch
- PR-latch



LAD

- ALU (110)
- MDR-B-latch
- GR-latch

