# **HAMIM REJA**

+91 9382426273 

hamim.reja.mail@gmail.com

linkedin.com/in/hamim-reja-a2ba42279/

Languages: English, Hindi, Bengali(Native) https://github.com/hamimreja-404

**Skills:** Soft skills like teamwork, communication Portfolio: <a href="https://github.com/hamimreja-404/Hamim-Reja">https://github.com/hamimreja-404/Hamim-Reja</a>

## **ACADEMIC DETAILS:**

CGPA: 7.68(till 5 <sup>th</sup>
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#### **PROJECTS**

# Clothe's Duniya – E-Commerce Web App

- Overview: A fully responsive e-commerce website for clothing products built using HTML, Tailwind CSS, and JavaScript. Designed with mobile-first principles and modern UI/UX best practices. Currently integrating a backend using Node.js and MongoDB to handle product management, cart functionality, and user authentication.
- Tech Stack: HTML, Tailwind CSS, JavaScript, Node.js (in progress), MongoDB (in progress)

## Finance Flow – Personal Expense Tracker:

- Overview: A real-time expense tracking web app developed with HTML, CSS, JavaScript, and React. The application categorizes and visualizes personal finances using dynamic charts and stateful components. Focused on clean UI, reusability, Responsive and accurate state management using React hooks.
- Tech Stack: React JS (Vite), CSS, HTML, Node.js(Working)

#### Global Currency Converter:

- Overview: A real-time currency converter web app supporting over 100+ currencies worldwide.
   Built with HTML, CSS, and JavaScript, and integrated with a live exchange rate API to fetch up-to-date conversion values. Includes responsive design and intuitive UI for fast currency switching.
- Tech Stack: HTML, CSS, JavaScript, Exchange Rate API (ExchangeRate-API)

#### **TECHNICAL SKILLS**

- Frontend Development: HTML, CSS, Tailwind CSS, Bootstrap, React JS, Javascript
- ❖ Backend Development: Node.js, MongoDB, RESTful APIs
- Programming Languages: C, Java, Javascript, Verilog, Verilog-A
- ❖ DSA

#### Certifications

#### Certificate of Participation

- **Program:** eSim Circuit Simulation Project
- Project Title: Implementation of 3-Input NAND Gate Using Transistor-Transistor Logic (TTL)

Successfully completed circuit simulation demonstrating practical application of TTL in digital logic design.