

HAMIM REJA

+91 9382426273 ◇ Kolkata, West Bengal

hamim.reja.mail@gmail.com ◇ [Linkedin](#) ◇ [Portfolio](#)

OBJECTIVE

Aspiring Full-Stack Developer with strong experience in frontend development. Seeking an opportunity to contribute technical and problem-solving skills in a growth-oriented environment.

EDUCATION

B.Tech in Electronics and Communication Engineering, Aliah University
CGPA: 7.68 (till 5th semester)

Expected 2026

Higher Secondary, Target Point (R) School
Marks: 89%

2020 - 2022

SKILLS

Programming Languages	C, Java (DSA), JavaScript,
Frontend Development	HTML, CSS, Bootstrap, Tailwind CSS, React.js
Backend Development	Node.js(learning), MongoDB(learning)
Version Control	Git(Basic)

EXPERIENCE

Freelance Full-Stack Developer

May 2025 – Present Remote

- Built complete real-world web applications from scratch—handled UI/UX, frontend and hosting.
- Created complete end-to-end web solutions for a school and a wedding photographer, managing everything from responsive UI design to backend logic and deployment—all independently.

PROJECTS

School Website ([Live Link](#))

Designed a responsive multi-page school website with sections for routine, notices, academics, gallery section, faculty section and admissions. Frontend completed using modern UI practices; backend development is ongoing(Admin Login dynamic Routine Upload).

Tech Stack: React (Vite), Tailwind CSS, Node.js

Wedding Photographer Portfolio Website([Live Link](#))

Created a responsive portfolio website for a wedding photographer with multiple sections in a Single Page: Home, About, Services, Pricing, Testimonials, Gallery, and Contact. Emphasized user experience and mobile-first design principles using modern frontend technologies.

Tech Stack: HTML, CSS, Tailwind CSS, Javascript

E-Commerce Web App ([Live Link](#))

Responsive clothing store UI with mobile-first design. Currently integrating backend with product/cart/user functionality.

Tech Stack: HTML, Tailwind CSS, JavaScript, Node.js, MongoDB

CERTIFICATIONS

eSim Circuit Simulation Project – Implemented a 3-input NAND gate using Transistor-Transistor Logic (TTL) to demonstrate practical application in digital logic.