

Lab Tasks

Lab Task#1:

- Design **OR Logic Gate** on Logic Trainer using **NAND Gates** only and develop the truth table.
- Design **AND Logic Gate** on Logic Trainer using **NOR Gates** only and develop the truth table.
- Design **XOR Logic Gate** on Logic Trainer using **Primary Gates** only and develop the truth table.
- Design **XNOR Logic Gate** on Logic Trainer using **Universal Gates** only and develop the truth table.

Lab Task#2:

Simplify the following Boolean expression and develop the truth table for the simplified expression

$$\overline{A}BC + A\overline{B}\overline{C} + \overline{A}\overline{B}\overline{C} + A\overline{B}C + ABC$$

Basic rules of Boolean algebra.

1. $A + 0 = A$

2. $A + 1 = 1$

3. $A \cdot 0 = 0$

4. $A \cdot 1 = A$

5. $A + A = A$

6. $A + \overline{A} = 1$

7. $A \cdot A = A$

8. $A \cdot \overline{A} = 0$

9. $\overline{\overline{A}} = A$

10. $A + AB = A$

11. $A + \overline{A}B = A + B$

12. $(A + B)(A + C) = A + BC$

A , B , or C can represent a single variable or a combination of variables.

Lab Task#3:

Develop a truth table for the standard SOP expression and implement on Logic Trainer:

$$\overline{A}\overline{B}C + \overline{A}B\overline{C} + ABC.$$

Lab Task#4:

Determine the truth table for the following standard POS expression and Implement on Logic Trainer:

$$(A + B + C)(A + \overline{B} + C)(A + \overline{B} + \overline{C})(\overline{A} + B + \overline{C})(\overline{A} + \overline{B} + C)$$

Lab Task#5:

Map the following standard SOP expression on a Karnaugh map:

$$\bar{A}\bar{B}C + \bar{A}B\bar{C} + A\bar{B}\bar{C} + ABC$$

Lab Task#6:

Map the following standard POS expression on a Karnaugh map:

$$(\bar{A} + \bar{B} + C + D)(\bar{A} + B + \bar{C} + \bar{D})(A + B + \bar{C} + D)(\bar{A} + \bar{B} + \bar{C} + \bar{D})(A + B + \bar{C} + \bar{D})$$

Lab Task#7:

Group the 1s in each of the Karnaugh maps in Figure below. Determine the product terms for each of the Karnaugh maps in Figures and write the resulting minimum SOP expression.

AB \ C	C	
	0	1
00	1	
01		1
11	1	1
10		

(a)

AB \ C	C	
	0	1
00	1	1
01	1	
11		1
10	1	1

(b)

AB \ CD	CD			
	00	01	11	10
00	1	1		
01	1	1	1	1
11				
10		1	1	

(c)

AB \ CD	CD			
	00	01	11	10
00	1			1
01	1	1		1
11	1	1		1
10	1		1	1

(d)