#### **Memory structure**

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## **Terminology**

- Program : What you want to do using computer
- Process: The instance of a program that is being executed and allocated on memory
- Thread : A component of a process.
- ➤ When you run two 'note pad', one same program and different two processes will be activated

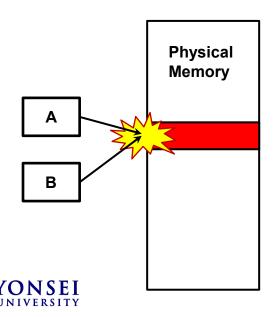
응용 프로그램 프로세스 서비스 성능 네트워킹 사용자					
이미지이름	사용자	C	메모리(	설명	^
ncsvc.exe	SYST	00	656 KB	NetCare	
notepad, exe	ParkS	00	2,308 KB	메모장	
notepad, exe	ParkS	00	2,324 KB	메모장	
OSPPSVC	NETW	00	2,368 KB	Microso	
POWERPNT	ParkS	00	31,044 KB	Microso	
Presentatio	LOCA	00	704 KB	Present	
Privacylcon	ParkS	00	15,484 KB	Intel(R)	
RAVCpl64,exe	ParkS	00	1,064 KB	Realtek	=
SearchInde	SYST	00	27,788 KB	Microso	

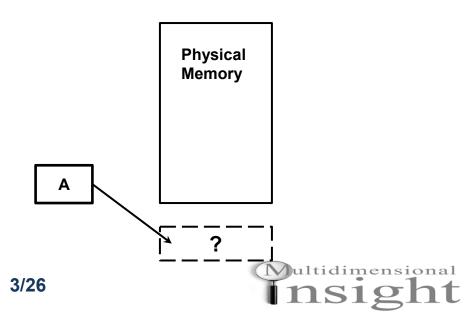




## Physical memory

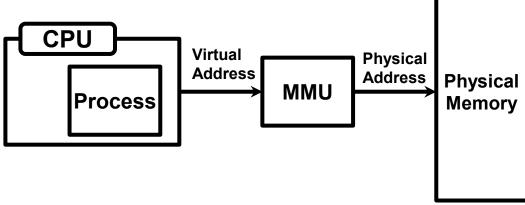
- Mainly, physical memory indicates RAM(primary memory)
- Problems:
  - What if processes A and B want to use a same physical address (or physical memory is already fully occupied)
  - What if a demanded address does not exist, because of too small RAM memory size





#### What is a virtual memory?

- Proposed to manage a physical memory efficiently
- Each process has its own virtual memory (4Gb for all 32bits OS)
- Each process can use the whole virtual memory whether the physical memory is less than 4Gb or not
- Virtual memory is translated to physical memory by MMU(Memory Management Unit)





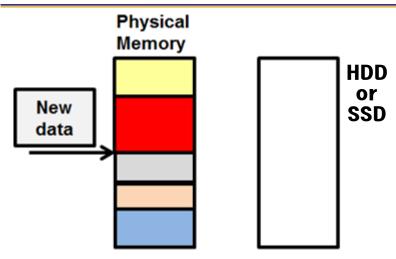


#### What is a virtual memory?(Continued)

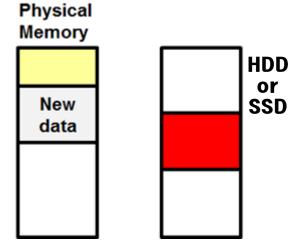
- MMU deceives CPU as like it uses a continuous physical memory while using a discrete physical memory
- Using the discontinuous physical memory space, MMU can deceive CPU as if CPU uses continuous physical memory
- If there is not enough RAM memory, it can use a part of HDD(or SSD) as a **Physical memory.** So you can use 4Gb or more memory with small RAM memory



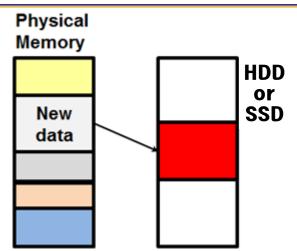




1. When new data gets into fully occupied RAM,



3. When some memories are released



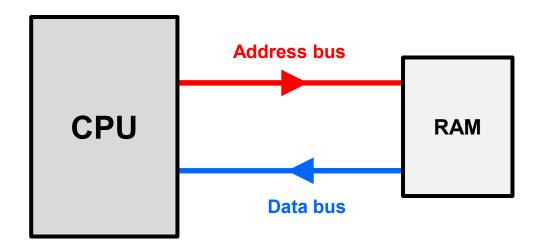
2. 'Unused data' goes to HDD and new data takes the RAM space



4. 'Unused data' can go back into the RAM space, if needed

#### What is a 32 bit computer?

- A sequence how CPU gets data from RAM
  - 1. CPU sends an address to RAM through the address bus
  - 2. RAM finds data using the received address from CPU
  - 3. Finally, RAM sends the requested data to CPU through the data bus

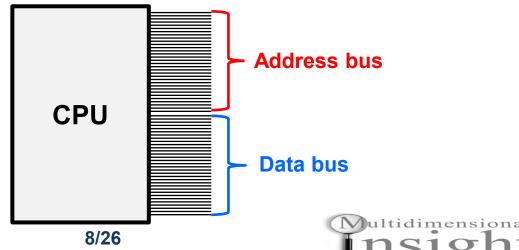






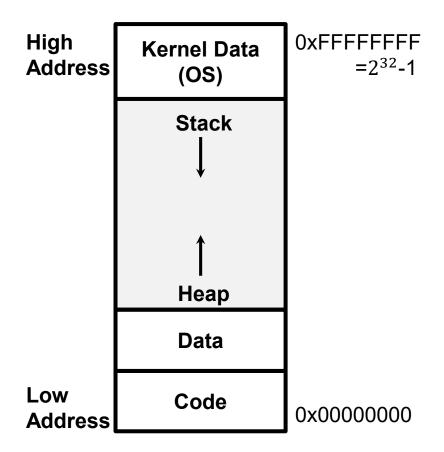
- In a 32 bit computer, both address and data bus have 32 pins
- Each pin can represent 1bit (0 or 1). So, 32 pins can represent 32 bit data and address
- RAM can send 32bit data to CPU for each clock cycle
- 32 bit can represent  $2^{32} = 4G$  cases. So ideally, 32bit computer can use 4Gb RAM in maximum.

It can be a reason for why virtual memory is designed to be 4Gb memory



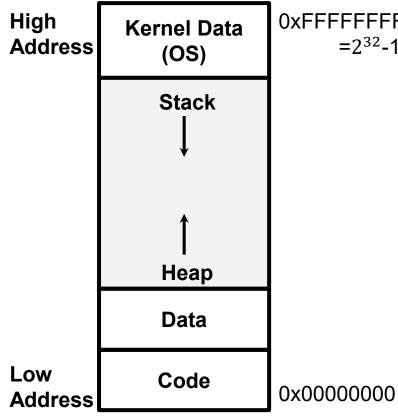


The structure of virtual memory (32bit computer)
(≈ structure of process loaded on memory)







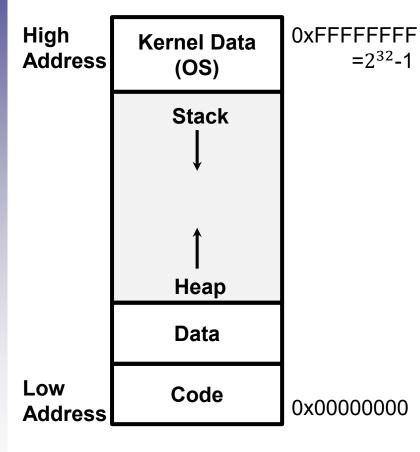


0xFFFFFFF  $=2^{32}-1$ 

- **Kernel Data region** 
  - : Region for kernel( $\approx$  OS)
- Stack
  - : Based on LIFO(Last In First Out) structure
- Heap
  - : almost same as a priority queue. User can control allocation and release timing
- We will cover stack and heap in another lecture







Data

: Region for static and global variables

Code

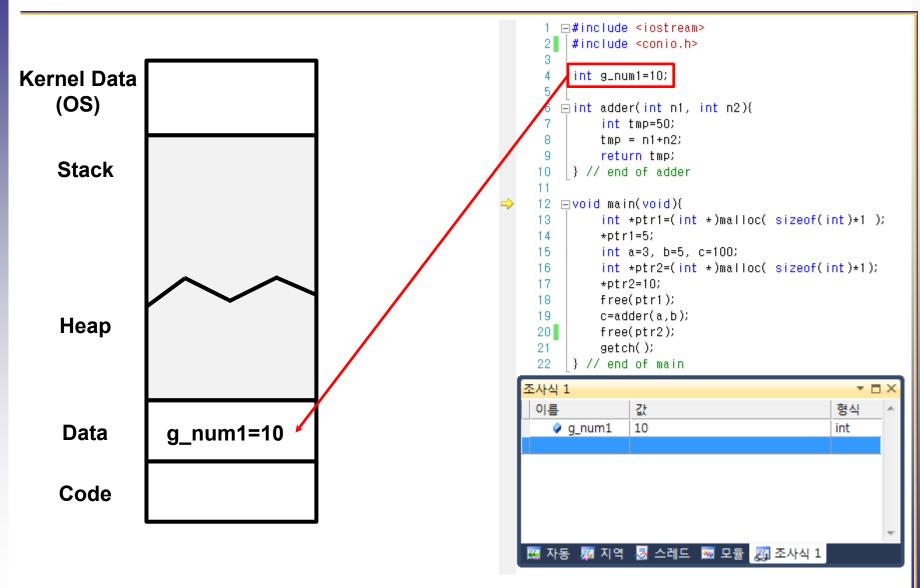
 $=2^{32}-1$ 

: Region for 'C language' code. Strictly saying, it is a region for binary version of code.

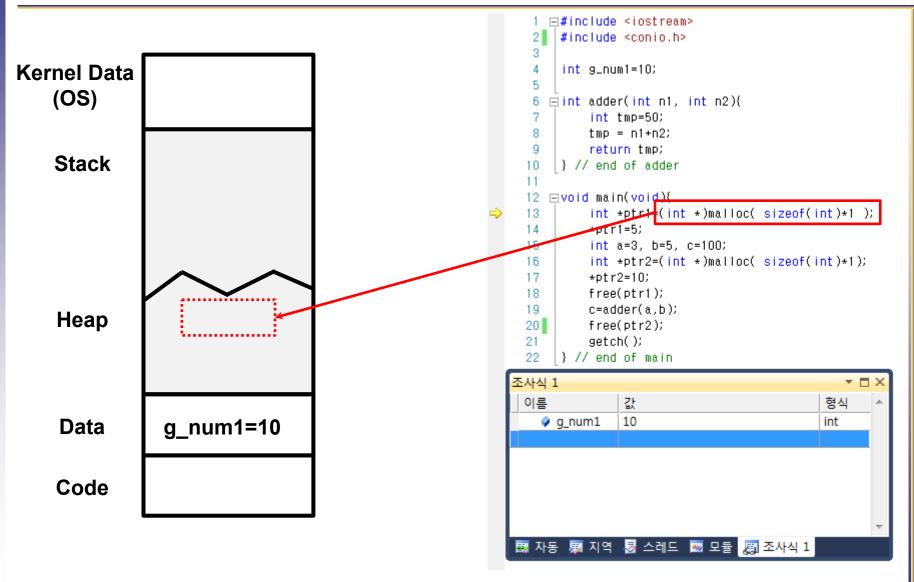




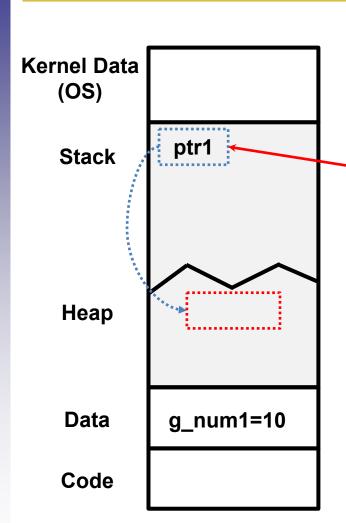
# Virtual memory(1/15)

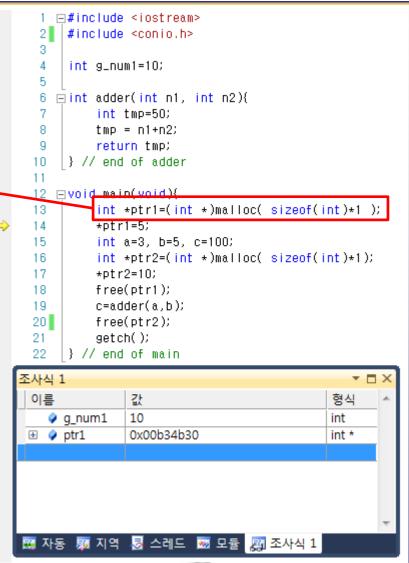


# Virtual memory(2/15)



# Virtual memory (3/15)

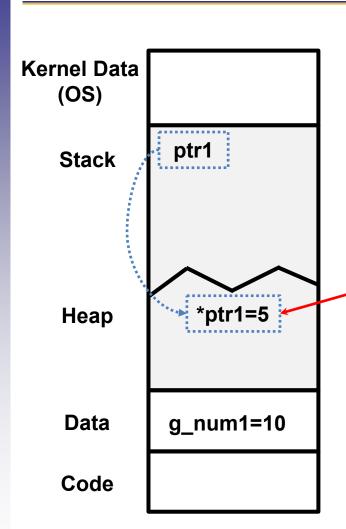


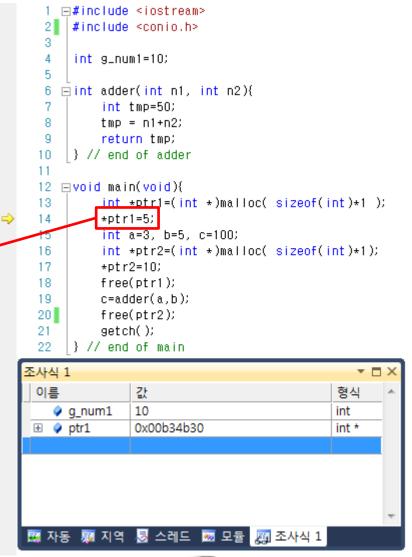






# Virtual memory(4/15)

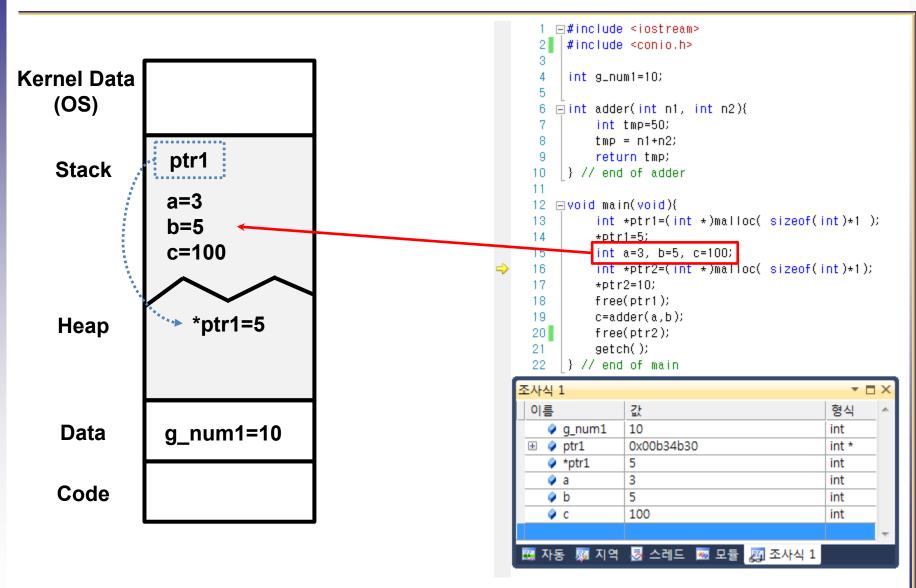




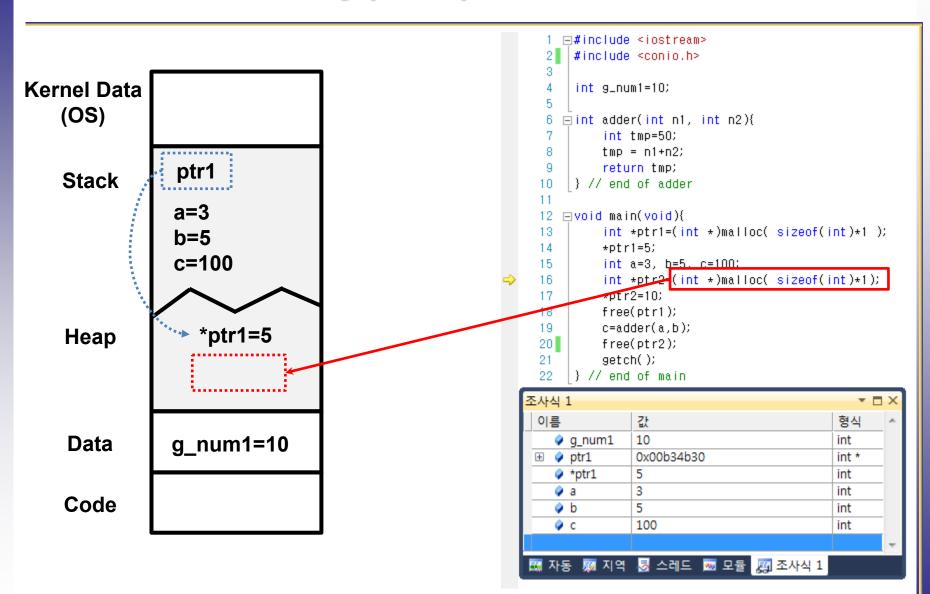




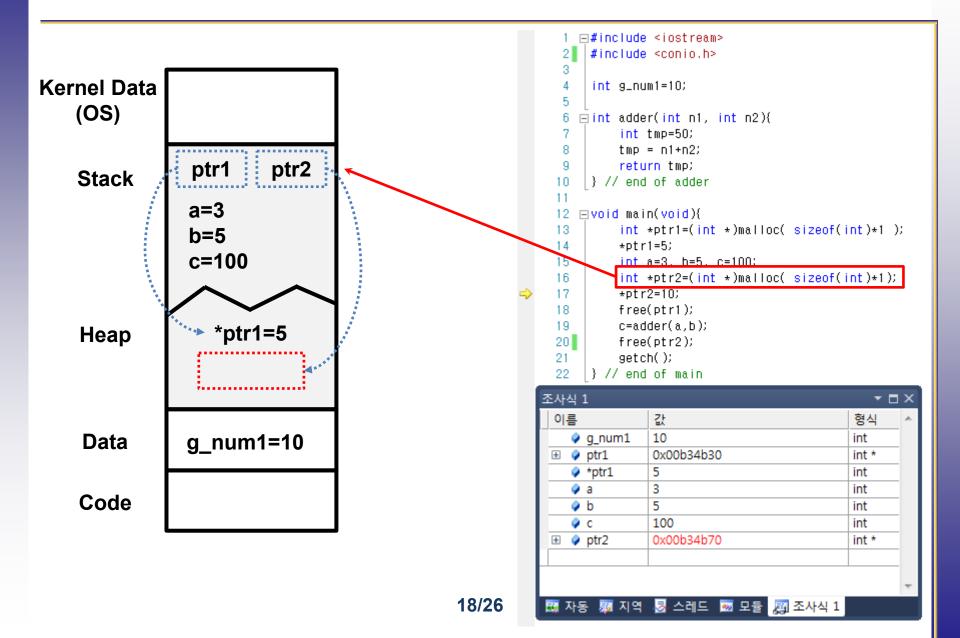
# Virtual memory(5/15)



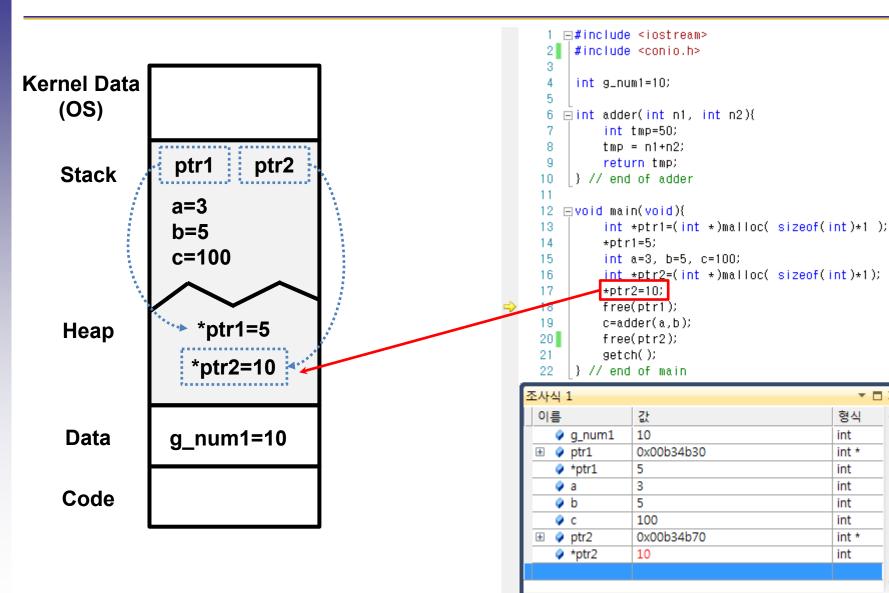
# Virtual memory(6/15)



# Virtual memory(7/15)



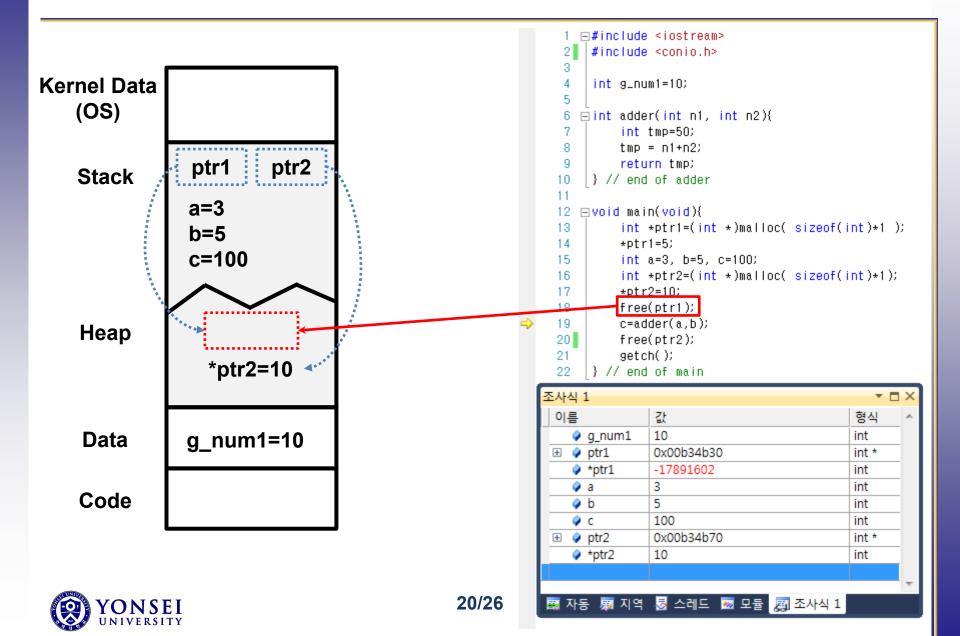
## Virtual memory(8/15)



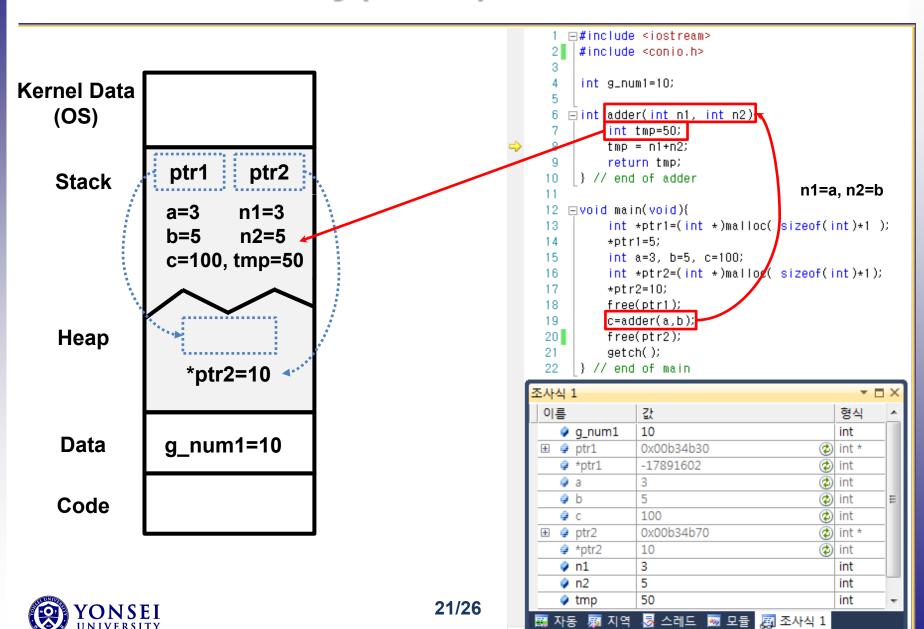
형식

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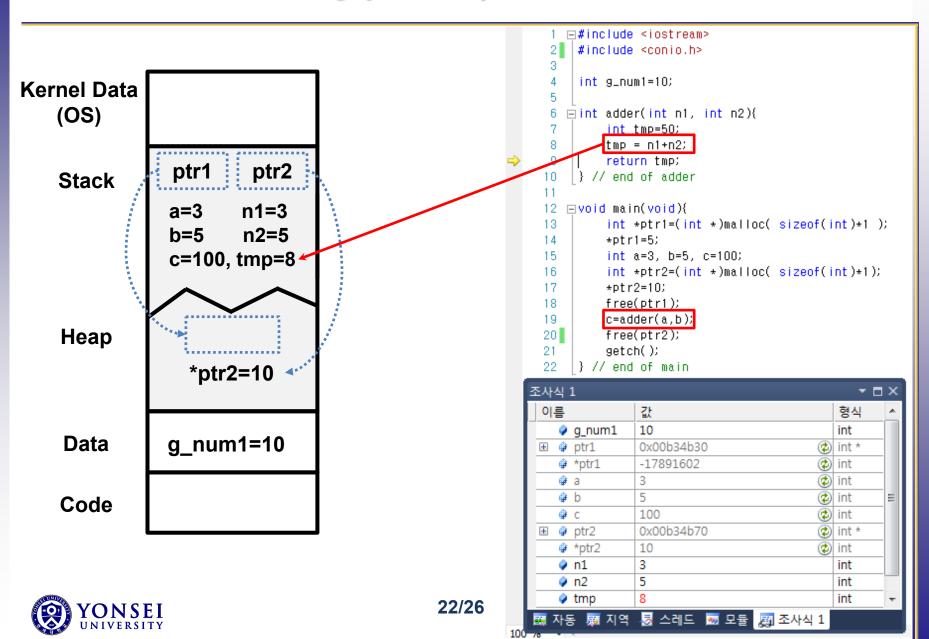
# Virtual memory(9/15)



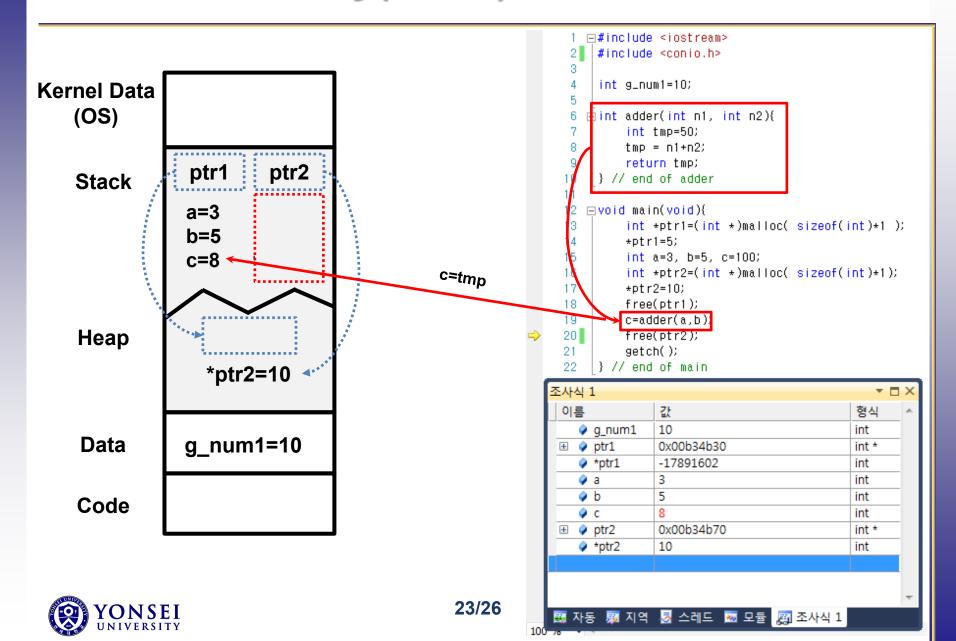
# Virtual memory(10/15)



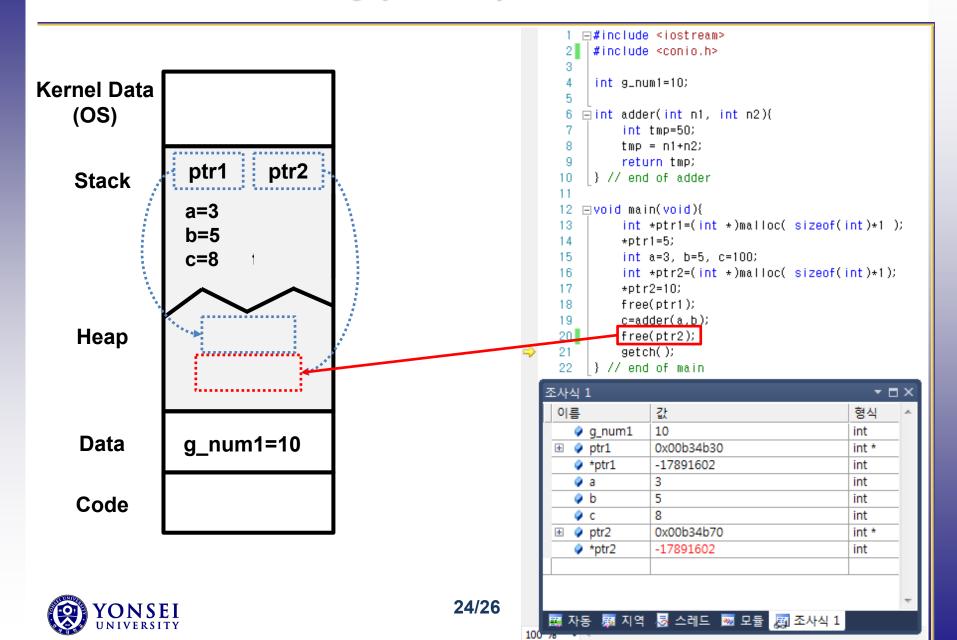
## Virtual memory(11/15)



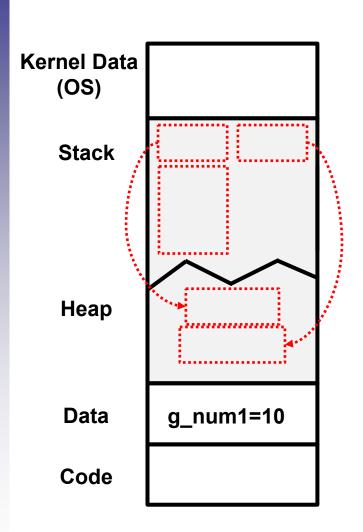
# Virtual memory(12/15)

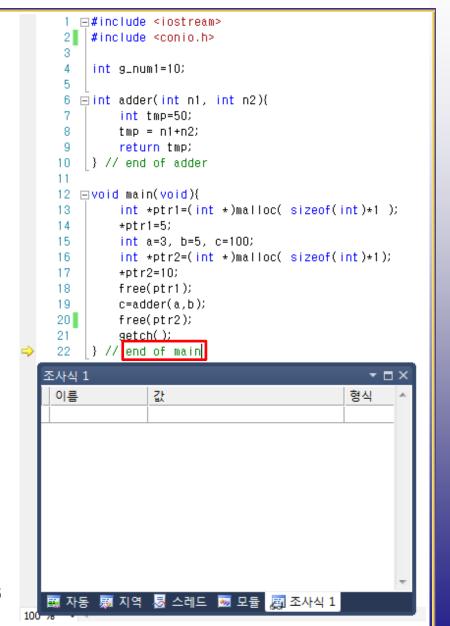


# Virtual memory(13/15)



## Virtual memory(14/15)







# Virtual memory(15/15)

