

HANANYA KAMPA

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CAREER OBJECTIVE:

- To pursue an entry level position in professional organizations with good communication skills and discipline where I can utilize my skills and knowledge efficiently for organizational growth.

ACADEMIC QUALIFICATION:

Course	Board/University	Year of Completion	Percentage\CGPA
B.Tech	Bapatla Engineering College(Autonomous), Bapatla,Acharya Nagarjuna University	2014-2018	7.08
XII (MPC)	Vignana Bharathi Junior College,Guntur ,Board of Intermediate Education,A.P	2012-2014	92.5
X (SSC)	Noble English Medium High		9.0

	School,Chirala, Board of Secondary Education	2011-2012	
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ACADEMIC EXPERIENCE:

Project Details:

- Worked on “**Solar Smart Inverter: A Novel Design using Multi level Topology and Pulse Width Modulation with Load Detections**”

Description:

- we are designing a Solar Multilevel Pulse Width Modulator inverter using microcontroller and cascade H bridge topology which increases the efficiency and reliability of the system.We are also assimilating Maximum Power Point Tracking for higher efficiency.

TECHNICAL SKILLS:

- C,C++,JAVA,HTML,CSS

WORKSHOP:

- Attended a 3 day workshop on "**Application of Matlab/Simulink to Electrical Systems**" approved by All India Council for Technical Education and conducted by Department of Electrical and Electronics Engineering.

ACHIEVEMENTS:

- Obtained MHRD merit Scholarship for having remarkable percentage in Intermediate

STRENGTHS:

- Self –Motivational
- Positive attitude
- Computer/Technology

EXTRA CURRICULAR ACTIVITIES:

- As a team of 7 members in my schooling participated in pyramids competition and got 1st price.
- In my intermediate I got 2nd prize in drawing in district level.

PERSONAL PROFILE:

- Name : K. Hananya
- Date of Birth : 3rd July 1997
- Father's Name : K. Neelakanta Rao
- Mother's Name : K. Vasantha
- Languages Known : Telugu, Hindi, English

DECLARATION:

- I do hereby confirm that information given in this form is true to the best of my knowledge and belief.

Place: Stuartpuram

Date:

(K.HANANYA)

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