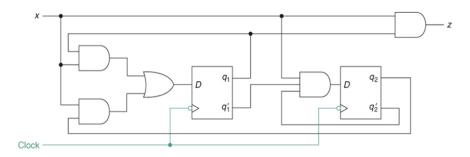
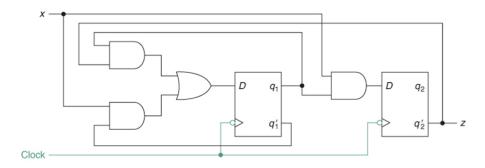
## 1. Sequential circuit analysis



- (1-a) Moore or Mealy? Why?
- (1-b) Draw state table
- (1-c) Draw state diagram

## 2. Sequential circuit analysis



- (2-a) Moore or Mealy? Why?
- (2-b) Draw state table.
- (2-c) Draw state diagram.

## 3. Ordered Binary Decision Diagram

Draw BDD for the function F = ab + cd. Assume that input order is a, b, c, d.

- 4. Ordered Binary Decision Diagram
  - (4-a) Explain the purpose of Ordered Binary Decision Diagram(OBDD).
  - (4-b) Draw a BDD for function f = ab' + acd. Assume that input order is b, c, a, d.

## 5. CMOS Circuit design

Draw a CMOS circuit design of the function f = ab+cd.