```
1 #pragma once
 2 #define ENDOP1 0xDE
3 #define ENDOP2 0xAD
4 #define ENDOP3 0xBE
 5 #define ENDOP4 0xEF
 6 //terms: iv: immediate value
 7 typedef enum {
       NOP,
                       //no operation
8
9
       MOV,
                       //value from r0 goes into r1 ex: MOV r0 r1
10
       LOADI,
                       //loads an iv into r# ex: LOADI r0 1337
                       //loads a str into sr#
11
       LOADS,
                                                     ex: LOADS r0 HELLO
12
       INC, DEC, ADD, SUB, MUL, DIV,
13
14
       //increment r#, decrement r#,
15
       //adds/subtracts/multiplies/divides r# r# into REG_RESULT
16
       //ex: ADD r0 r1
                          (REG_RESULT is now r0+r1)
17
18
       SWP,
                       //swaps two registers values
19
20
       POP,
                       //pops the stack into r#
                                                      ex: POP r#
21
       PUSH,
                       //push a register value;
                                                      ex: PUSH r#
                       //push an immediate to r#
22
       PUSHI,
                                                       ex: PUSH 0
23
24
       SYSCALL,
25
                       //calls a label with name
       CALL,
                                                     ex: CALL lblname
26
       RET,
                       //returns to the calling funk ex: RET
27
       INLINE_STR_START, INLINE_STR_END, INLINE_ID_REF,
28
29
30
       END
31 } OPCODES;
32
33 typedef enum {
34
       WRITEVAL, WRITEL
35 } SYSCALLS;
36
37 typedef enum {
       REG_RETVAL = 50, REG_ACCUMULATE, REG_RESULT
39 } SPECIAL_REGISTERS;
41 typedef enum {
       LBL_START=END, LBL_END
42
43 } KEYWORDS;
44
45 #include <stdint.h>
46
47 #define BASE TYPE uint32 t
48 #define ID_REGI_START 0
49 #define ID_STRREGI_START 505
50
51 #include "packer.h"
52 #include <vector>
```

```
53 #include "trace.h"
54 #include "VMException.h"
55 #include <string>
56 #define nxti (++i < n ? str[i] : 0)
57 using namespace std;
58 class subroutine_assember;
59 class assemblr {
60 public:
61
       std::vector<BASE_TYPE> m_prog;
62
        assemblr* s(string str);
63
64
       assemblr* call(char* _name);
65
        assemblr* syscall(SYSCALLS sc);
66
        assemblr* opcode(OPCODES op);
67
       assemblr* kw(KEYWORDS kw);
68
        assemblr* immediate(BASE_TYPE val);
       assemblr* regi(BASE_TYPE num);
69
70
       assemblr* strRegi(BASE_TYPE num);
71
       assemblr* end();
72
       virtual assemblr* finish() {
73
            return this;
74
       }
75
        subroutine assember* subroutine(char* 1b1);
       void _subroutine_finished(subroutine_assember *sa);
76
77
       BASE TYPE* assemble(int &n) {
78
            n = m_prog.size();
79
            BASE_TYPE *cc = new BASE_TYPE[n];
80
            for (int i = 0; i < n; i++)
81
                cc[i] = m_prog.at(i);
82
            return cc;
83
       }
84 };
85 class subroutine_assember : public assemblr {
86 public:
87
        assemblr *m_ctx;
88
        char *m lbl;
89
        subroutine_assember(assemblr *ctx, char* 1b1) :m_ctx(ctx), m_lbl(lb1) {
            kw(LBL_START)->immediate(_hash_sdbm((unsigned char*)1b1));
90
91
92
        assemblr* finish() {
93
            kw(LBL_END);
            m_ctx->_subroutine_finished(this);
95
            return m_ctx;
96
       }
97 };
```