

	In Lecture (Monday) In Lab (Tuesday-Friday)	Async. Assignment Due Dates
11-Jan		
12-Jan	Intro, Digital Design	
13-Jan	Lab 0 (at home)	
14-Jan		
15-Jan		
16-Jan		Course Logistics
17-Jan		
18-Jan		
19-Jan	Holiday	
20-Jan	Lab 1	
21-Jan		Breadboard Prototyping
22-Jan		
23-Jan		Intro to Oscilloscopes
24-Jan		
25-Jan		
26-Jan	Circuit Prototyping, Debugging	
27-Jan	Lab 2	
28-Jan		Scope Measurements & Controls
29-Jan		
30-Jan		Course Logistics Refresher
31-Jan		
1-Feb		
2-Feb	Oscilloscopes, HDLs	
3-Feb	Lab 3	State Machine Refresher
4-Feb		
5-Feb		VHDL Basics
6-Feb		
7-Feb		
8-Feb		
9-Feb	State Machines, Technical Comm	Hierarchical Design in HDL
10-Feb	Lab 4	
11-Feb		
12-Feb		Signal Tap Logic Analyzer
13-Feb		
14-Feb		
15-Feb		
16-Feb	In-class Exam 1	
17-Feb	Lab 5	
18-Feb		
19-Feb		Lab 6 State Machine Intro
20-Feb		
21-Feb		
22-Feb		
23-Feb	State Machine Design	
24-Feb	Lab 6	
25-Feb		
26-Feb		SCOMP Architecture
27-Feb		
28-Feb		
1-Mar		
2-Mar	Computer Architecture	
3-Mar	Lab 7	
4-Mar		
5-Mar		Processor I/O
6-Mar		
7-Mar		
8-Mar		
9-Mar	Processor Peripherals	
10-Mar	Lab 8	
11-Mar		
12-Mar		Design Project Background
13-Mar		Project Teaming Survey
14-Mar		
15-Mar		
16-Mar	Project Information	
17-Mar	Oscilloscope Practical Exercise - then - Project Intro	Project-related Assignments
18-Mar		
19-Mar		
20-Mar		Logbook 1
21-Mar		
22-Mar		
23-Mar	Holiday	
24-Mar		
25-Mar		
26-Mar		
27-Mar		
28-Mar		
29-Mar		
30-Mar	Project Information	
31-Mar	State Machine Practical Exercise - then - Project Work	
1-Apr		
2-Apr		
3-Apr		Lookbook 2
4-Apr		
5-Apr		
6-Apr	Proposals and Demos	Technical Checkpoint
7-Apr	Project Work	Proposals (scheduled with GTA)
8-Apr		
9-Apr		
10-Apr		Logbook 3
11-Apr		
12-Apr		
13-Apr	- No Lecture -	
14-Apr	Project Work	
15-Apr		
16-Apr		
17-Apr		Logbook 4
18-Apr		
19-Apr		
20-Apr	In-class Exam 2	Project Files Submission
21-Apr	Project Demo Time - Restricted Attendance -	
22-Apr		
23-Apr		
24-Apr		Logbook 5
25-Apr		
26-Apr		
27-Apr	Lab closed	
28-Apr		Project Demo & Summary