CEG3136 Lab 1 Marking Scheme

Group#	
Students:	
Work Bench	

(Note: Marks will be removed for poorly commented code)

Section	Sub	Task	Comment#	Points
0		Lab rules and regulations		/5
		Draft of Lab report		/10
		TOTAL:		**/15**
Demonstrations				
Part 1	e	New program with loop of 15, > prompt, and semi-colon separator		/5
Part 2	b	AlarmSimul program recognizes digits properly.		/5
Part 3		Delays implemented and bug resolved..		/5
		TOTAL		**/15**
Lab Report				
Part 1	b	Correct machine code in table.		/2.5
	b	Explanation of machine code and addressing modes		/5
	c	Proper explanation with correct C pseudo-code		/5
	f	Correct listing		/2.5
		TOTAL:		**/15**
Part2	g	Table of recorded register values.		/5
	h	Addressing modes of MOVW instructions and explanation of the effect of the instruction.		/5
	h	Discussion of the rts instructions and effect of its execution.		/5
	i	Discussion on debugging the program.		/10
		TOTAL:		**/20**
Part 3		Design of overall module and the setDelay routines		/15
		Design of pollDelay subroutine		/20
		TOTAL		**/35**
		GRAND TOTAL:		**/100**

#	Comment