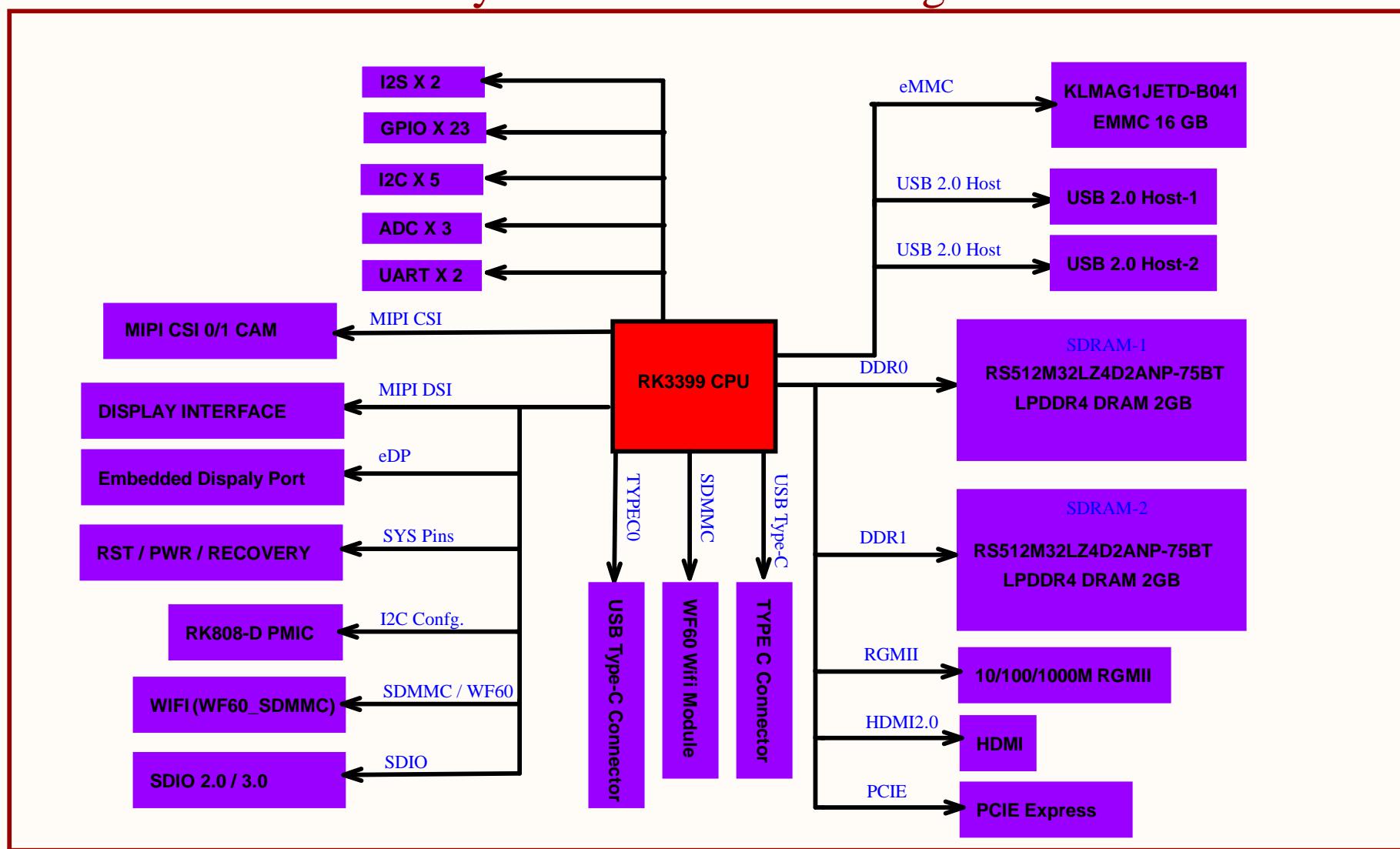
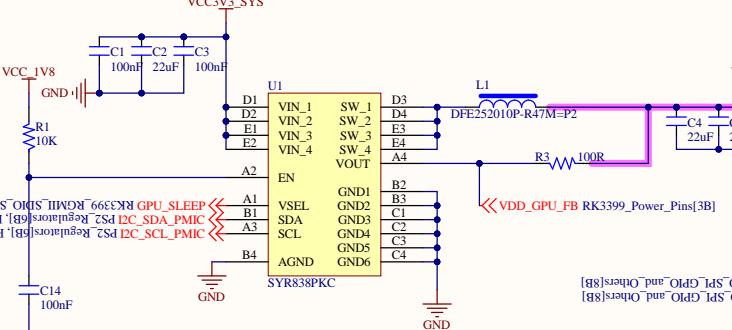


# System On Module Using RK3399

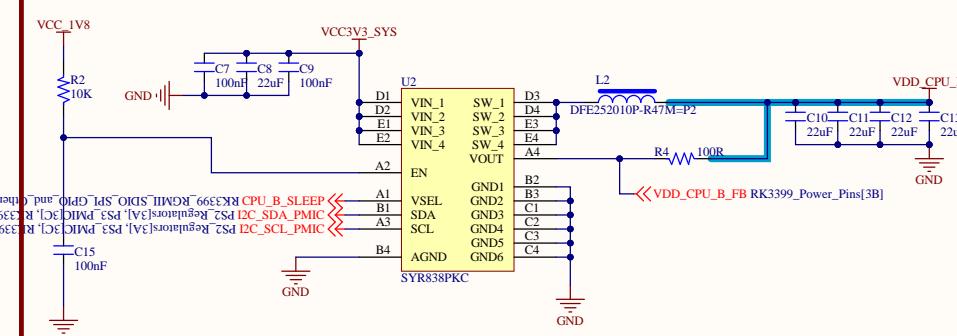


Title		
Size	Number	Revision
A4		
Date:	8/14/2025	Sheet of
File:	D:\High_Speed_PCB\.\Block_Diagram.SchDoc	Download By:

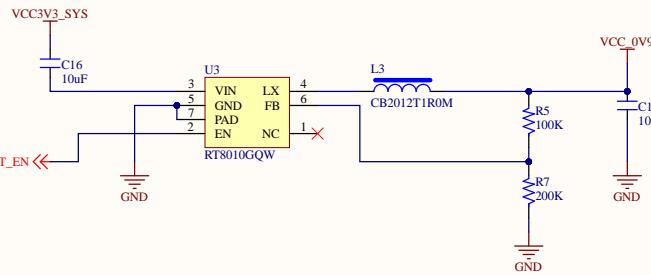
## GPU\_Regulator



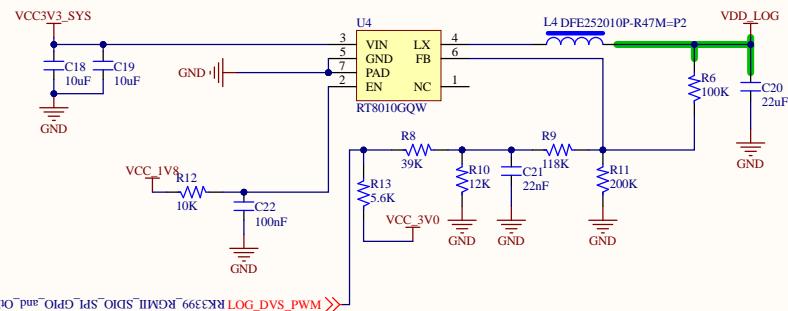
## CPU\_Regulator



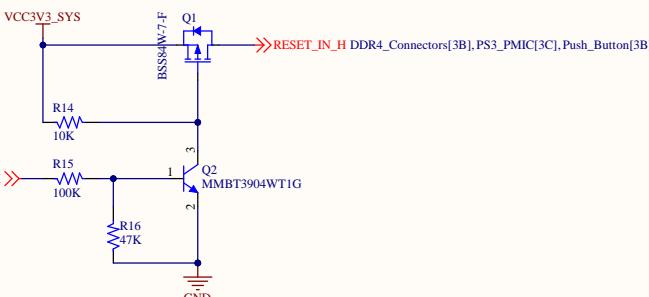
## 3V3\_to\_0.9V\_Regulator



## Regulator\_For\_CPU\_Logic



## Temperature\_Protection



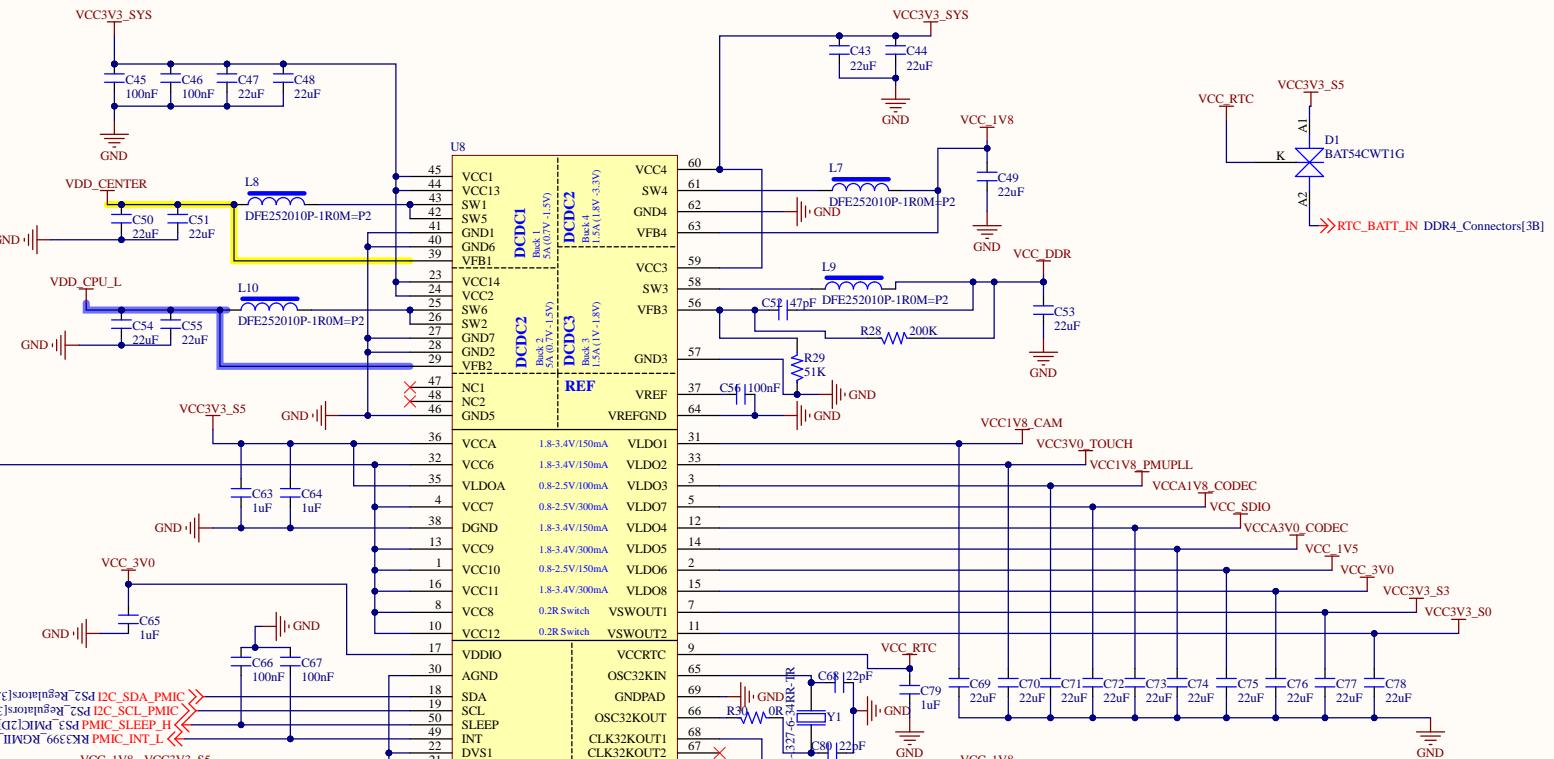
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Size	Number	Revision
A3		
Date:	8/14/2025	Sheet of
File:	D:\High_Speed_PCB\PS2_Regulators	10
By:		

# PMIC RK808-D

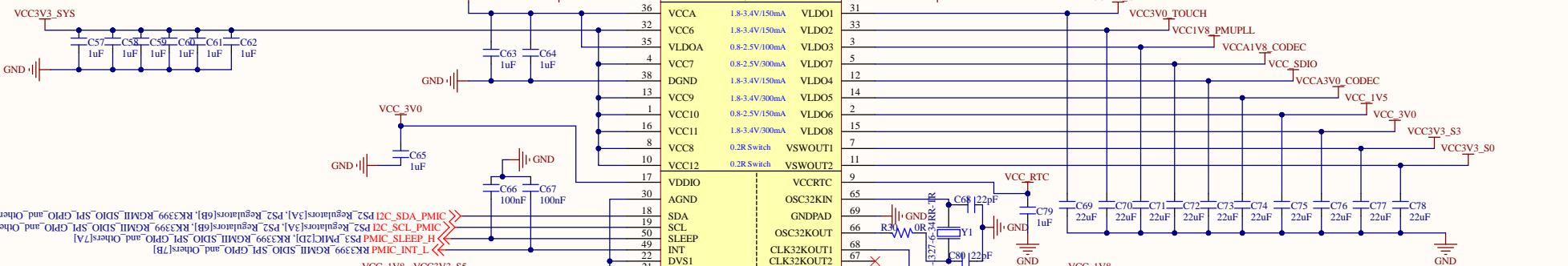
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A



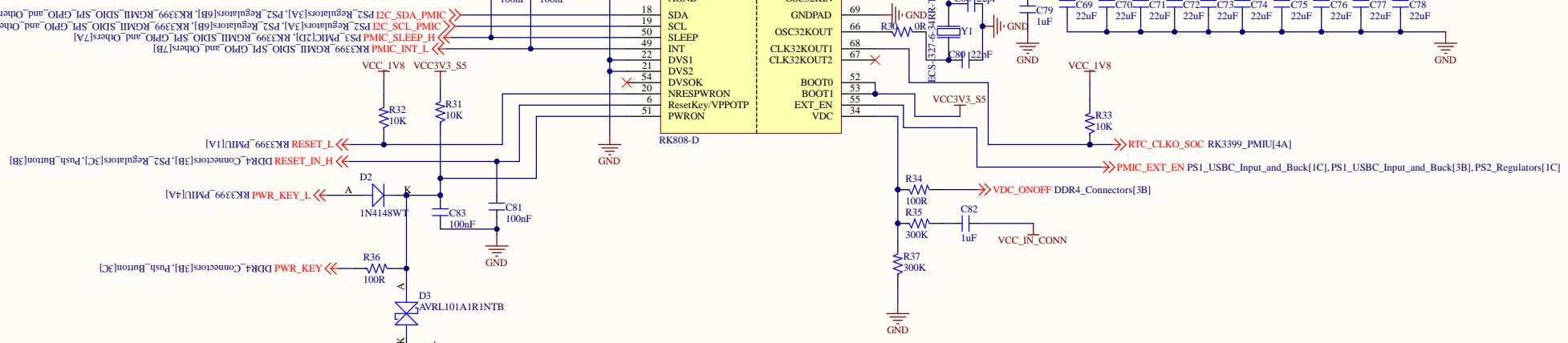
B

B



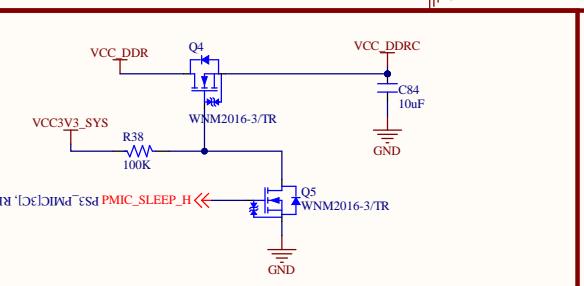
C

C



D

D



Title		
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Date:	8/14/2025	Sheet of
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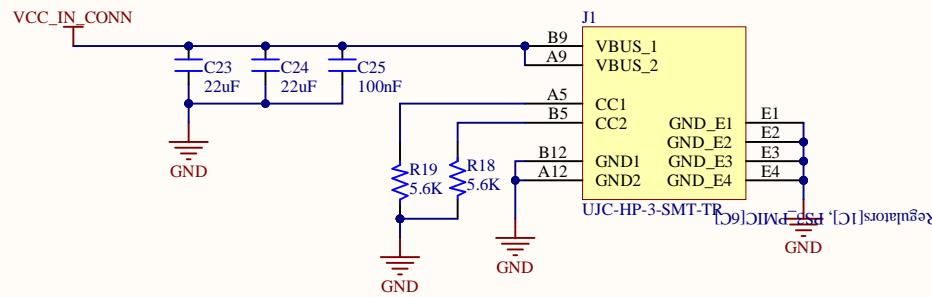
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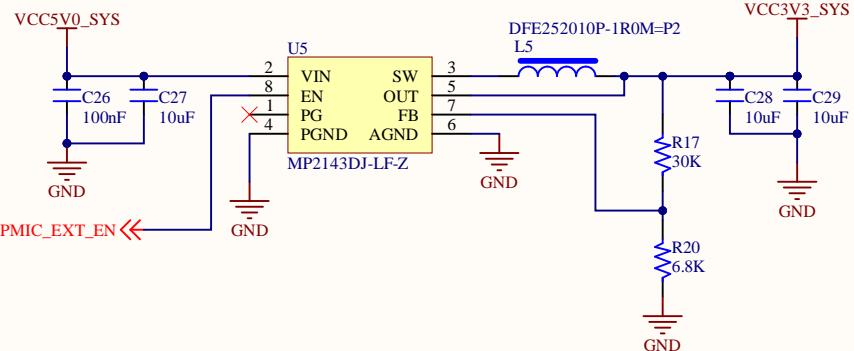
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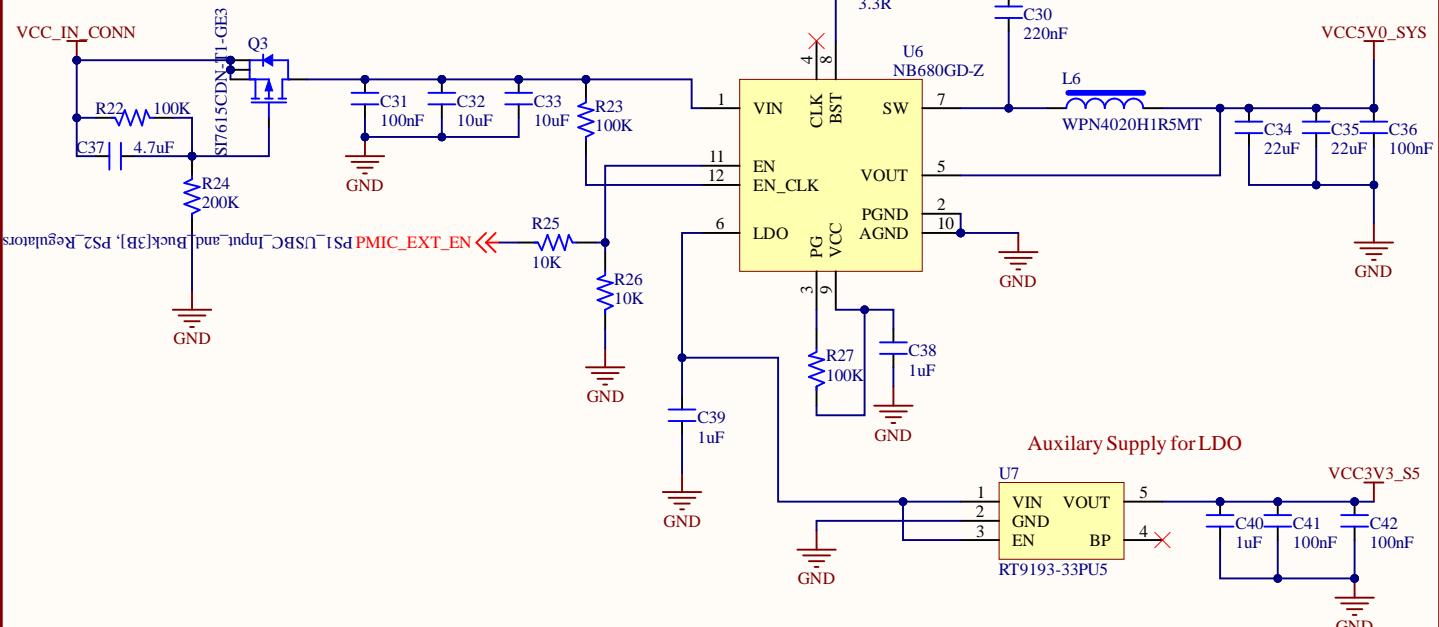
## USB\_Type\_C\_Input +12V



## +5V to +3V3



## +12V to 5V Buck



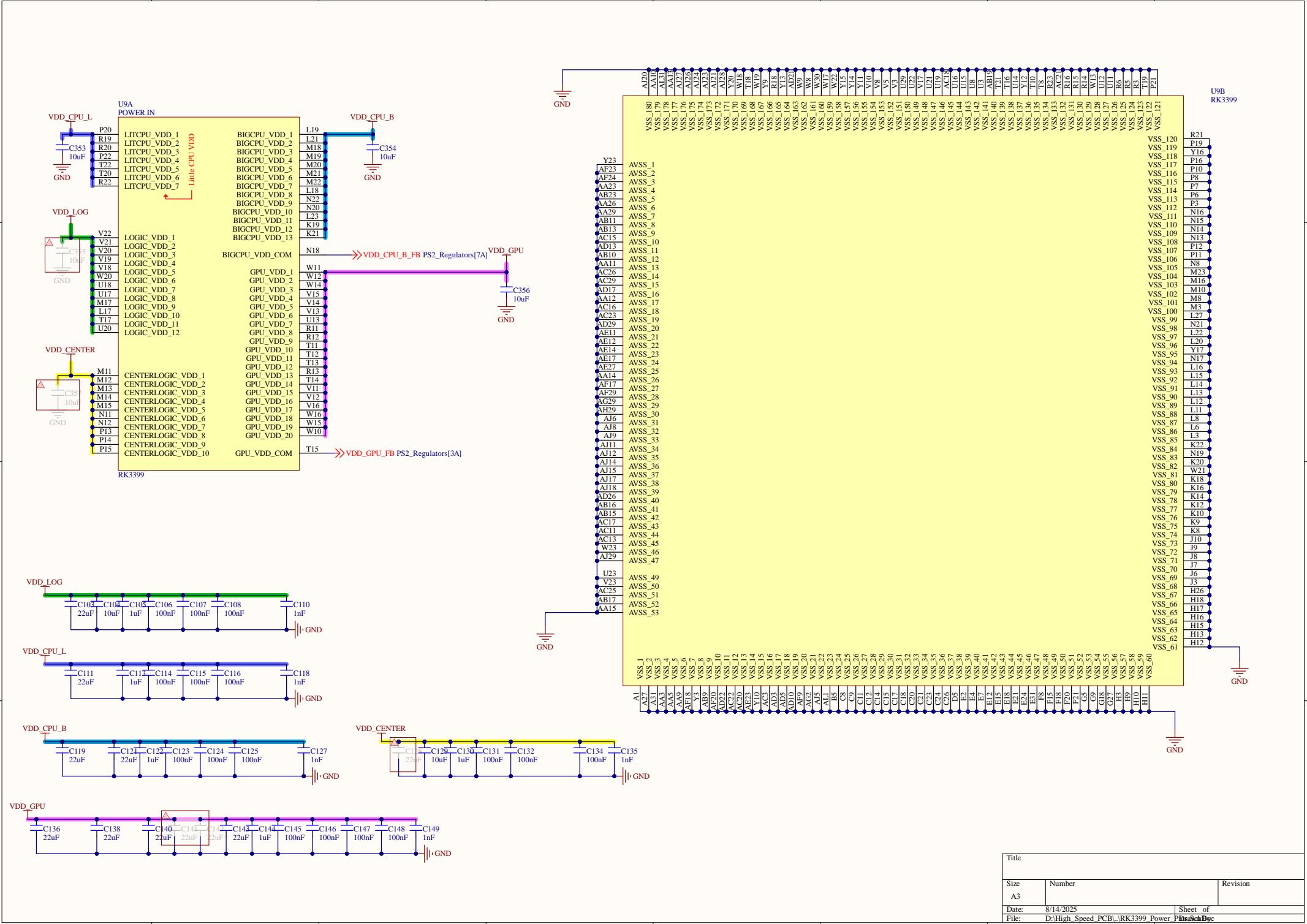
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Size	Number	Revision
A4		
Date: 8/14/2025	Sheet of	
File: D:\High_Speed_PCB\PS1_USBC_Input\Aux_Buck.SchDoc		

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2

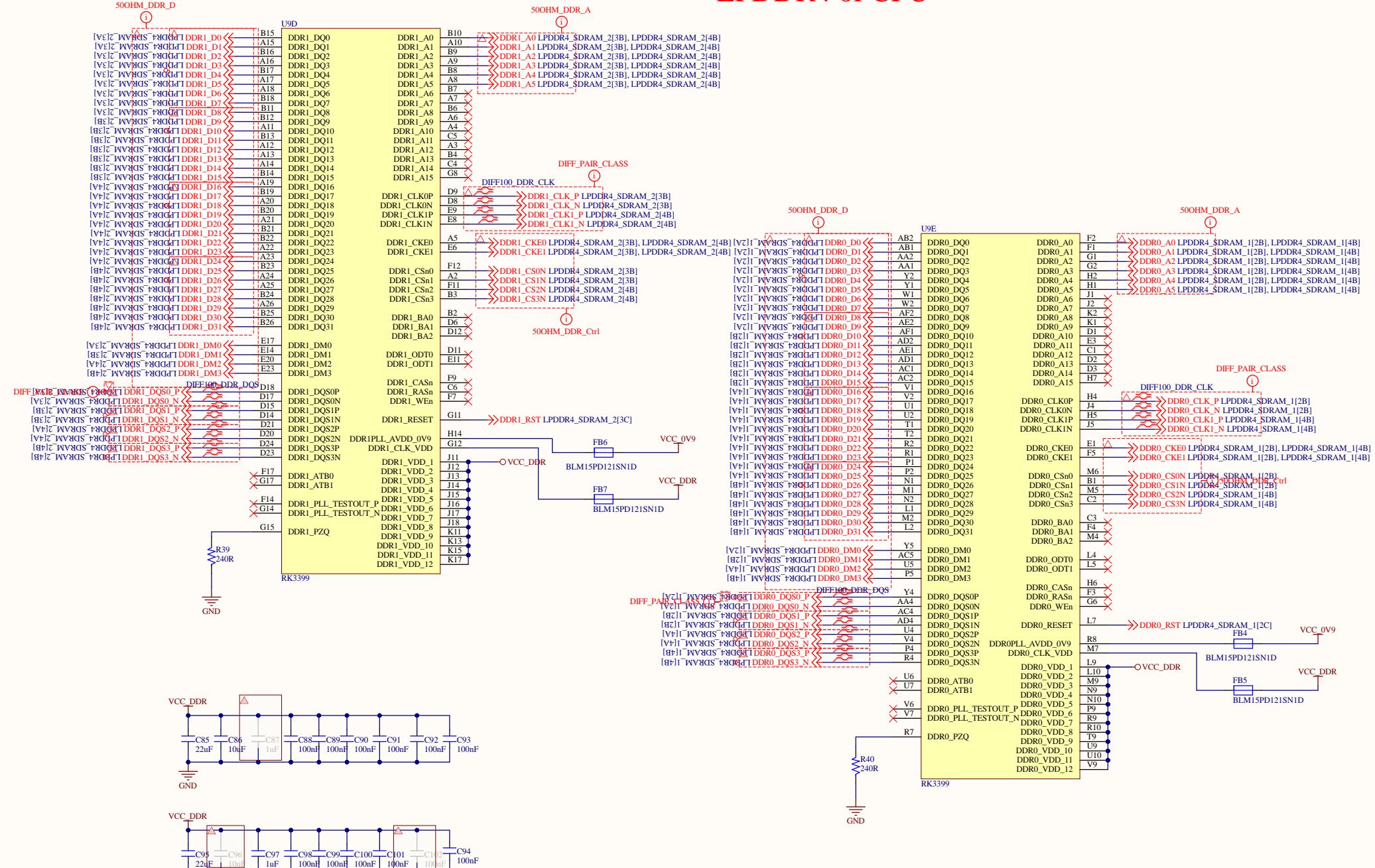
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4

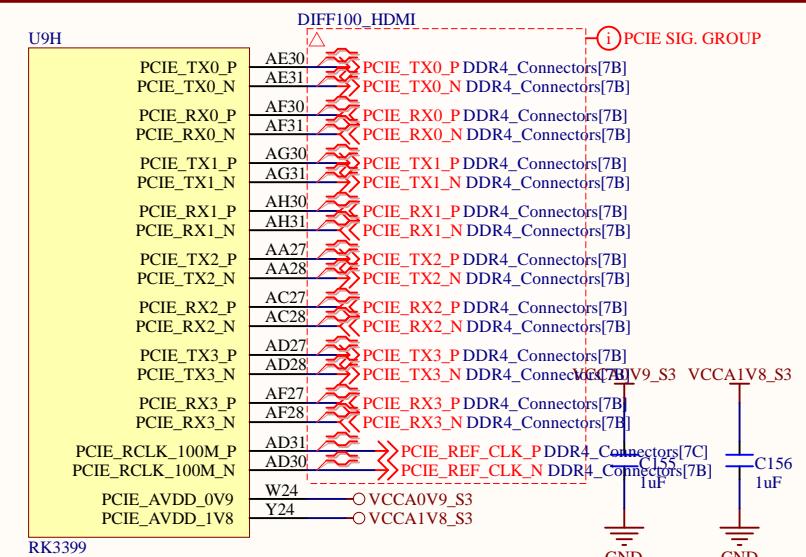
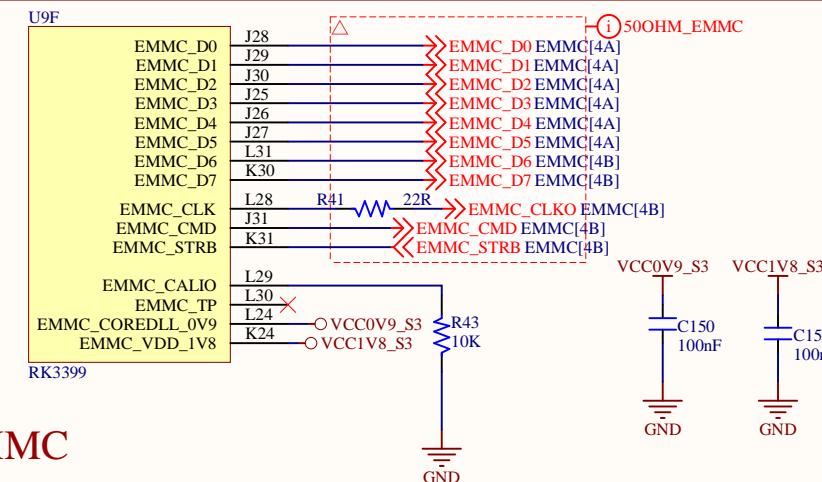
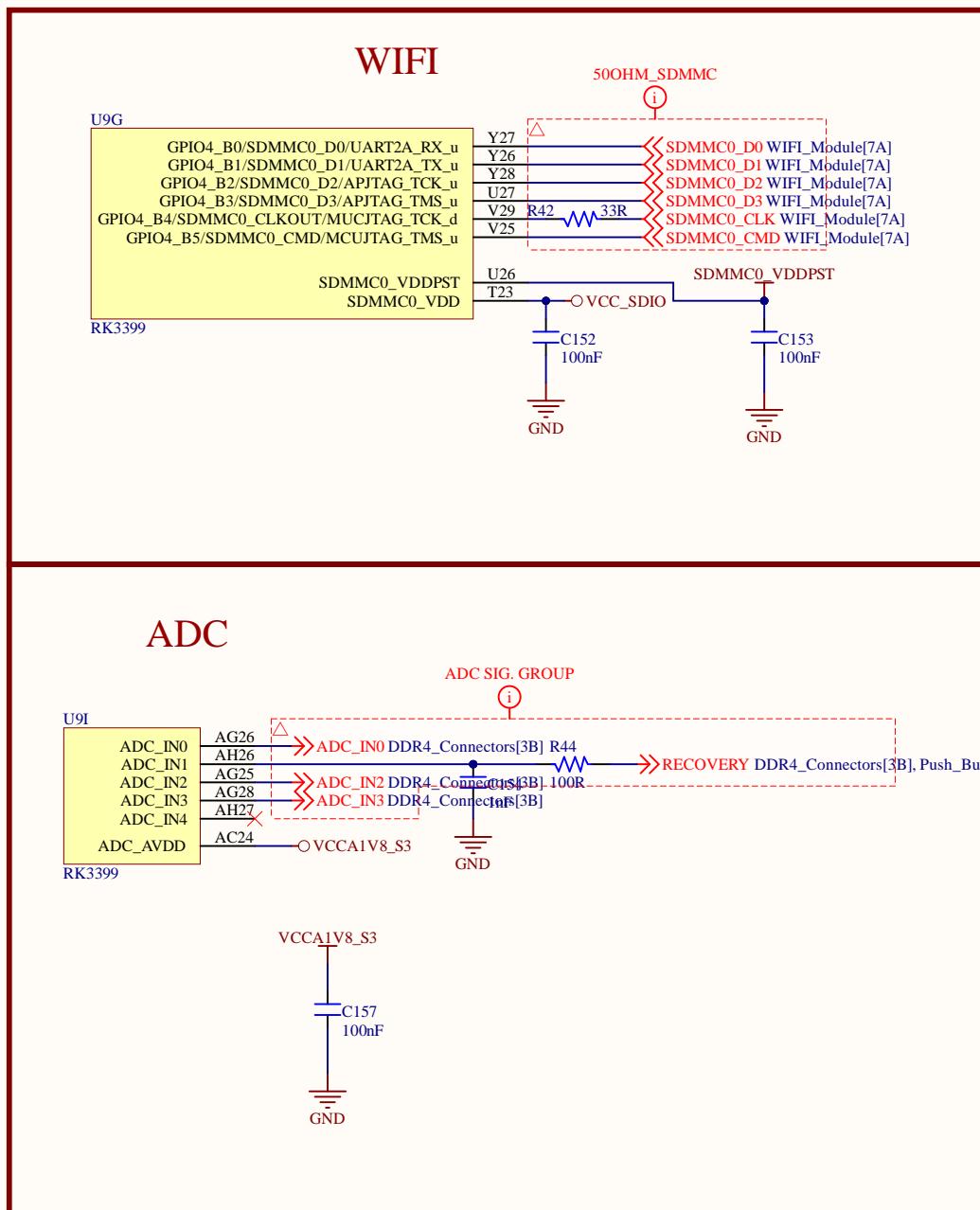


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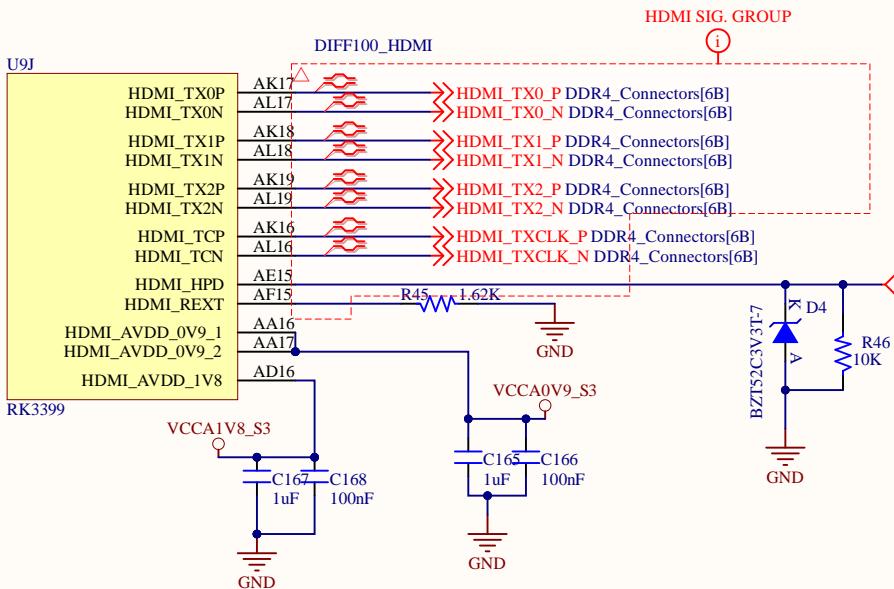
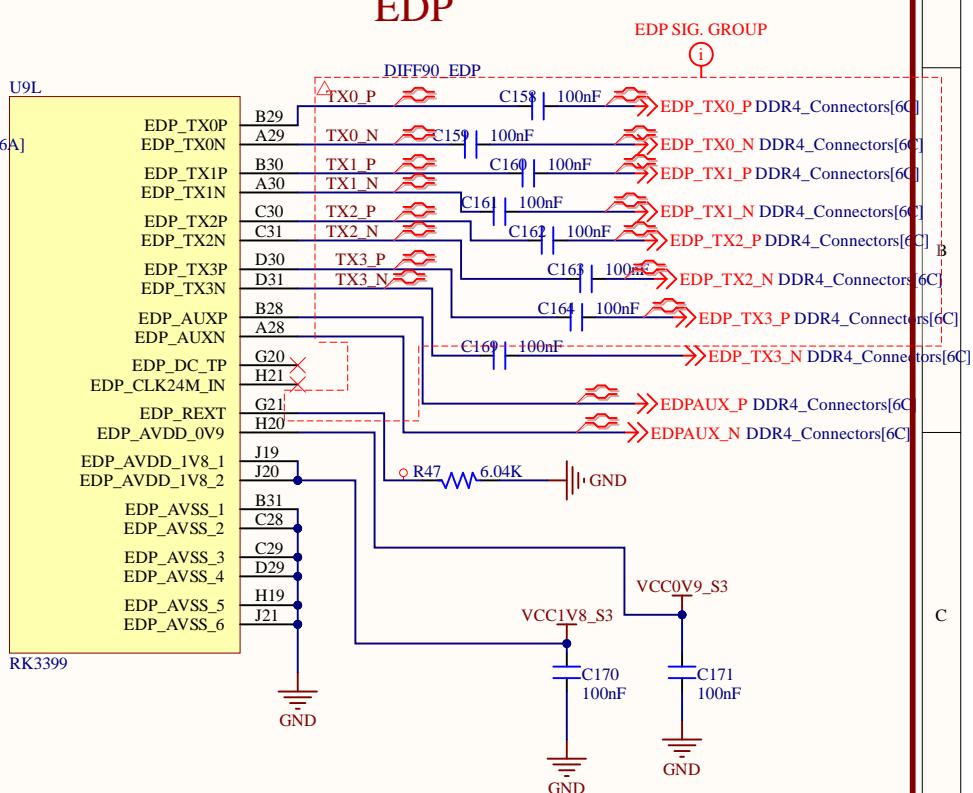
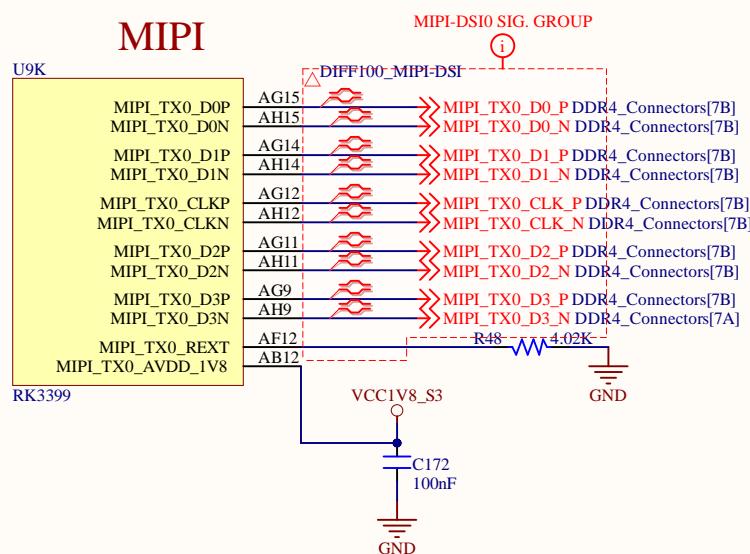
## LPDDR4 of CPU



Title		
Size	Number	Revision
A3		
Date:	8/14/2025	Sheet of
File:	D:\High Speed PCB\PK3390-SDRAM	16page-Bill SchDoc



Title		
Size	Number	Revision
A4		
Date: 8/14/2025	Sheet of	
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**HDMI****EDP****MIPI**

Title

Size

A4

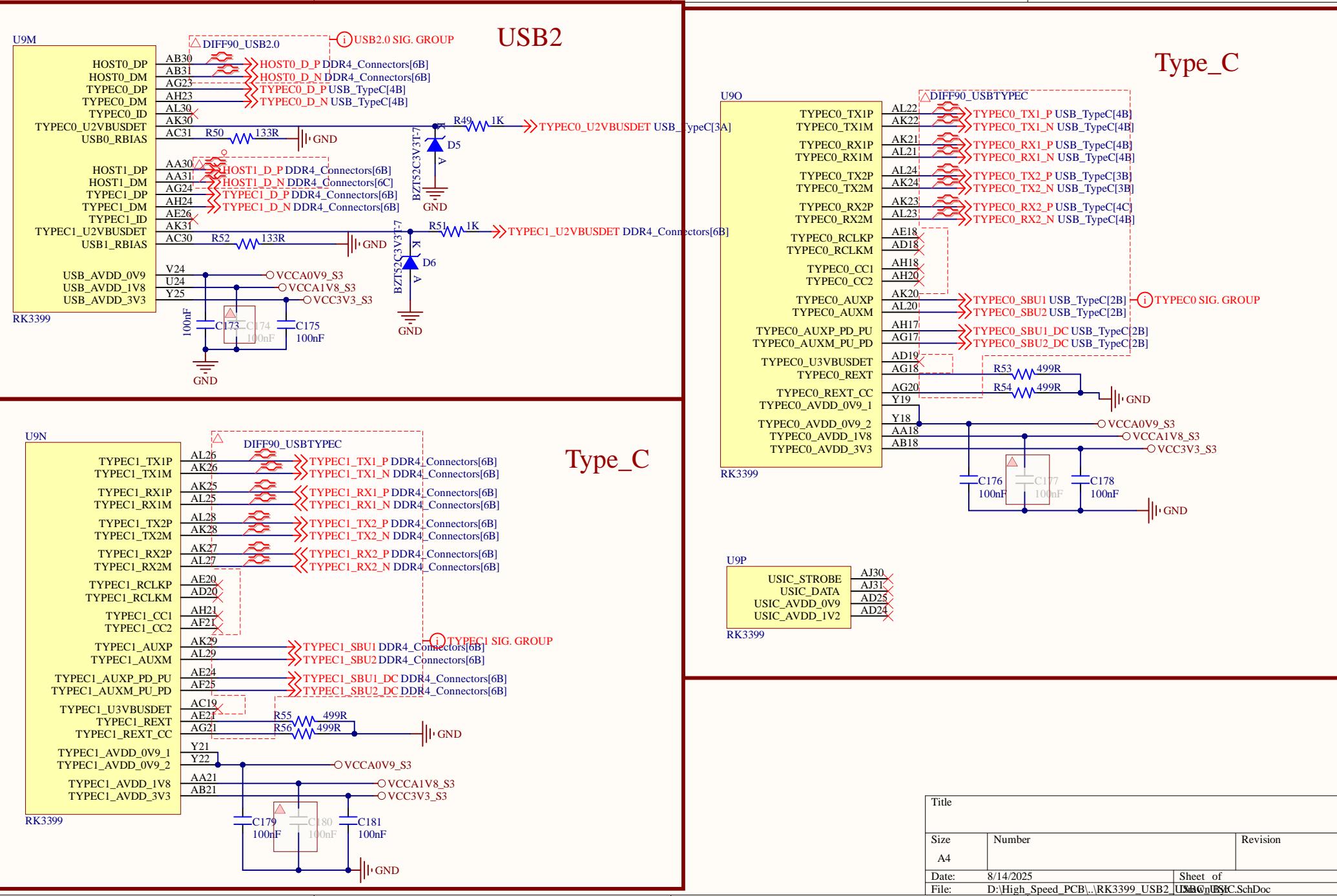
Number

Revision

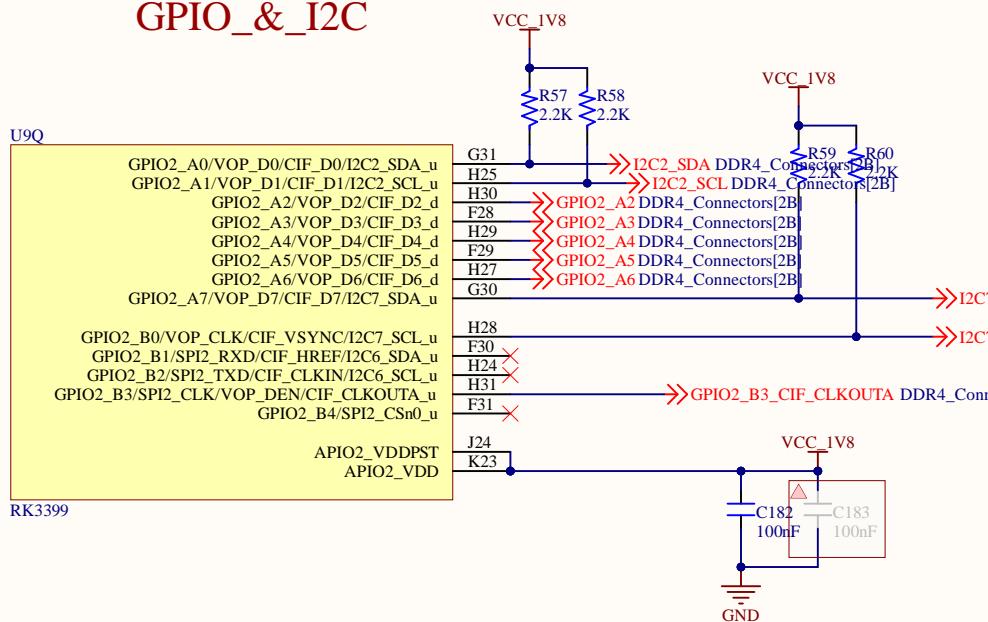
Date: 8/14/2025

Sheet of

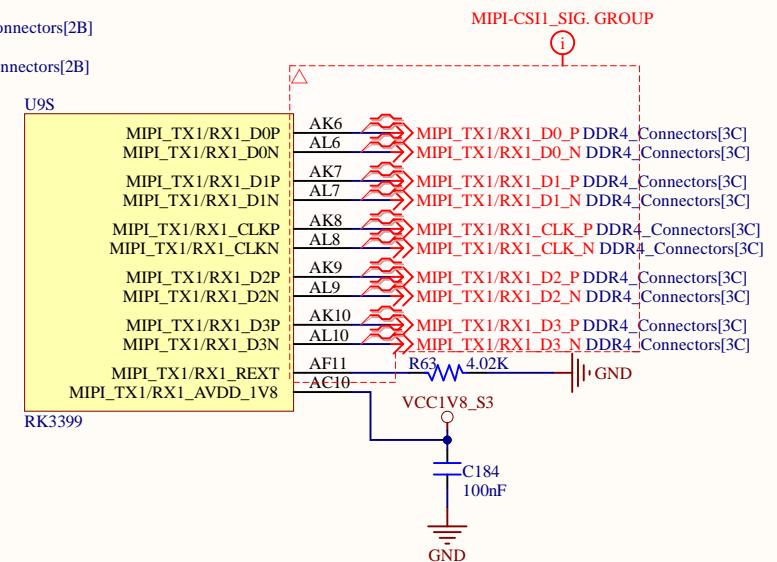
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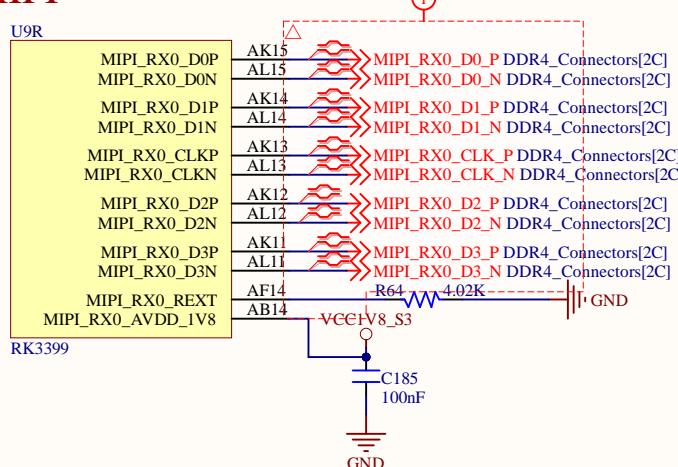
## GPIO\_&\_I2C



## MIPI



## MIPI



Title

Size

A4

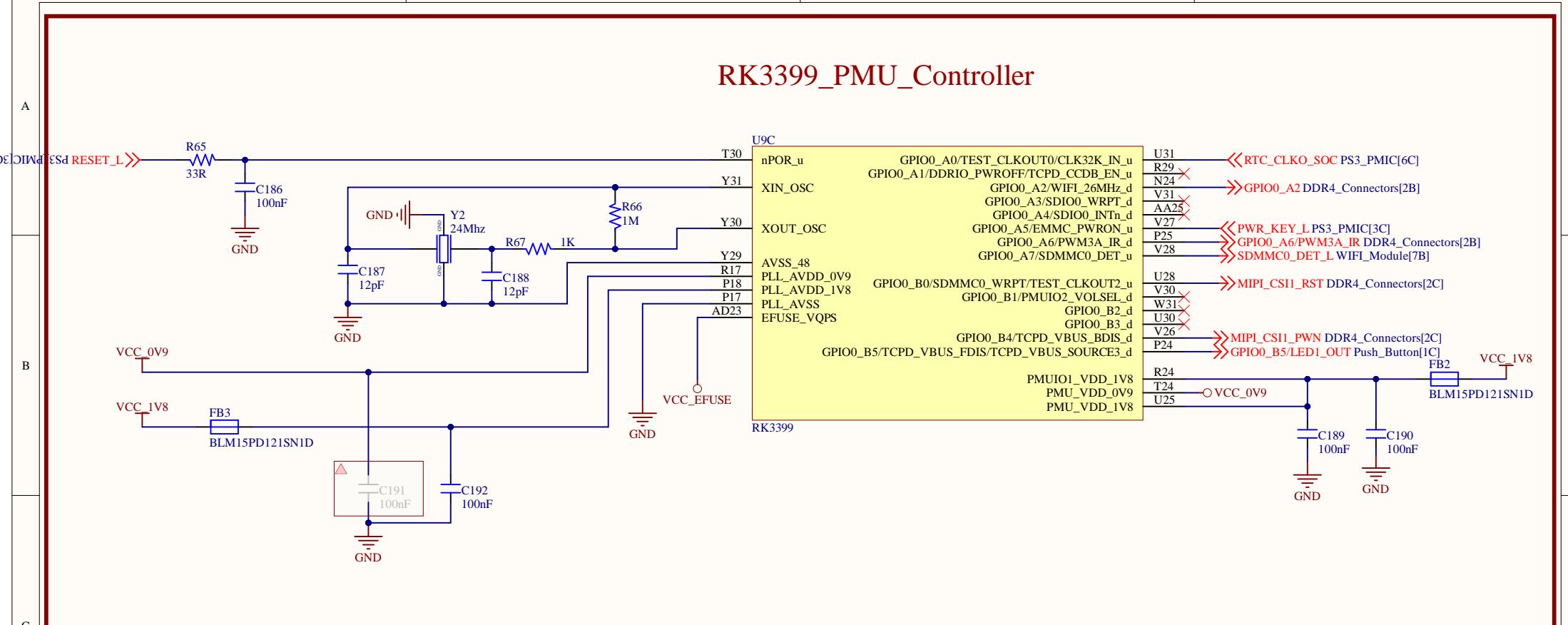
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Revision

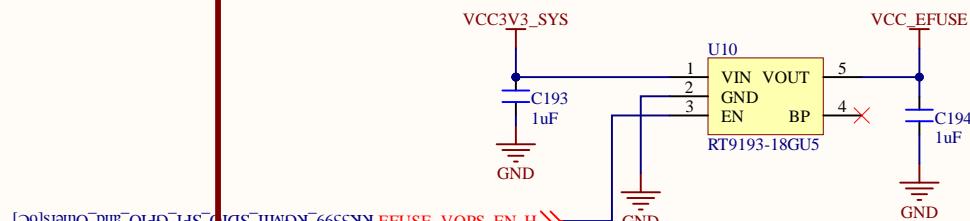
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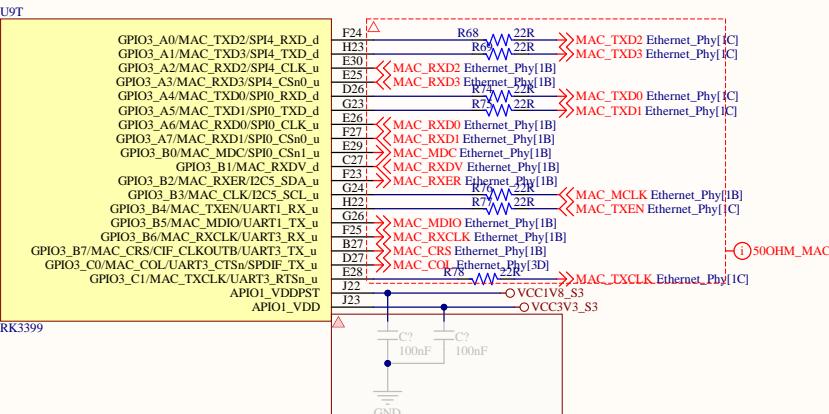
# RK3399\_PMU\_Controller



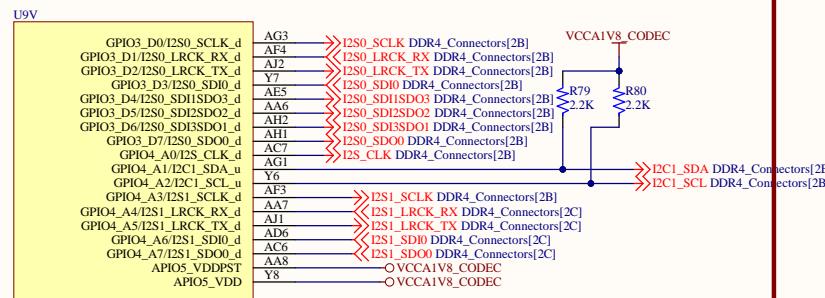
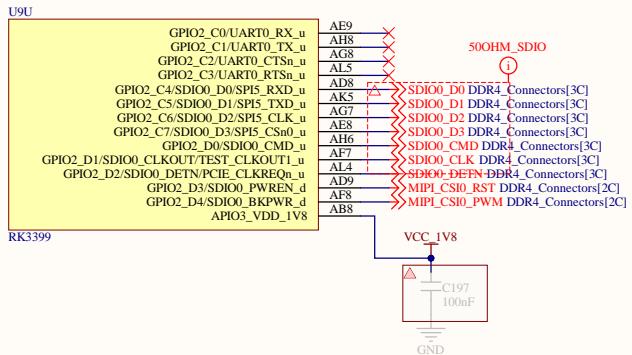
## 1V8\_Regulator



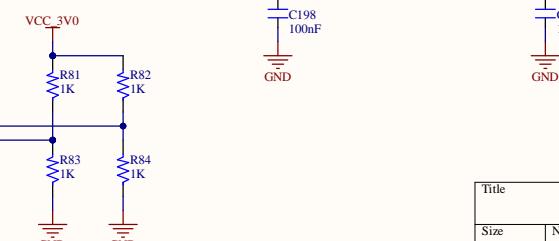
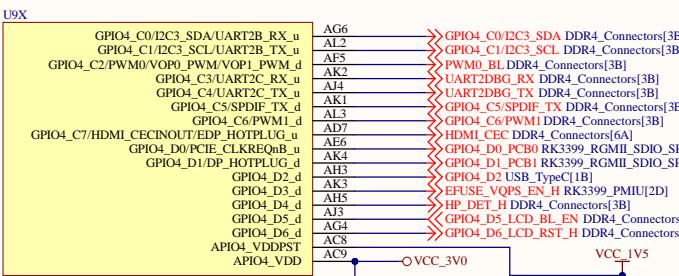
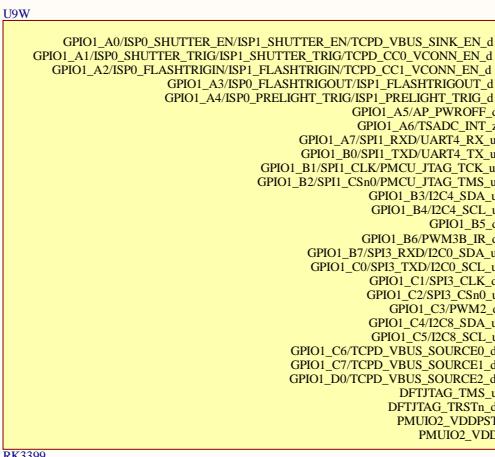
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SDIO



I2S

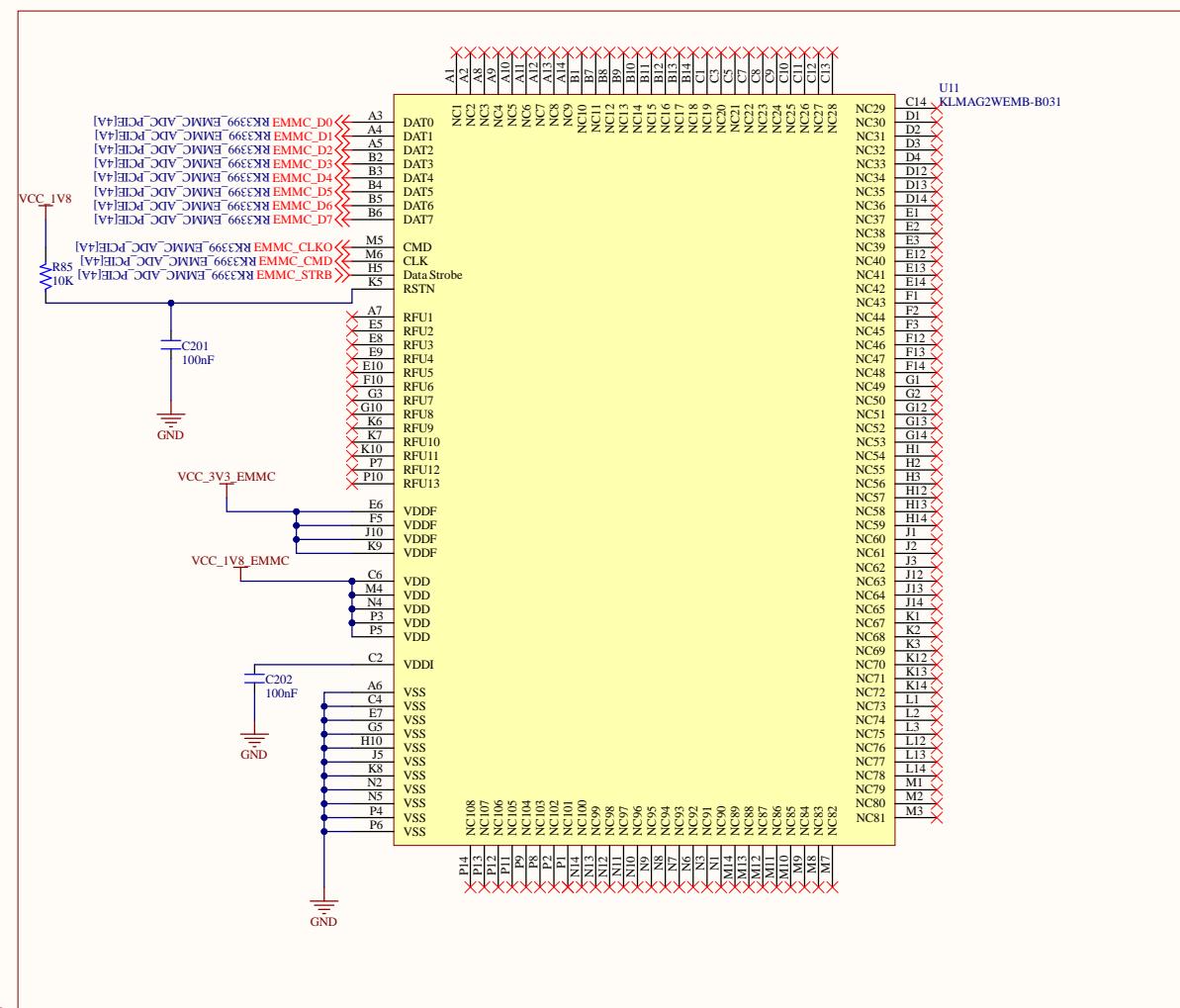


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Date:	8/14/2025	Sheet of
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**KLMAG2WEPD-B031**  
16GB NAND Flash Memory

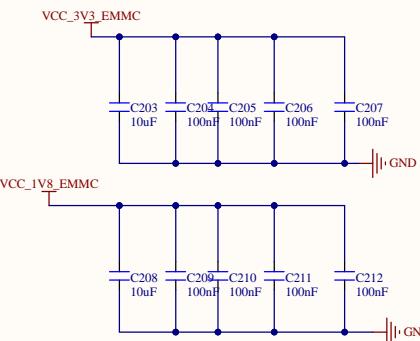
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A



B

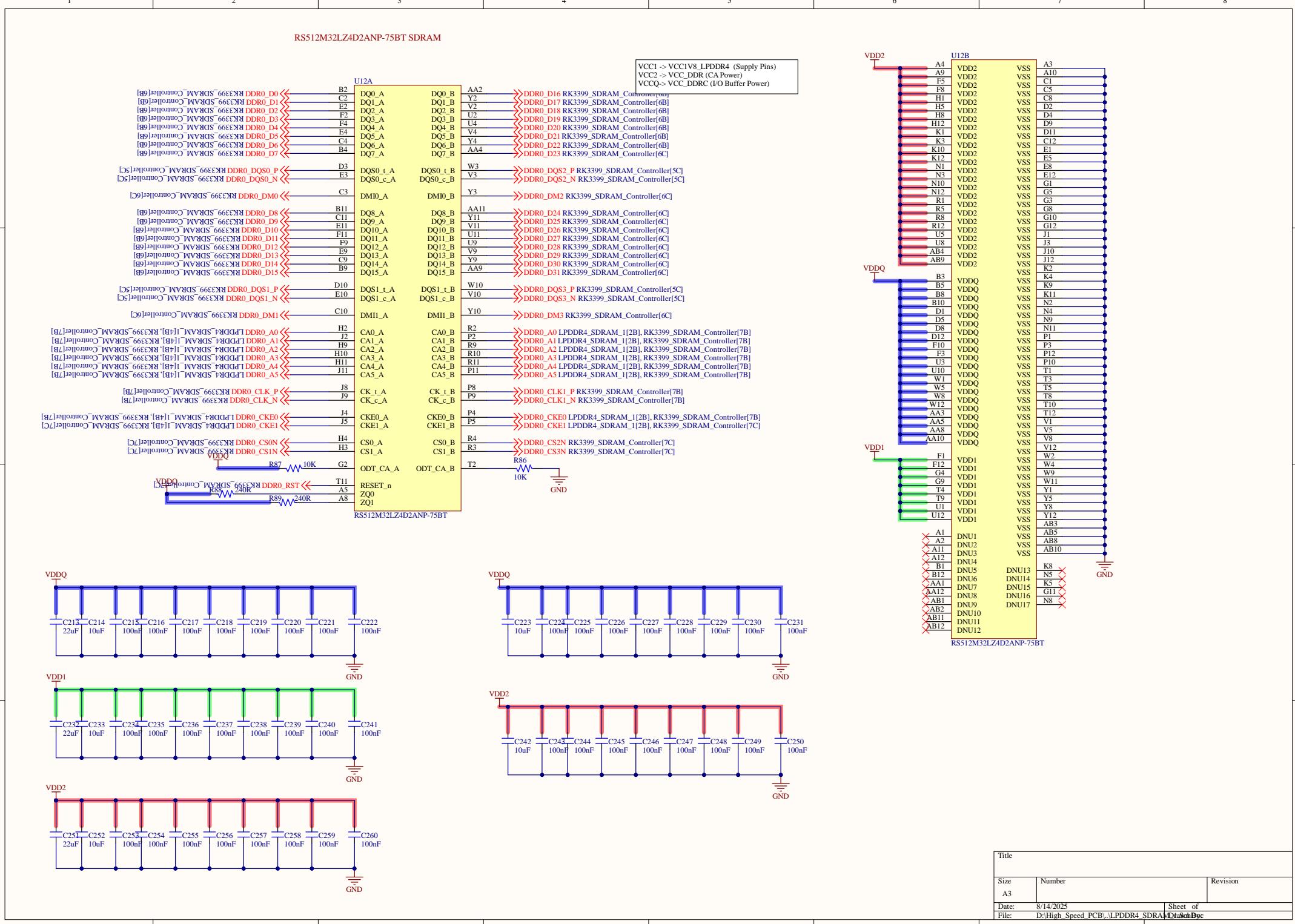
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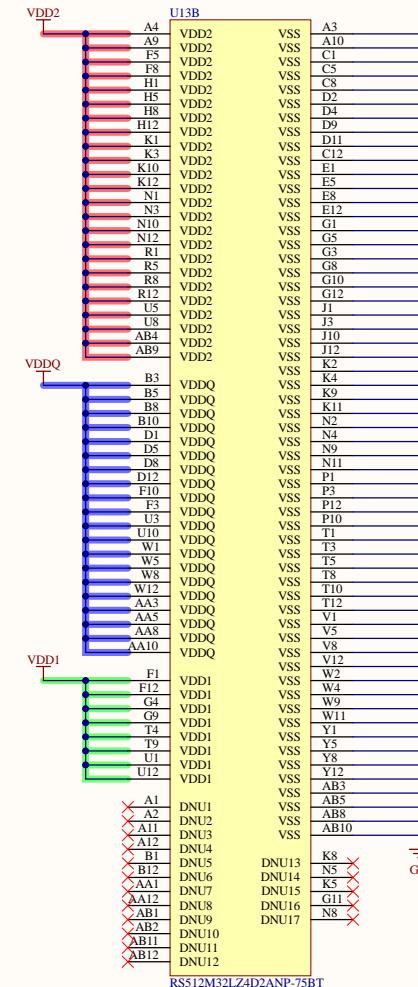
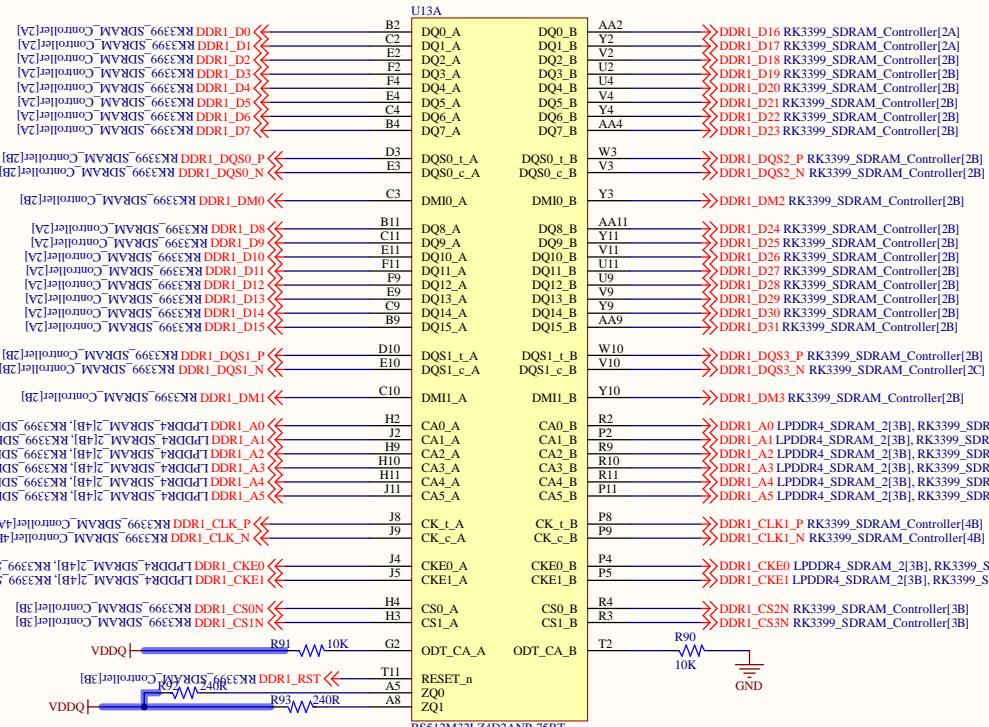


C

C

Title		
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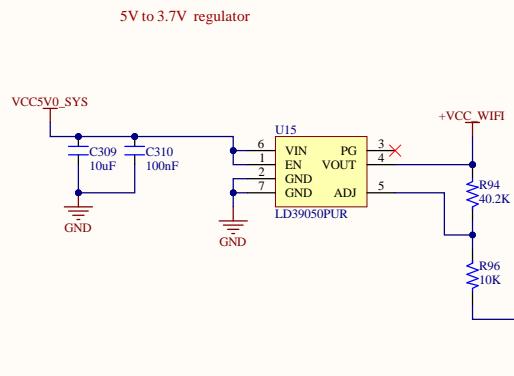




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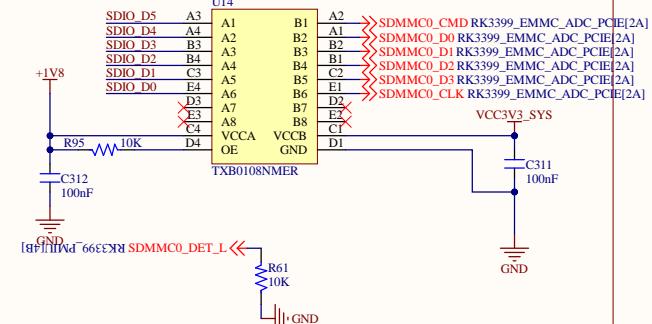
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## 5V to 3.7V regulator



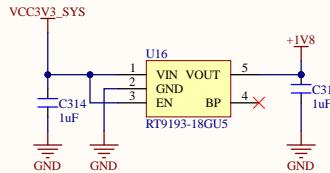
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## Level Shifter



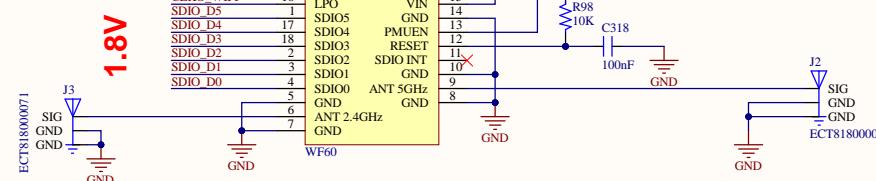
A

## 3.3V to 1.8V regulator



C

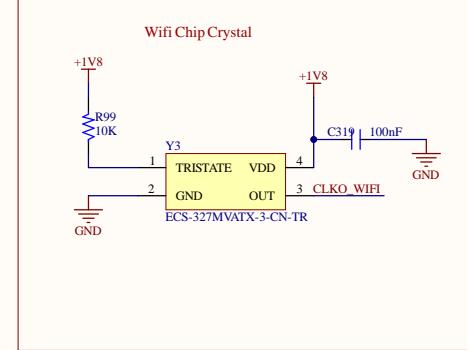
## Wifi\_Module\_WF60



B

D

## Wifi Chip Crystal



C

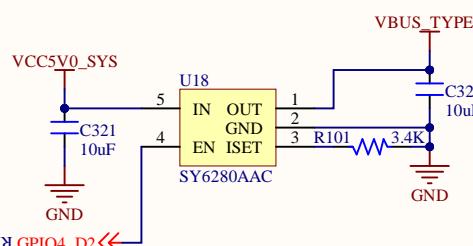
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## Title

Size	Number	Revision
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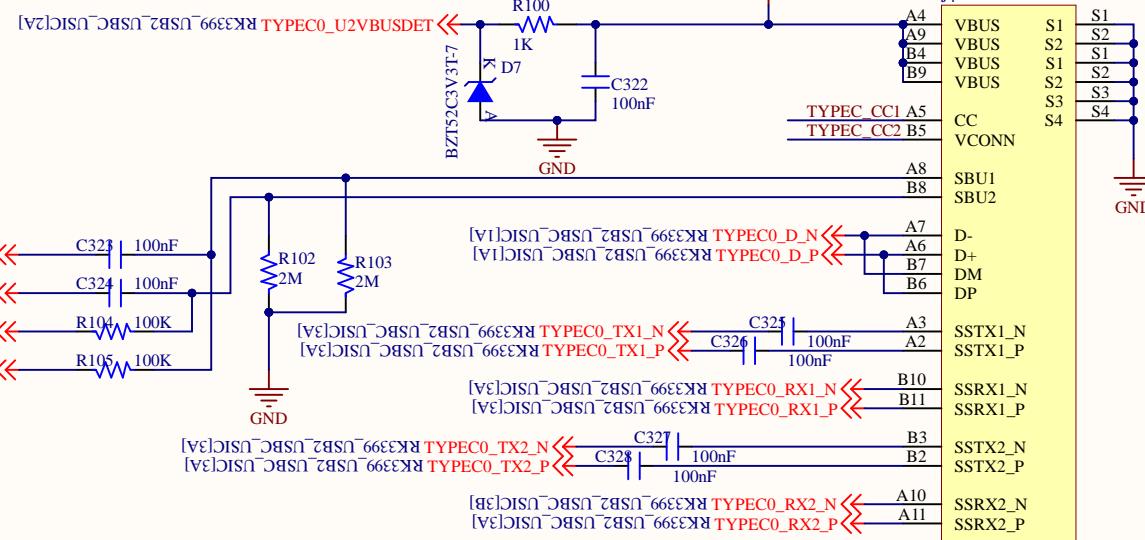
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File: D:\High\_Speed\_PCB\WiFi\_Module.SchDoc By:

1 2 3 4

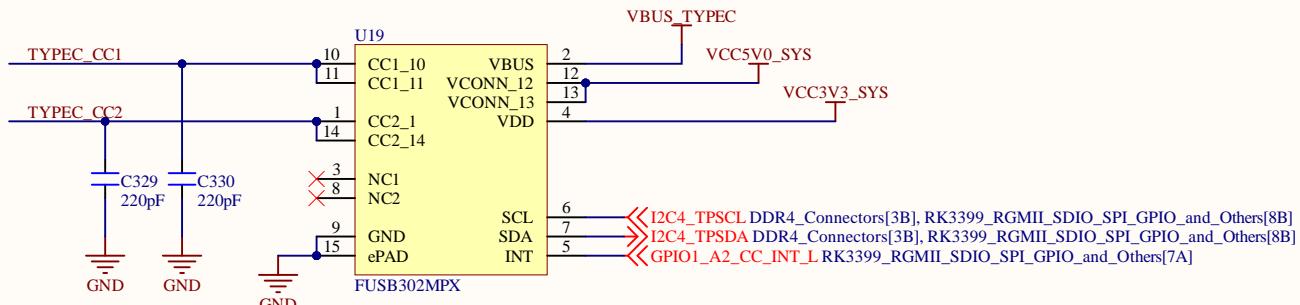


VCC5V0\_SYS  
IN OUT GND EN ISET  
SY6280AAC  
C320 10uF  
GND

VBUS\_TYPEC  
C320 10uF  
GND



VBUS\_TYPEC  
BZT52C3V3T-7  
R100 1K  
D7  
C322 100nF  
GND  
TYPEPEC0\_U2VBUSDET  
RK3399\_USB2\_USBC\_USIC[2a]  
TYPEPEC0\_SBU1  
RK3399\_USB2\_USBC\_USIC[3b]  
TYPEPEC0\_SBU2  
RK3399\_USB2\_USBC\_USIC[3b]  
TYPEPEC0\_SBU1\_DC  
RK3399\_USB2\_USBC\_USIC[3a]  
TYPEPEC0\_SBU2\_DC  
RK3399\_USB2\_USBC\_USIC[3a]  
TYPEPEC0\_RX1\_N  
RK3399\_USB2\_USBC\_USIC[3a]  
TYPEPEC0\_RX1\_P  
RK3399\_USB2\_USBC\_USIC[3a]  
TYPEPEC0\_TX1\_N  
RK3399\_USB2\_USBC\_USIC[3a]  
TYPEPEC0\_TX1\_P  
RK3399\_USB2\_USBC\_USIC[3a]  
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RK3399\_USB2\_USBC\_USIC[3a]  
TYPEPEC0\_RX2\_P  
RK3399\_USB2\_USBC\_USIC[3a]

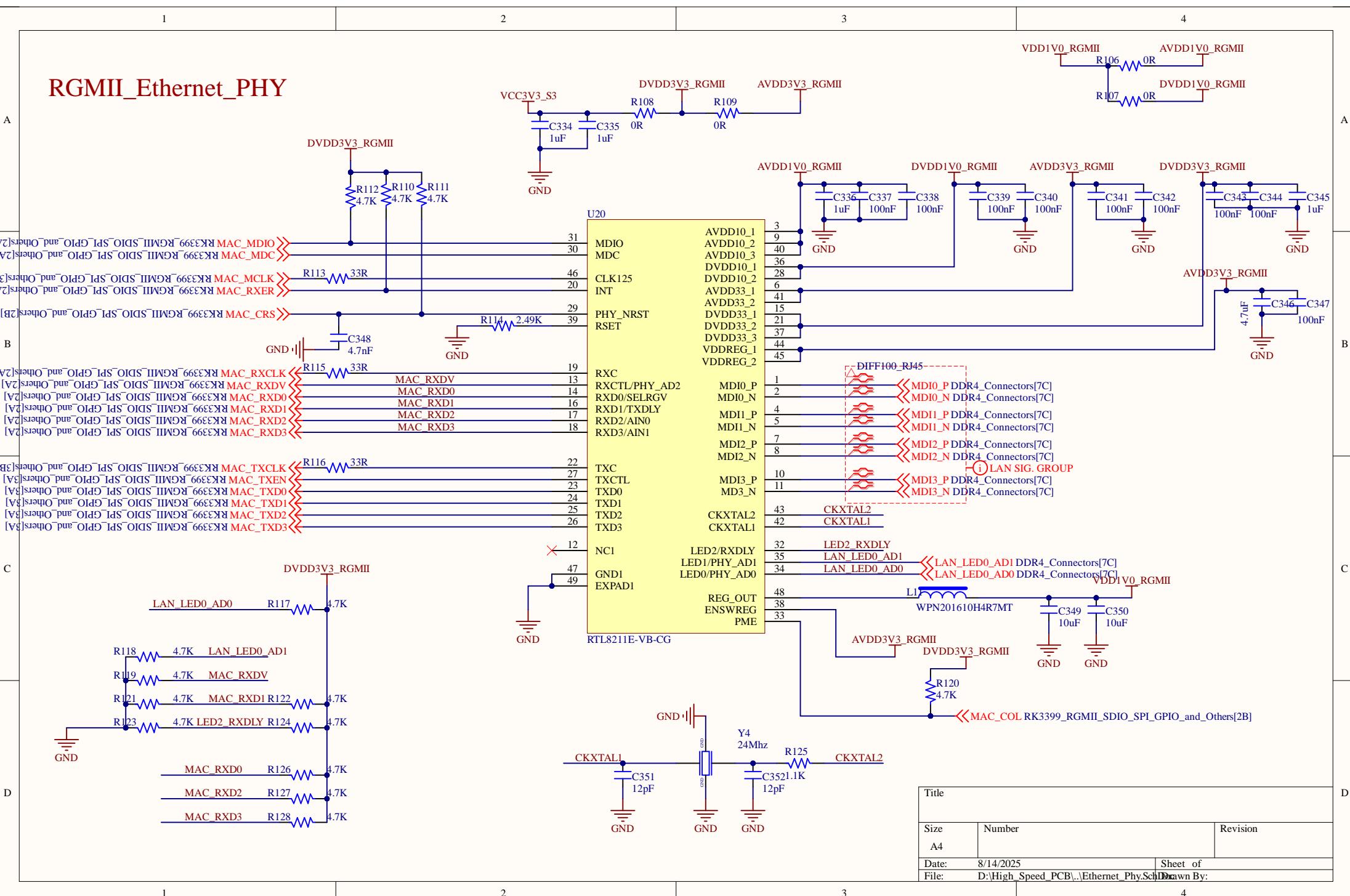


VCC3V3\_SYS VBUS\_TYPEC VCC5V0\_SYS  
C331 1uF C332 1uF C333 1uF  
GND

Title		
Size	Number	Revision
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Date:	8/14/2025	Sheet of
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1 2 3 4

# RGMII\_Ethernet\_PHY



A

A

B

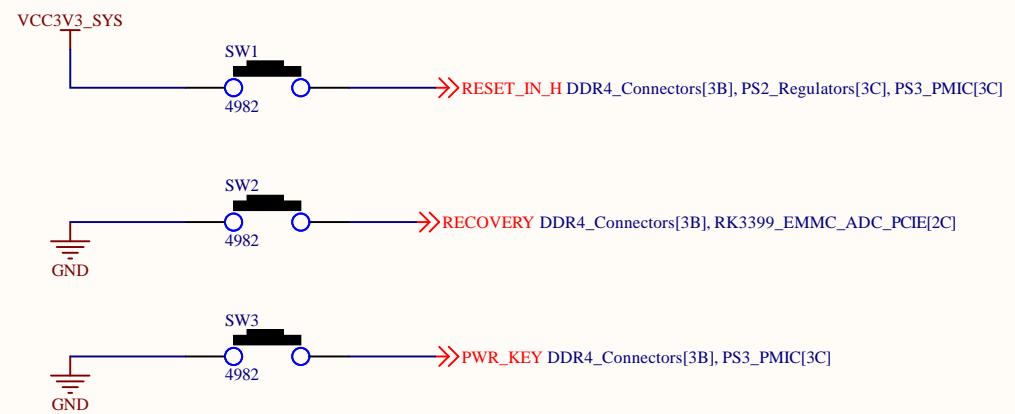
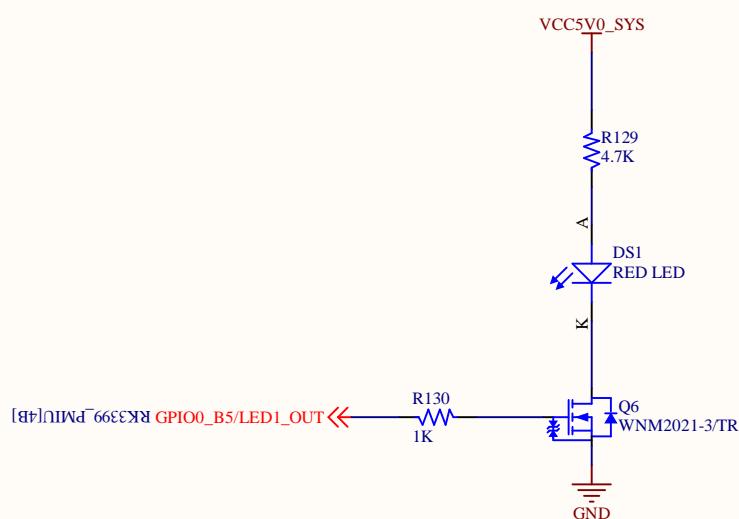
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C

C

D

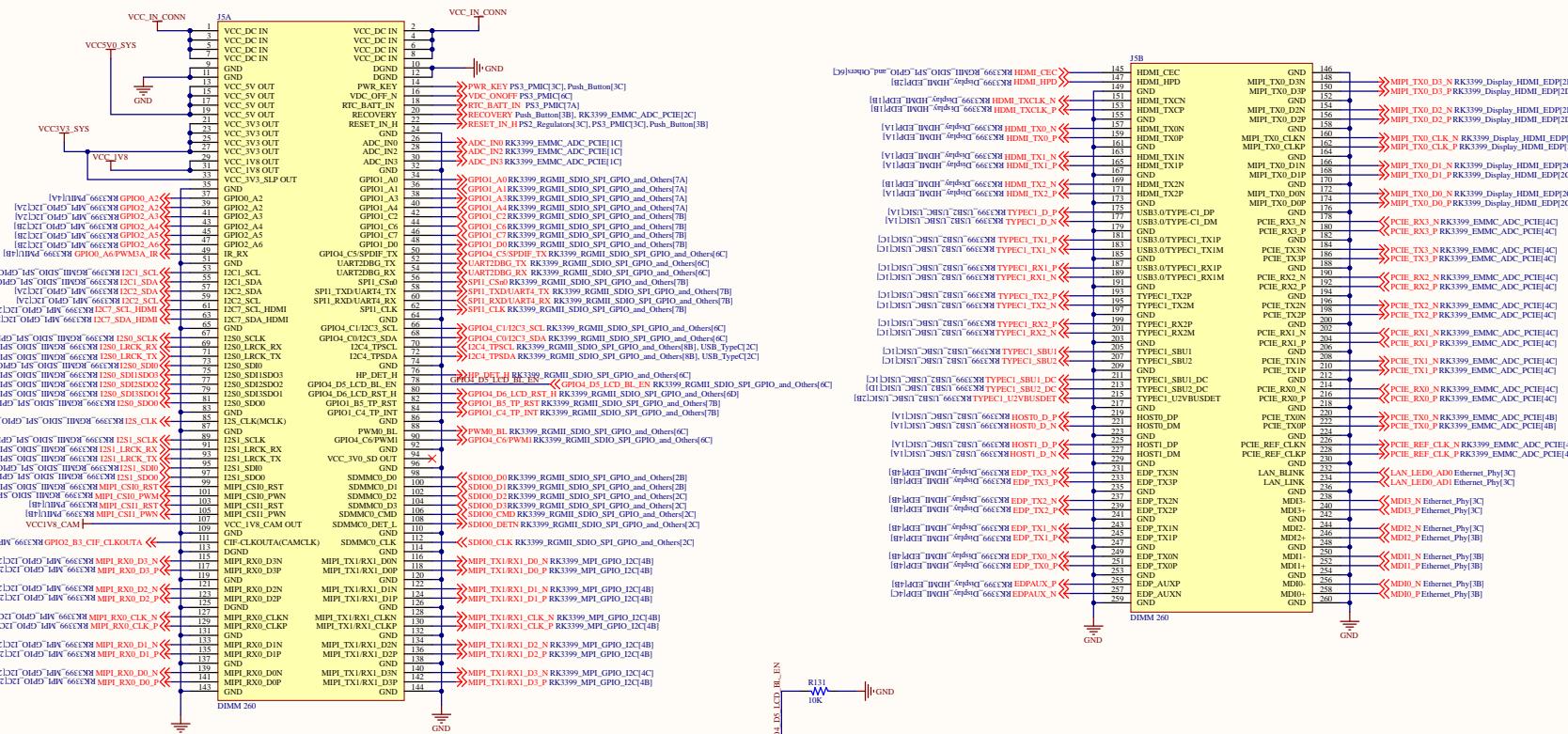
D



Title		
Size	Number	Revision
A4		
Date: 8/14/2025	Sheet of	
File: D:\High_Speed_PCB\Push_Button.Sch	10	Drawn By:

## DIMM 260 DDR4 CONNECTOR

VCC\_IN\_CONN= 12V input



### Mounting Holes



Title		
Size	Number	Revision
A2		
Date: 8/14/2025	Sheet of	
File: D-High Speed PCB-DDR4 Connector	Digital	

