

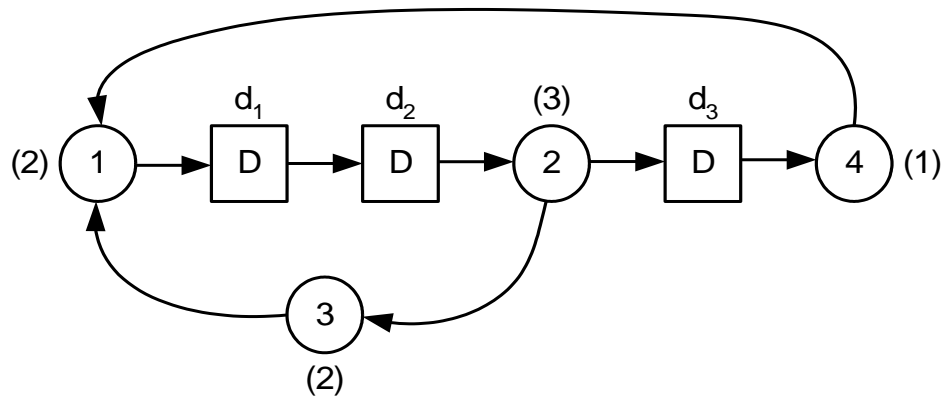
DSP in VLSI Design

Homework (II)

Iteration Bound

Deadline: Oct. 6

1. For the DFG shown in the following figure, where the computation times of the nodes are shown in parentheses, compute the iteration bound of this DFG using LPM algorithm.



Please deliver the homework in PDF format on NTU COOL