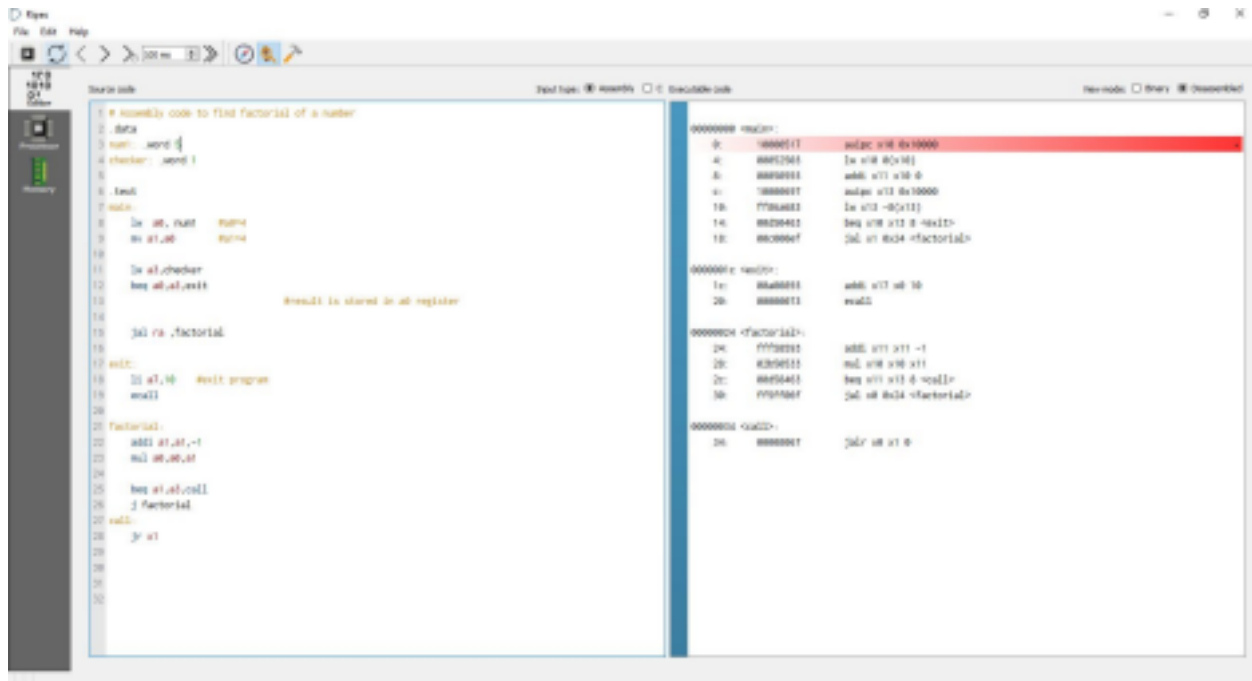


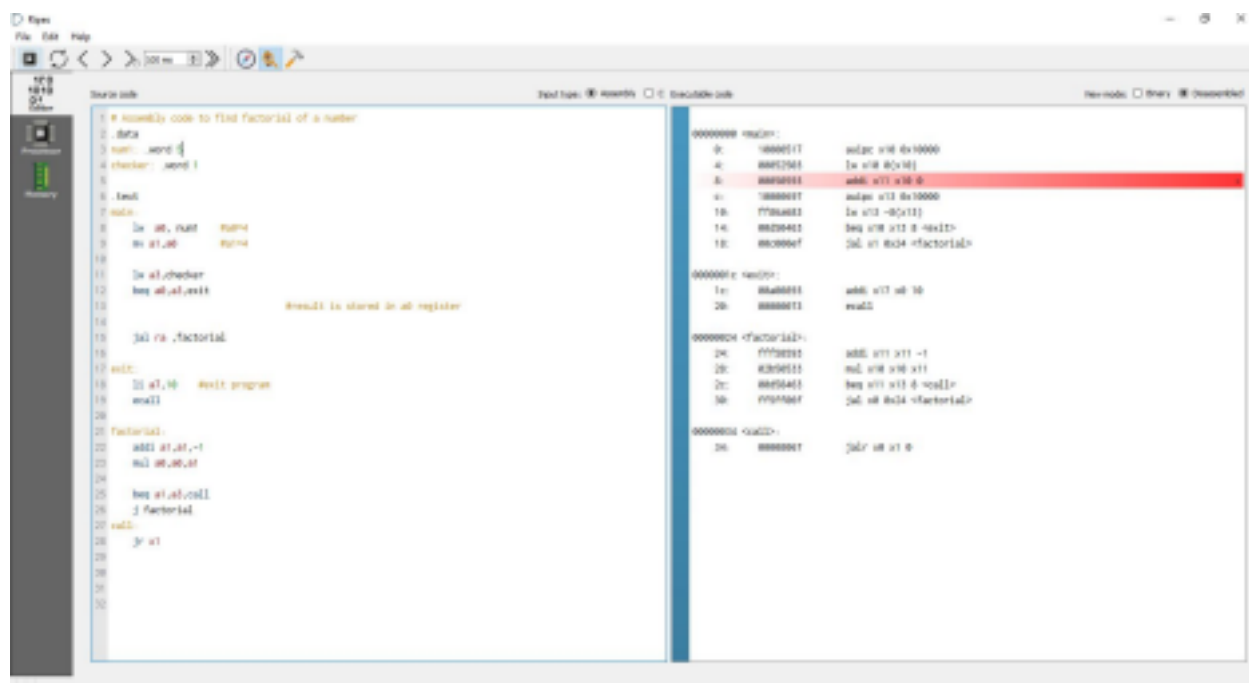
# COAL ASSIGNMENT

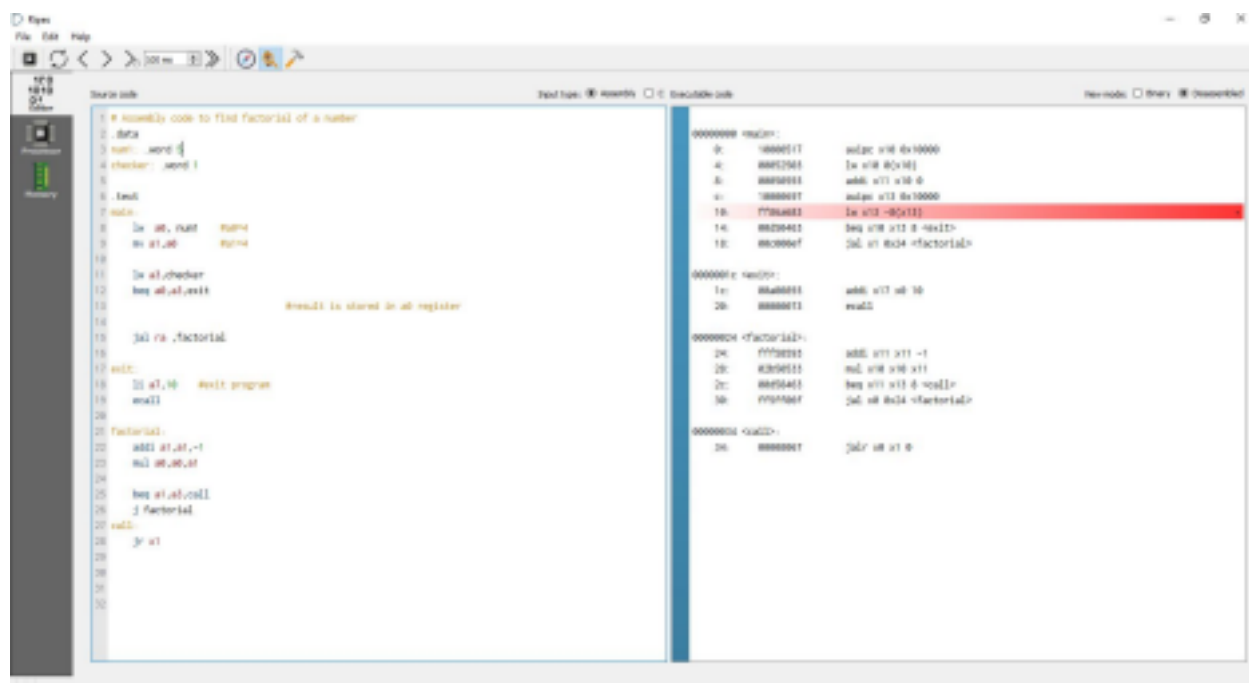
Abdul Hannan  
BS(CS)3-A

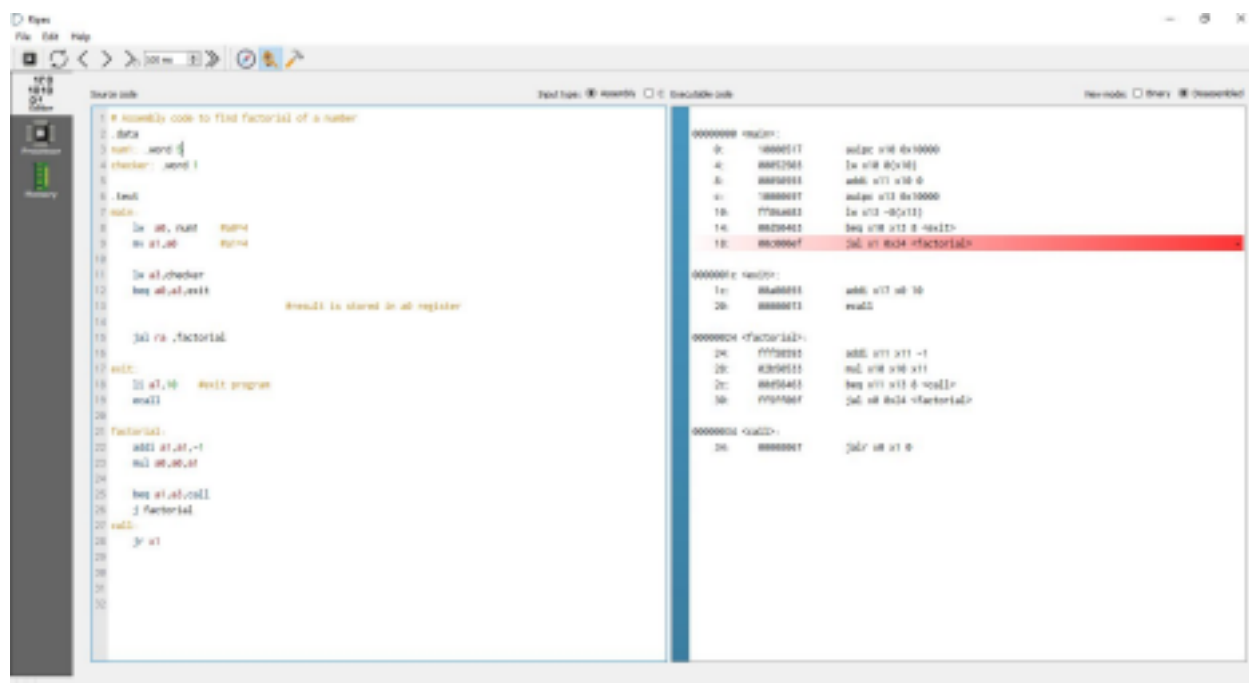
## Single core processor:

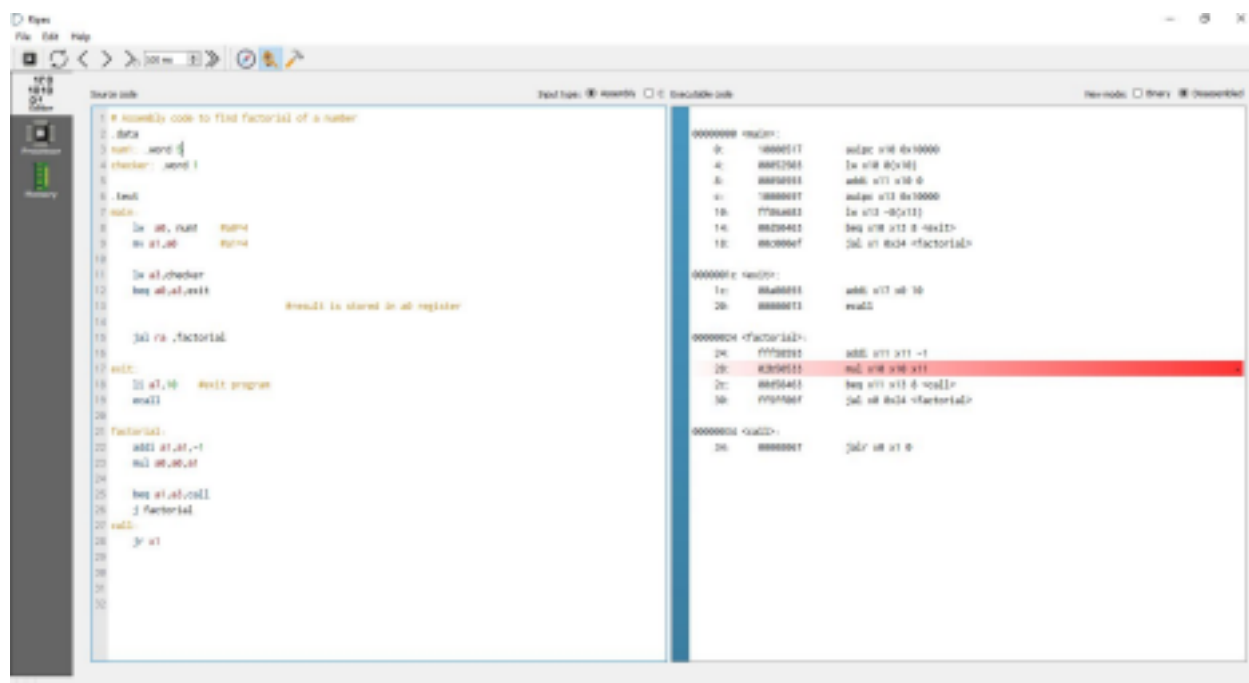
All the steps are shown in the screenshots attached below:

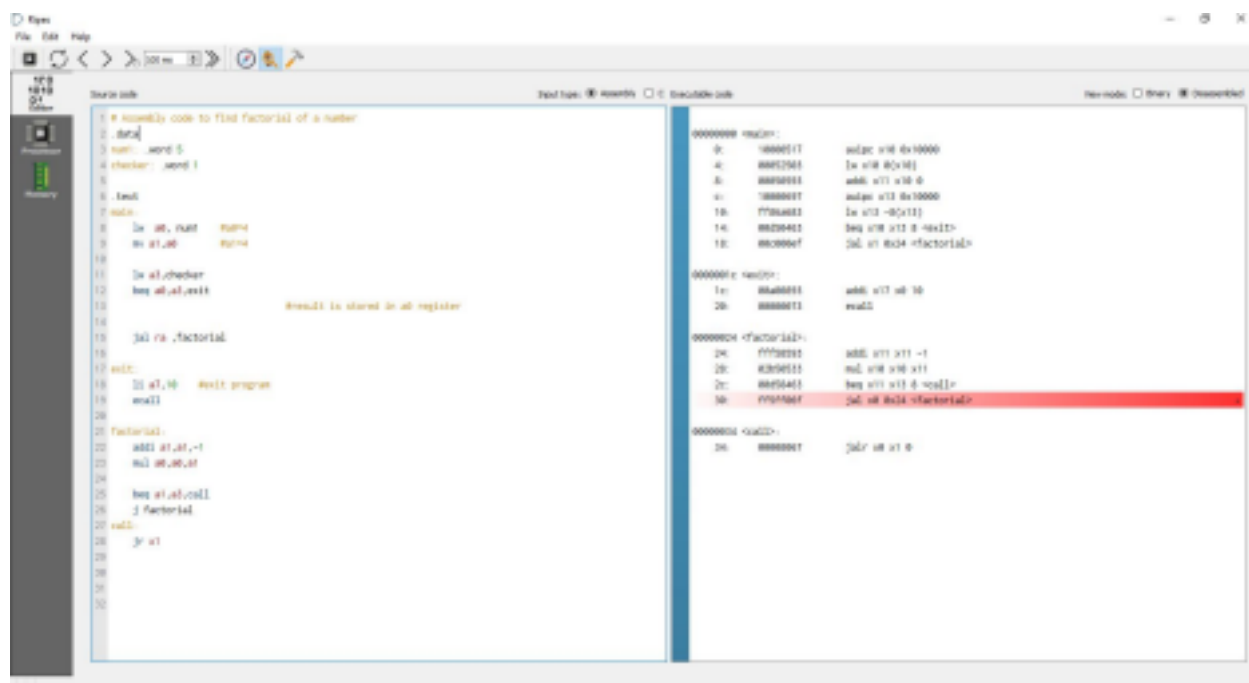


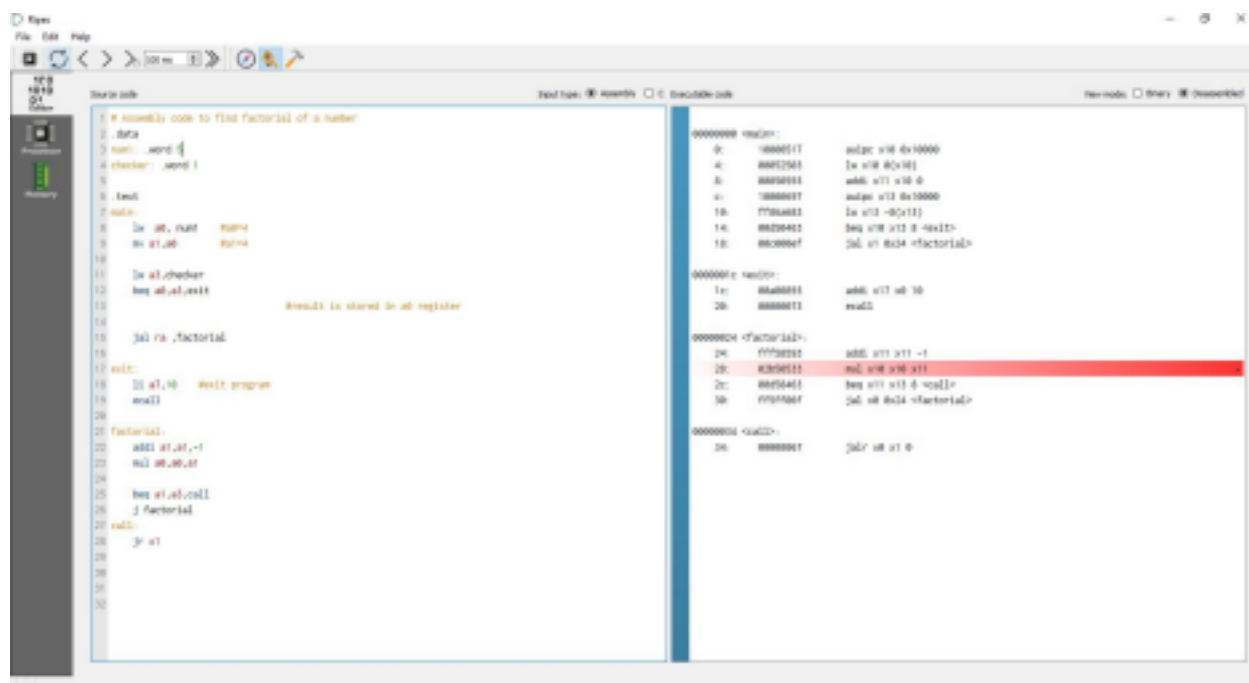


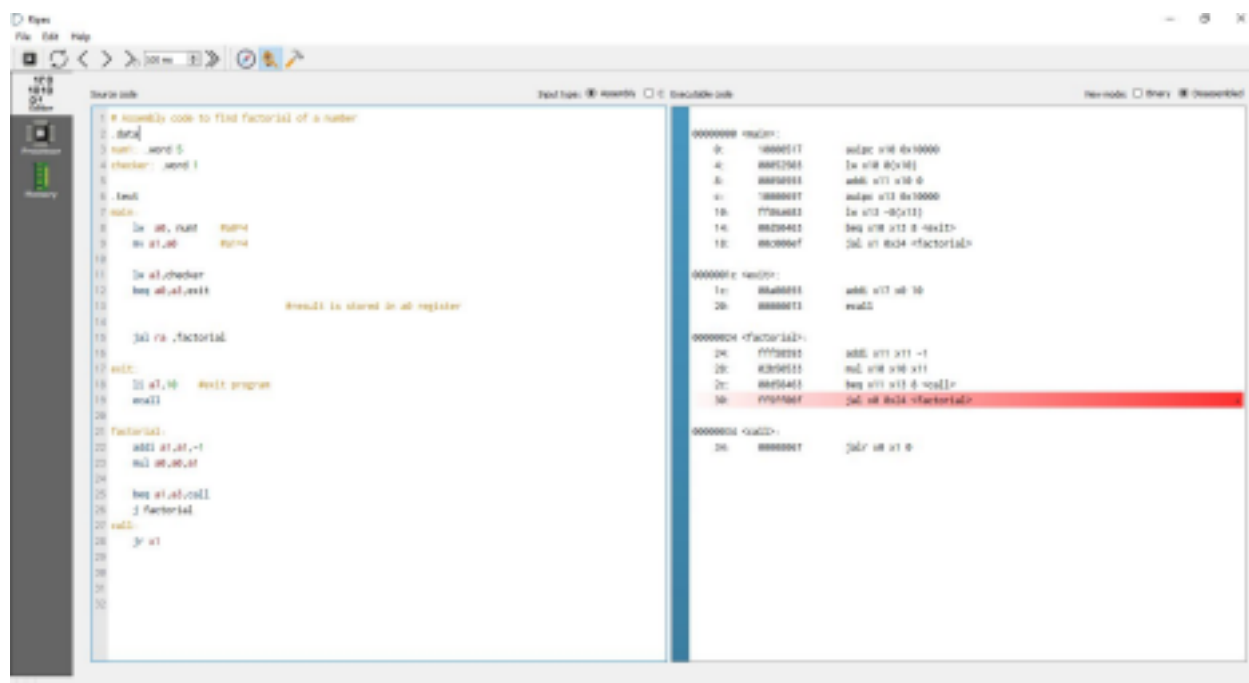
























Single core processor stages:

Result is stored in a0 register



## **Five core Processor:**

All the steps are shown in the screenshots attached below:





















































Five core processor stages:

Result is stored in a0 register

