

Instruction	Operation	Operands	R1	R2	R3	R4	R10	R11	Memory
0100	LIL	R10, 0Fh	U	U	U	U	0x0F	U	
0101	LD	R1, R10, 0	0x05	U	U	U	0x0F	U	Loads 0x05 from memory location 0x0F
0102	LIL	R2, 04h	0x05	0x04	U	U	0x0F	U	
0103	MOV	R4, R1	0x05	0x04	U	0x05	0x0F	U	
0104	ADD	R4, R2	0x05	0x04	U	0x09	0x0F	U	
0105	LIL	R1, 01h	0x01	0x04	U	0x09	0x0F	U	
0106	MOV	R3, R1	0x01	0x04	0x01	0x09	0x0F	U	
0107	ADD	R3, R4	0x01	0x04	0x0A	0x09	0x0F	U	
0108	XOR	R10, R10	0x01	0x04	0x0A	0x09	0x00	U	
0109	ADD	R2, R10	0x01	0x04	0x0A	0x09	0x00	U	
010A	ADD	R2, R3	0x01	0x0E	0x0A	0x09	0x00	U	
010B	ADD	R2, R3	0x01	0x18	0x0A	0x09	0x00	U	
010C	SUB	R2, R4	0x01	0x0F	0x0A	0x09	0x00	U	
010D	SL	R11, R2	0x01	0x0F	0x0A	0x09	0x00	0x1E	
010F	ST	R2, R11, 0	0x01	0x0F	0x0A	0x09	0x00	0x1E	Stores 0x0F into memory location 0x1E

Highlighted register is the one that is changed with each instruction. U represents uninitialized registers.