

## ENET Test Report

**Overall result: Pass**

DUT: BD642A\_OUT PORT\_A\_PAIR  
Comment: Temperature is PHY IC.  
Time of session start: 03/23/2023 10:37:51  
Operator: Lyoo.H.S.  
Temperature: 28° C  
Standard in use: ENET

Session ID: 64, Continuation #: 1:

Time of run: 2023/03/23 10:37:56  
Configuration in use: 10/100BASE-T All tests (Copy)  
Limits in use: Default  
Oscilloscope Name: LCRY2805N56639 Model: WR640ZI  
Oscilloscope Serial #: LCRY2805N56639  
Computer: LCRY2805N56639  
Oscilloscope firmware version: 9.2.0.4 (Build 278085)  
QualiPHY core version: 8.7.0.1 (Build 255738)

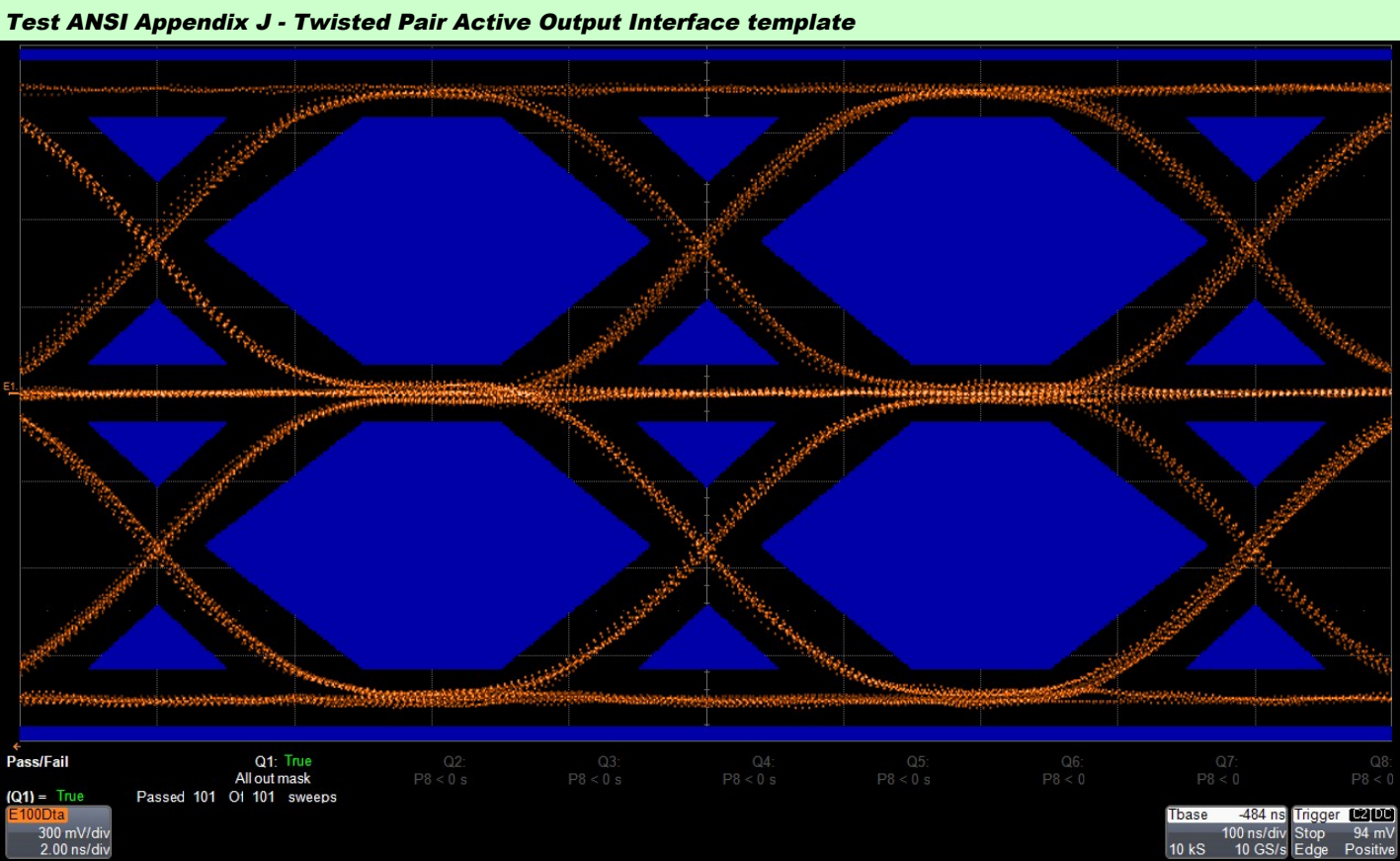
QualiPHY script version: 8.7.0.1  
Stylesheet version: 1.2.0.7

# Summary Table

[\[Hide Table\]](#)


Pass	#	Test	Measurement	Current Value	Test Criteria
✓	1	ANSI Appendix J	<a href="#">Twisted Pair Active Output Interface template</a>	AllPass	match
✓	1	ANSI 9.1.9	<a href="#">Jitter Base to Upper</a>	588 ps	$x \leq 1.400 \text{ ns}$
✓	1	ANSI 9.1.9	<a href="#">Jitter Base to Lower</a>	566 ps	$x \leq 1.400 \text{ ns}$
✓	1	ANSI 9.1.2.2	<a href="#">UTP DOV Base to Upper</a>	993.4 mV	$950.0 \text{ mV} < x < 1.0500 \text{ V}$
✓	1	ANSI 9.1.2.2	<a href="#">UTP DOV Base to Lower</a>	1.0014 V	$950.0 \text{ mV} < x < 1.0500 \text{ V}$
✓	1	ANSI 9.1.4	<a href="#">Signal Amplitude Symmetry</a>	992.0 m	$980.0 \text{ m} < x < 1.0200$
✓	1	ANSI 9.1.3	<a href="#">Overshoot Positive</a>	2.3 %	$x \leq 5.0 \%$
✓	1	ANSI 9.1.3	<a href="#">Overshoot Negative</a>	2.1 %	$x \leq 5.0 \%$
✓	1	ANSI 9.1.6	<a href="#">Rise Base to Upper</a>	3.812 ns	$x = 4.000 \text{ ns} \pm 1.000 \text{ ns}$
✓	1	ANSI 9.1.6	<a href="#">Fall Upper to Base</a>	3.817 ns	$x = 4.000 \text{ ns} \pm 1.000 \text{ ns}$
✓	1	ANSI 9.1.6	<a href="#">Rise Lower to Base</a>	4.101 ns	$x = 4.000 \text{ ns} \pm 1.000 \text{ ns}$
✓	1	ANSI 9.1.6	<a href="#">Fall Base to Lower</a>	3.846 ns	$x = 4.000 \text{ ns} \pm 1.000 \text{ ns}$
✓	1	ANSI 9.1.6	<a href="#">Rise/Fall Symmetry</a>	289 ps	$x \leq 500 \text{ ps}$
✓	1	ANSI 9.1.8	<a href="#">Duty Cycle Distortion</a>	50.6 ps	$-250.0 \text{ ps} < x < 250.0 \text{ ps}$

# Details




100Base-TX Template, scale factor 1.05  
Timestamp: 03/23/2023 10:38:19


[\[ Up \]](#)

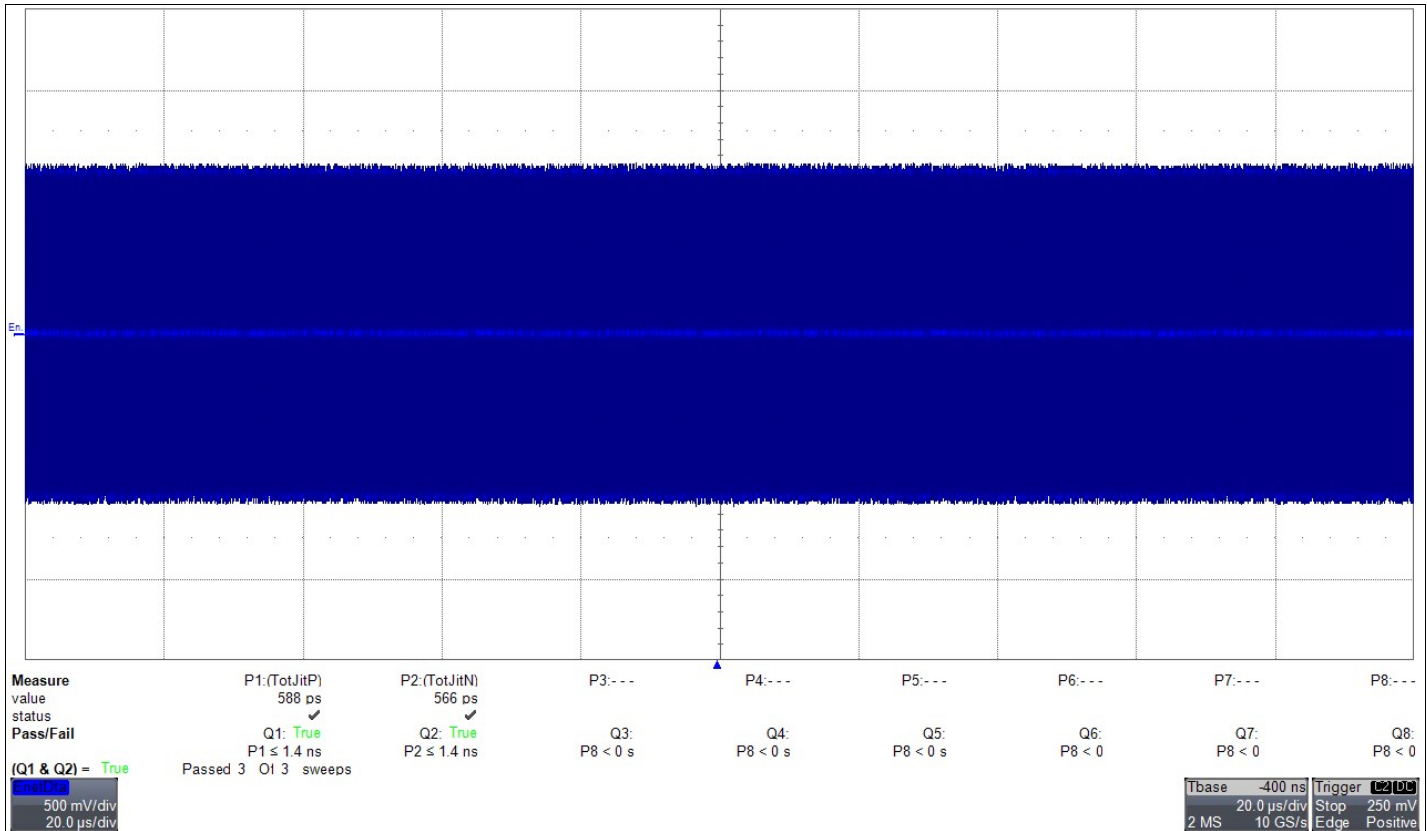
 <b>Pass</b>	Measurement: <b>Twisted Pair Active Output Interface template</b>	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 03/23/2023 10:38:19	Limit Name: Mask-Test

**Test ANSI 9.1.9 - Jitter**[\[ Up \]](#)

 <b>Pass</b>	Measurement: <b>Jitter Base to Upper</b>	
	Current Value: 588 ps	Test Criteria: $x \leq 1.400$ ns
	Timestamp: 03/23/2023 10:38:31	Limit Name: 100BT-Jitter

[\[ Up \]](#)


 <b>Pass</b>	Measurement: <b>Jitter Base to Lower</b>	
	Current Value: 566 ps	Test Criteria: $x \leq 1.400$ ns
	Timestamp: 03/23/2023 10:38:31	Limit Name: 100BT-Jitter

**100Base-TX Jitter**


Timestamp: 03/23/2023 10:38:31

### Test ANSI 9.1.2.2 - UTP differential output voltage

[\[Up\]](#)


 <b>Pass</b>	Measurement: <b>UTP DOV Base to Upper</b>		
	Current Value: 993.4 mV	Test Criteria: 950.0 mV < x < 1.0500 V	
	Timestamp: 03/23/2023 10:38:40	Limit Name: 100BT-DOV	

[\[Up\]](#)

 <b>Pass</b>	Measurement: <b>UTP DOV Base to Lower</b>		
	Current Value: 1.0014 V	Test Criteria: 950.0 mV < x < 1.0500 V	
	Timestamp: 03/23/2023 10:38:40	Limit Name: 100BT-DOV	


### Test ANSI 9.1.4 - Signal amplitude symmetry

[\[Up\]](#)


 <b>Pass</b>	Measurement: <b>Signal Amplitude Symmetry</b>		
	Current Value: 992.0 m	Test Criteria: 980.0 m < x < 1.0200	
	Timestamp: 03/23/2023 10:38:40	Limit Name: 100BT-SAS	

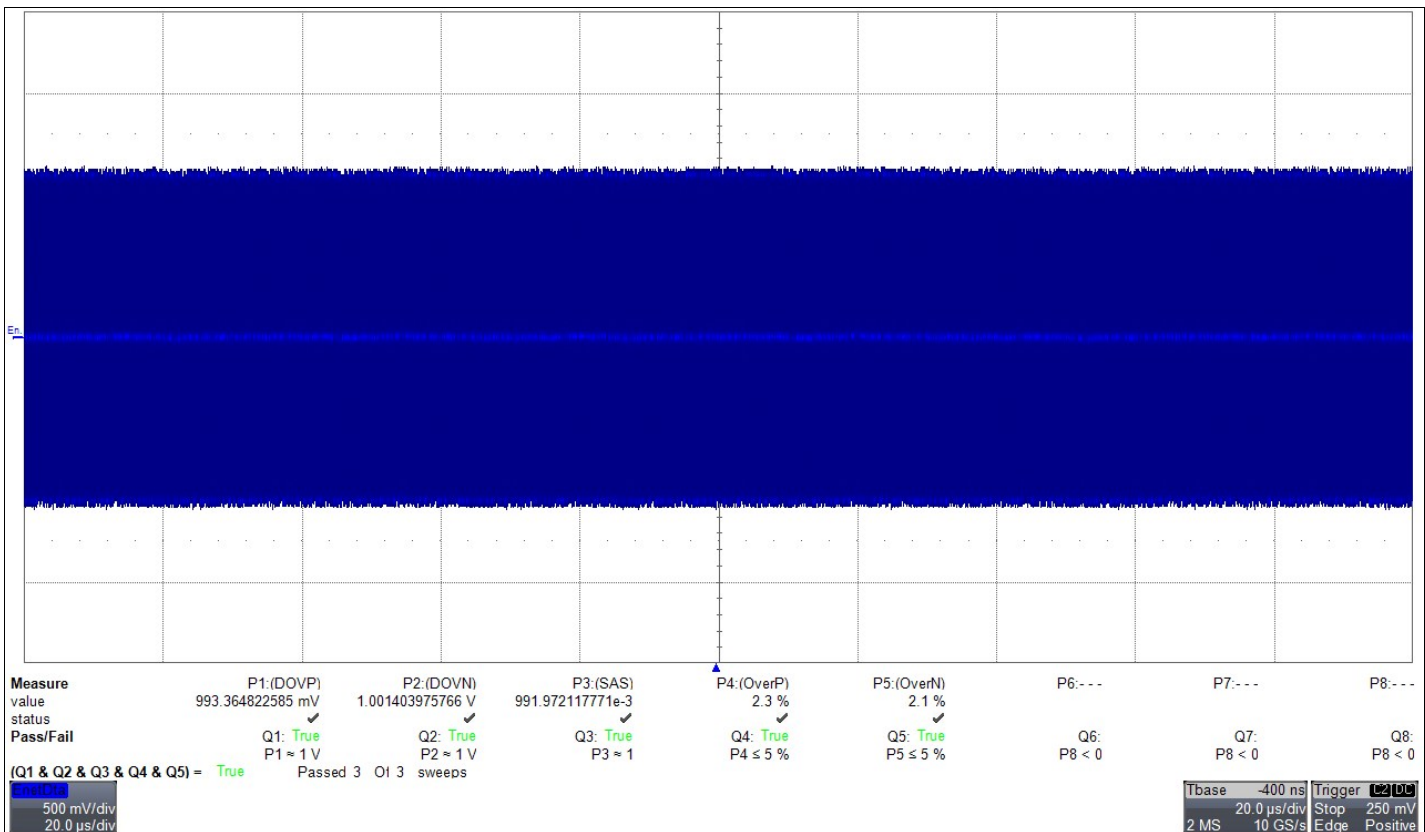
### Test ANSI 9.1.3 - Waveform overshoot

[\[Up\]](#)

 <b>Pass</b>	Measurement: <b>Overshoot Positive</b>		
	Current Value: 2.3 %	Test Criteria: x <= 5.0 %	
	Timestamp: 03/23/2023 10:38:41	Limit Name: 100BT-OverP	

[\[Up\]](#)


 <b>Pass</b>	Measurement: <b>Overshoot Negative</b>		
	Current Value: 2.1 %	Test Criteria: x <= 5.0 %	
	Timestamp: 03/23/2023 10:38:41	Limit Name: 100BT-OverN	




### 100Base-TX Differential Output Voltage, symmetry, overshoot

Timestamp: 03/23/2023 10:38:41


**Test ANSI 9.1.6 - Rise/Fall**[\[Up\]](#)

 <b>Pass</b>	Measurement: <b>Rise Base to Upper</b>		
	Current Value: 3.812 ns	Test Criteria: $x = 4.000 \text{ ns} \pm 1.000 \text{ ns}$	
	Timestamp: 03/23/2023 10:38:47	Limit Name: 100BT-URise	


[\[Up\]](#)

 <b>Pass</b>	Measurement: <b>Fall Upper to Base</b>		
	Current Value: 3.817 ns	Test Criteria: $x = 4.000 \text{ ns} \pm 1.000 \text{ ns}$	
	Timestamp: 03/23/2023 10:38:47	Limit Name: 100BT-UFall	


[\[Up\]](#)

 <b>Pass</b>	Measurement: <b>Rise Lower to Base</b>		
	Current Value: 4.101 ns	Test Criteria: $x = 4.000 \text{ ns} \pm 1.000 \text{ ns}$	
	Timestamp: 03/23/2023 10:38:47	Limit Name: 100BT-LRise	


[\[Up\]](#)

 <b>Pass</b>	Measurement: <b>Fall Base to Lower</b>		
	Current Value: 3.846 ns	Test Criteria: $x = 4.000 \text{ ns} \pm 1.000 \text{ ns}$	
	Timestamp: 03/23/2023 10:38:47	Limit Name: 100BT-LFall	

[\[Up\]](#)

 <b>Pass</b>	Measurement: <b>Rise/Fall Symmetry</b>		
	Current Value: 289 ps	Test Criteria: $x \leq 500 \text{ ps}$	
	Timestamp: 03/23/2023 10:38:47	Limit Name: 100BT-RFSymmetry	

**Test ANSI 9.1.8 - Duty Cycle Distortion**[\[Up\]](#)

 <b>Pass</b>	Measurement: <b>Duty Cycle Distortion</b>		
	Current Value: 50.6 ps	Test Criteria: $-250.0 \text{ ps} < x < 250.0 \text{ ps}$	
	Timestamp: 03/23/2023 10:38:54	Limit Name: 100BT-DCD	



--- End of report ---