MICROCONTROLLER

Practical -2

MULTIPLICATION

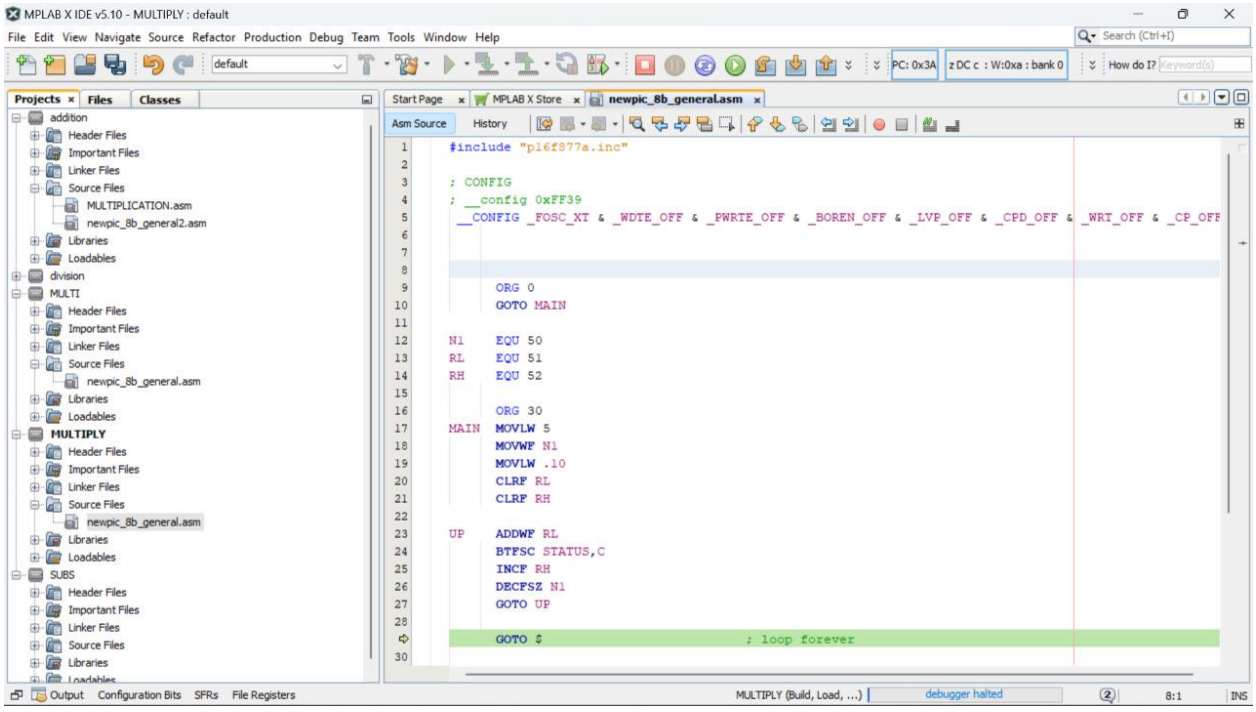
AIM:

Write a MPLAB program/ALP for multiplication.

APPARATUS REQUIRED:

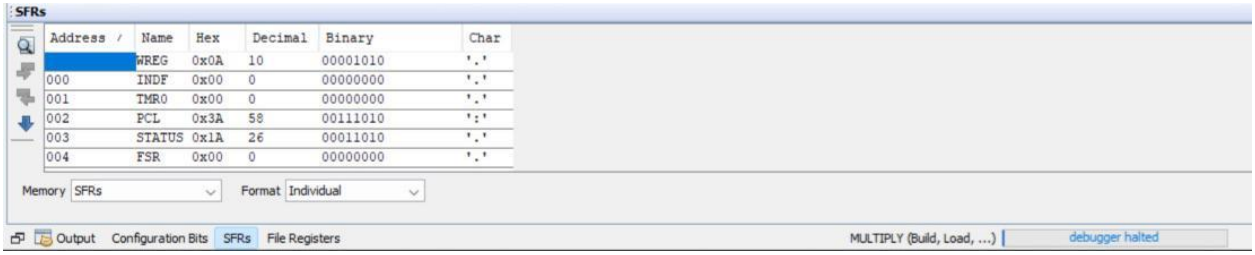
MPLAB X IDE

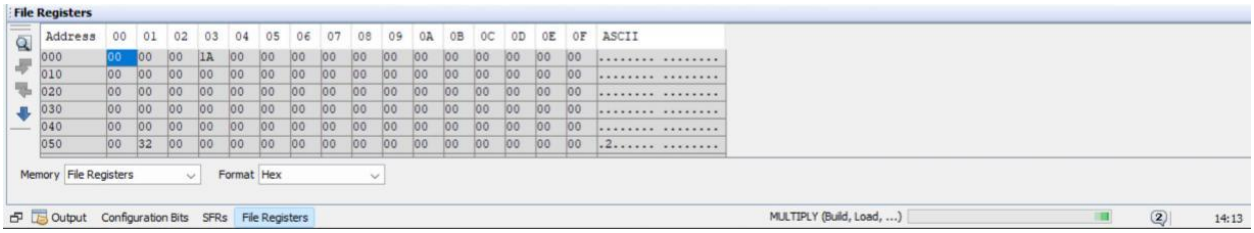
Input (code):



OUTPUT:


Output
Trace/Profiling
Config Bits Source
Debugger Console
Simulator
MULTIPLY (Clean, Build, ...)
make[2]: Leaving directory 'C:/Users/ASUS/MPLABXProjects/MULTIPLY.X'
make[1]: Leaving directory 'C:/Users/ASUS/MPLABXProjects/MULTIPLY.X'
BUILD SUCCESSFUL (total time: 2s)
Loading code from C:/Users/ASUS/MPLABXProjects/MULTIPLY.X/dist/default/production/MULTIPLY.X.production.hex...
Loading completed
Output
Configuration Bits
SFRs
File Registers
MULTIPLY (Build, Load, ...)


SFRs
Address
Name
Hex
Decimal
Binary
Char
WREG 0x0A 10 00001010 '.'
000 INDF 0x00 0 00000000 '.'
001 TMR0 0x00 0 00000000 '.'
002 PCL 0x3A 58 00111010 ':'
003 STATUS 0x1A 26 00011010 '.'
004 FSR 0x00 0 00000000 '.'
Memory SFRs
Format Individual
Output
Configuration Bits
SFRs
File Registers
MULTIPLY (Build, Load, ...)
debugger halted


File Registers
Address 00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F ASCII
000 00 00 00 1A 00 00 00 00 00 00 00 00 00 00 00 00 ....
010 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 ....
020 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 ....
030 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 ....
040 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 ....
050 00 32 00 00 00 00 00 00 00 00 00 00 00 00 00 00 .2...... ....
Memory File Registers
Format Hex
Output
Configuration Bits
SFRs
File Registers
MULTIPLY (Build, Load, ...)
14:13