88E6352 128 Pin DB

Designer : Amulya K Patra

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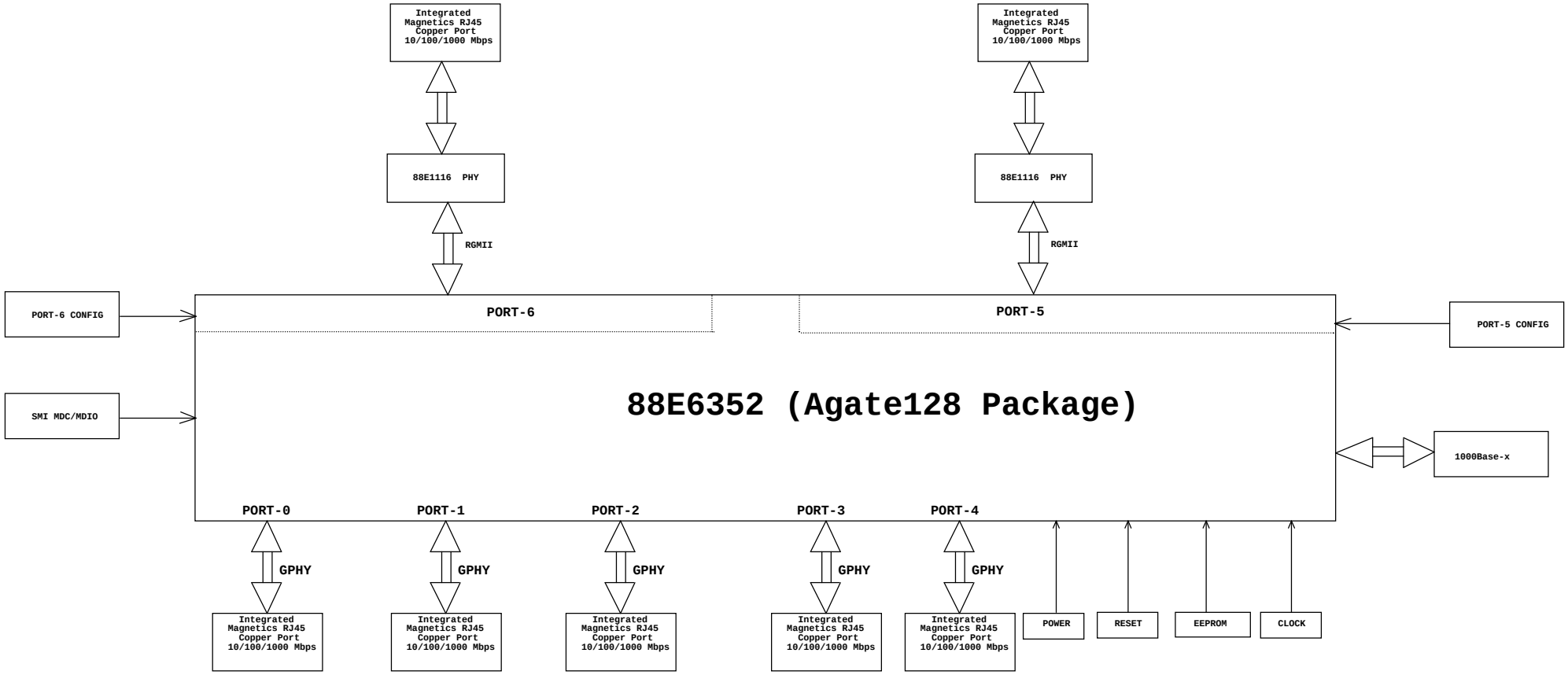
TOP LEVEL BLOCK DIAGRAM



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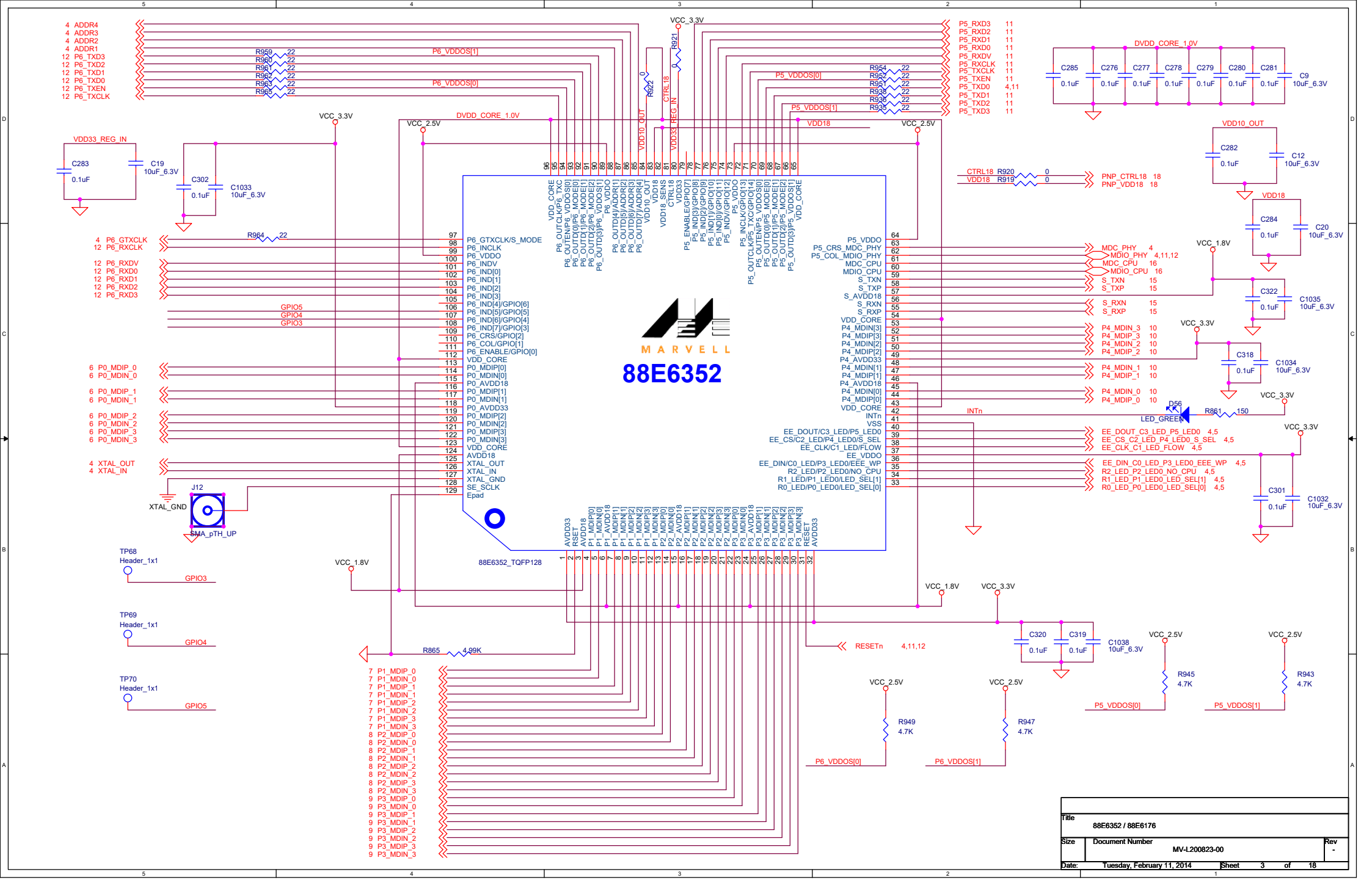
88E6352
MARVELL
88E6352_TQFP128
VCC_3.3V
VCC_2.5V
VCC_1.8V
DVDD_CORE_1.0V
VDD18
VDD33_REG_IN
VDD10_OUT
XTAL_GND
SMA_pTH_UP
J12
TP68
Header_1x1
TP69
Header_1x1
TP70
Header_1x1
GPIO3
GPIO4
GPIO5
LED_GREEN
INTn
RESETn
P6_VDDOS[1]
P6_VDDOS[0]
P5_VDDOS[0]
P5_VDDOS[1]
CTRL18
VDD18_REG_IN
PNP_CTRL18
PNP_VDD18
MDC_PHY
MDIO_PHY
MDC_CPU
MDIO_CPU
S_TXN
S_TXP
S_RXN
S_RXP
EE_DOUT_C3_LED_P5_LED0
EE_CS_C2_LED_P4_LED0_S_SEL
EE_CLK_C1_LED_FLOW
EE_DIN_C0_LED_P3_LED0_EEE_WP
R2_LED_P2_LED0_NO_CPU
R1_LED_P1_LED0_LED_SEL[1]
R0_LED_P0_LED0_LED_SEL[0]
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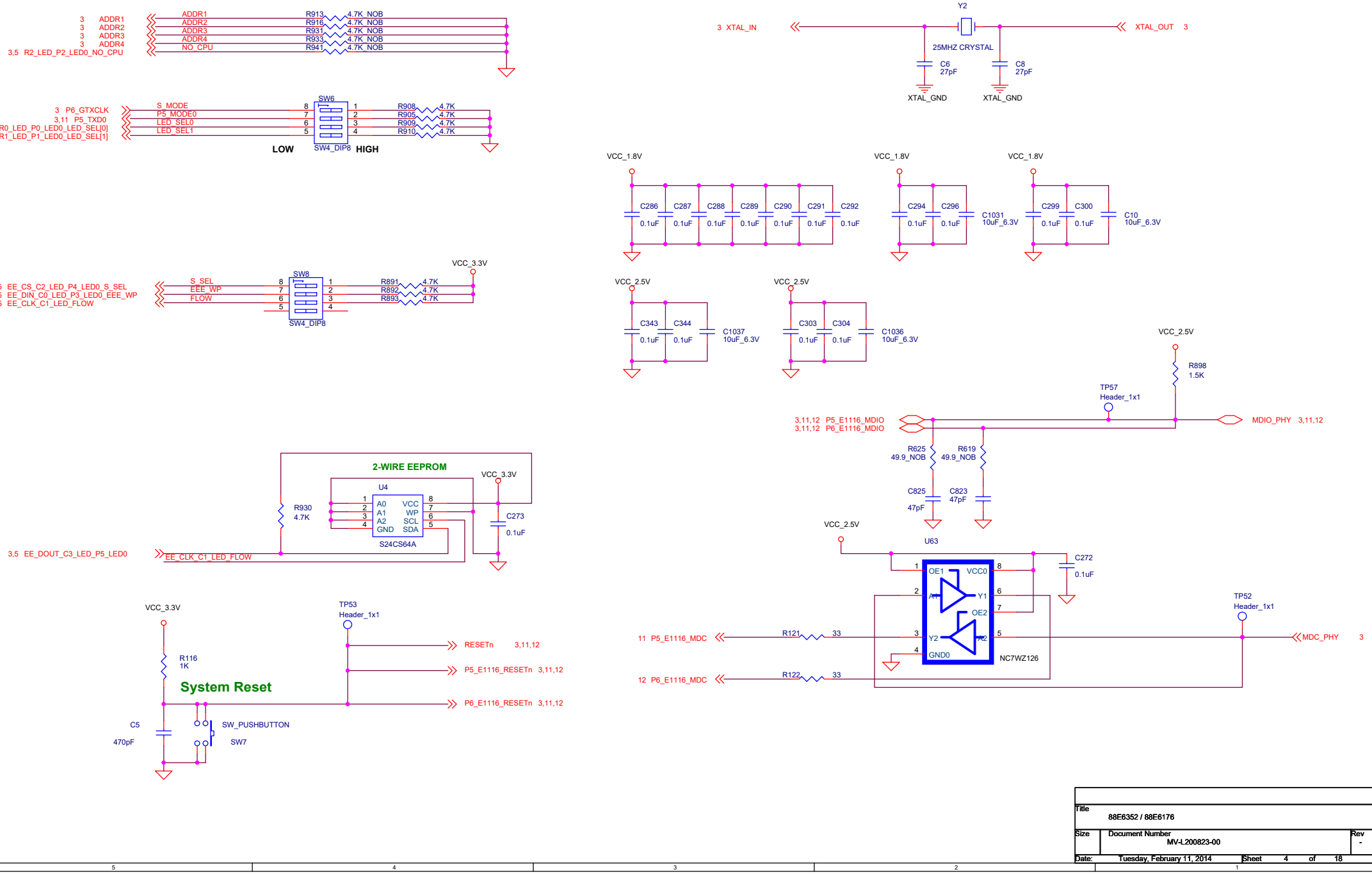

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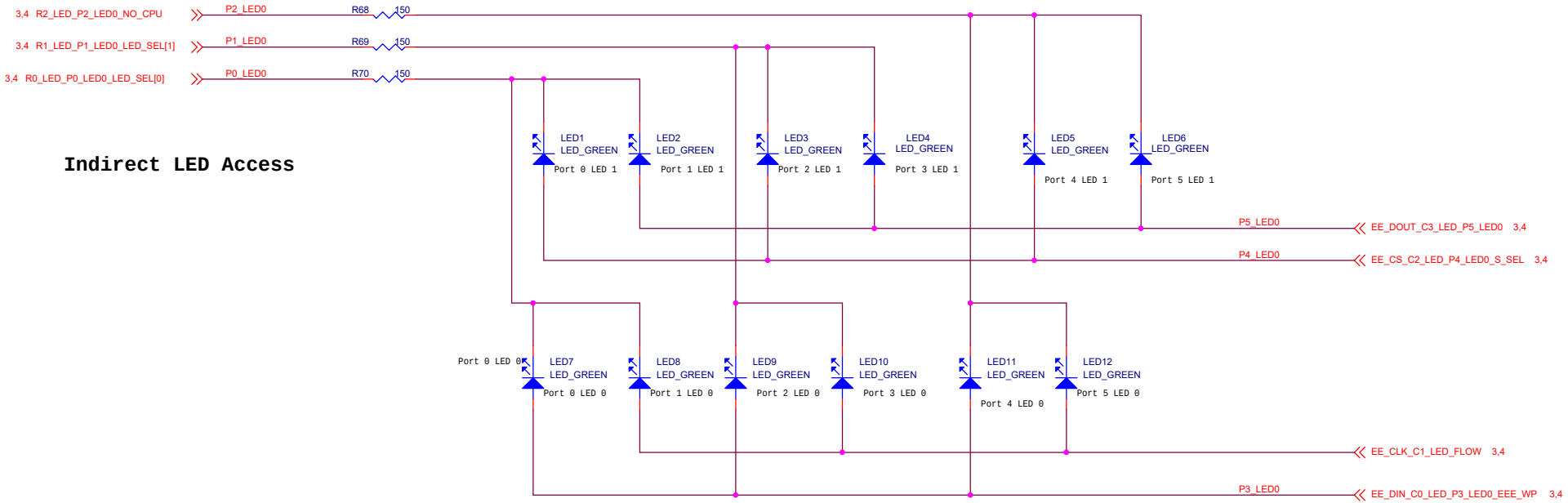
GPHY LED



	C0_LED	C1_LED	C2_LED	C3_LED
R0_LED	Port 0 LED 0	Port 1 LED 0	Port 0 LED 1	Port 1 LED 1
R1_LED	Port 2 LED 0	Port 3 LED 0	Port 2 LED 1	Port 3 LED 1
R2_LED	Port 4 LED 0	Port 5 LED 0	Port 4 LED 1	Port 5 LED 1

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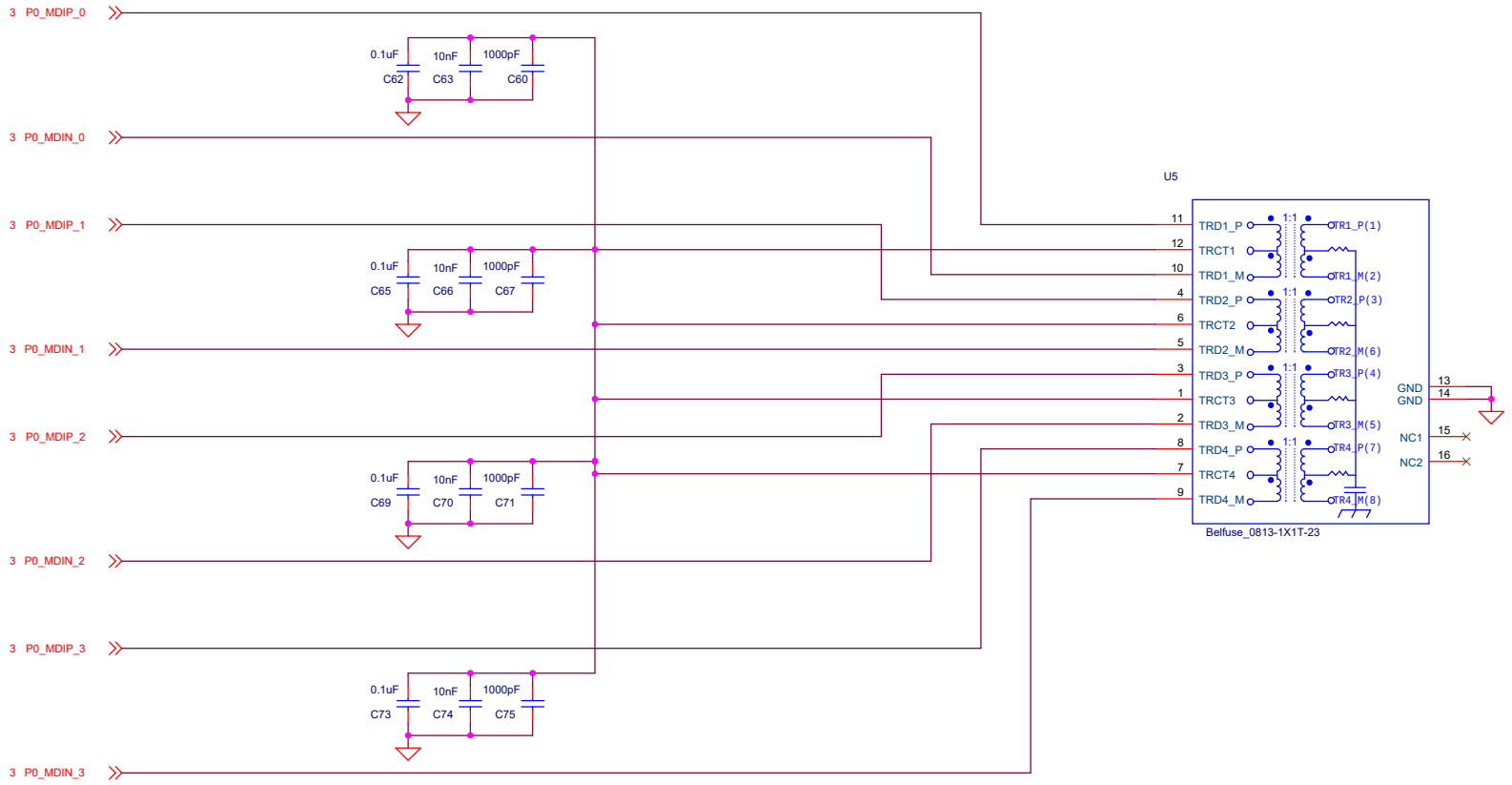
PORT-0 MDI I/F



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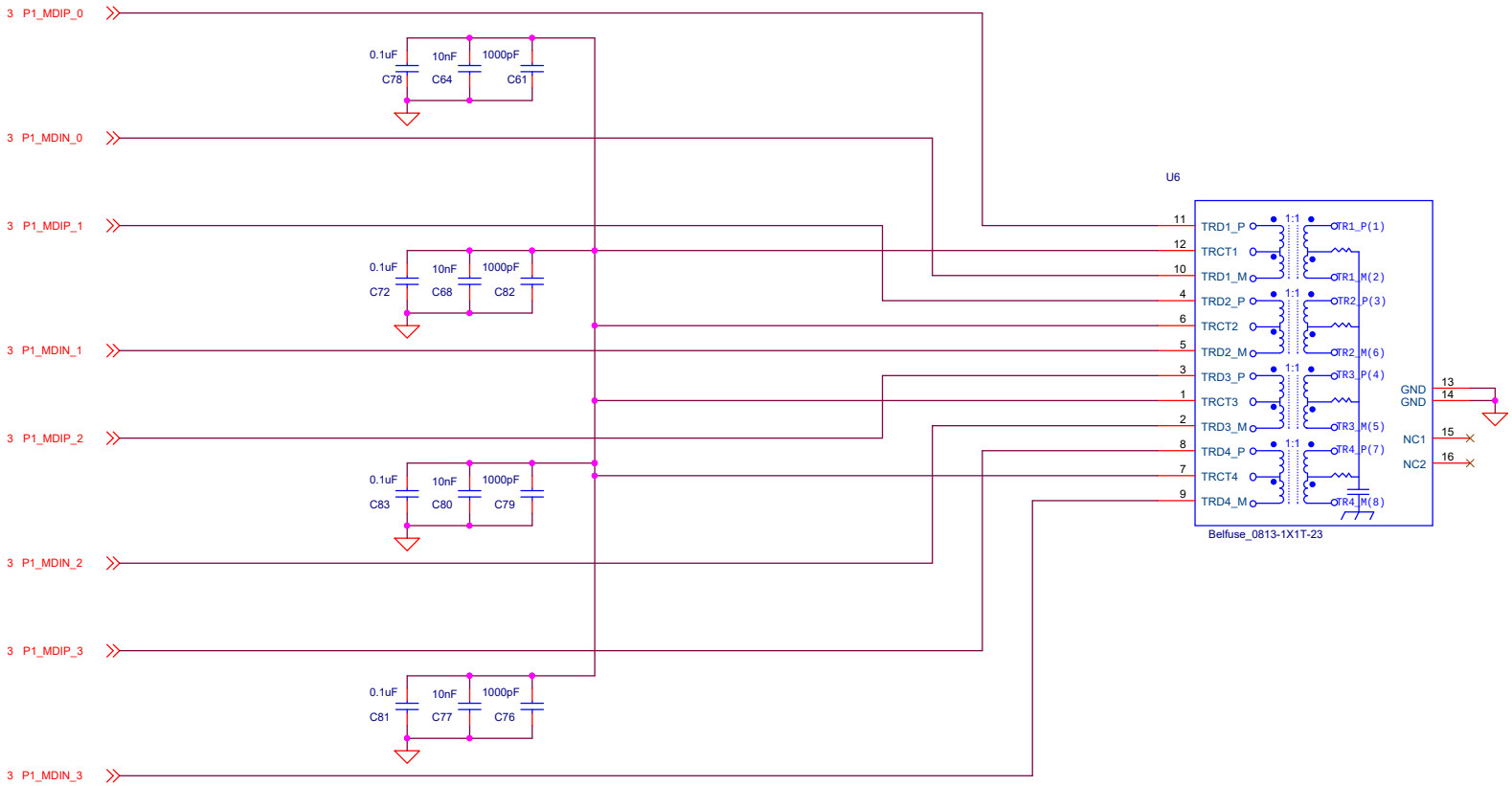
PORT-1 MDI I/F



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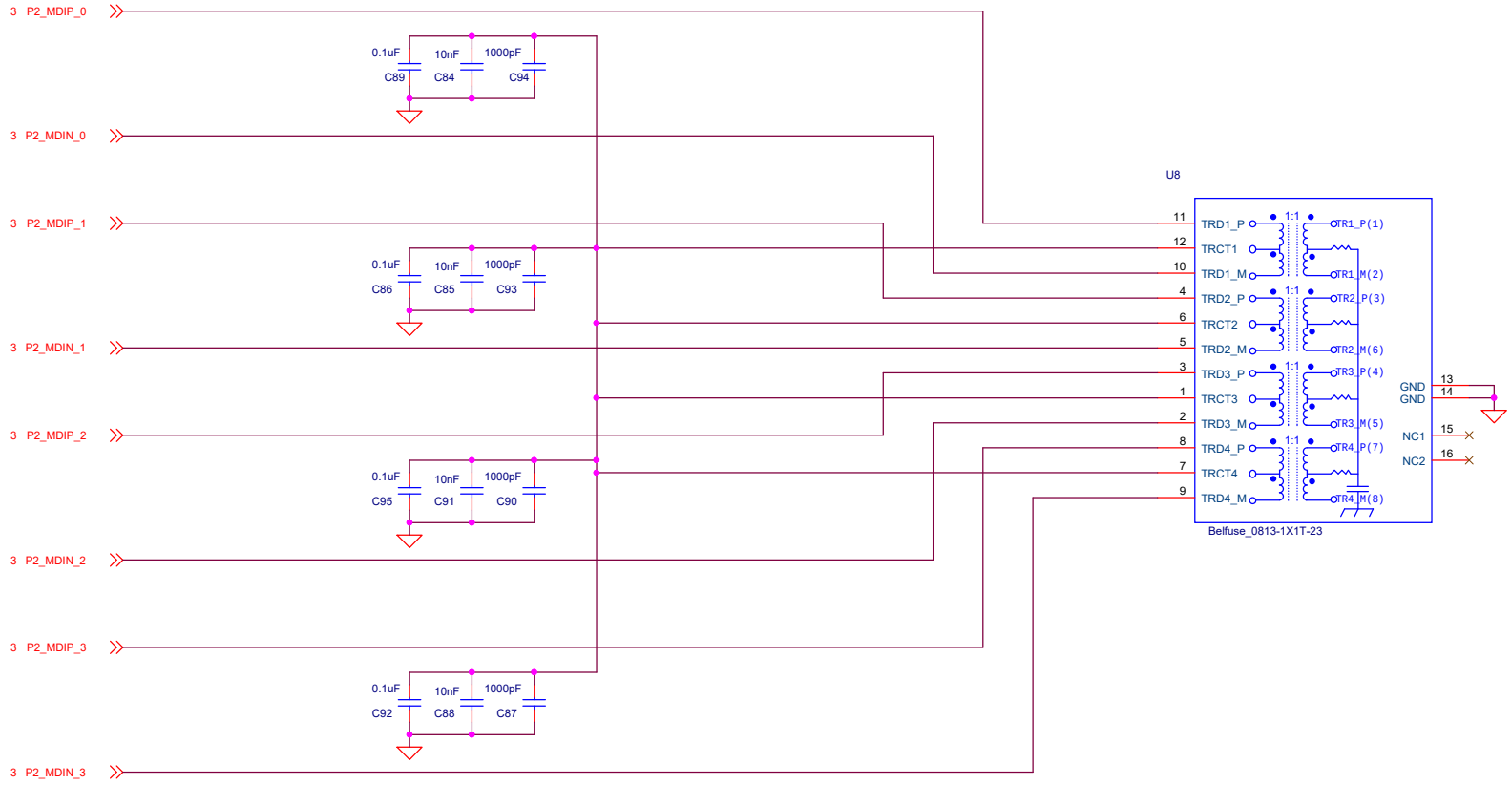
PORT-2 MDI I/F



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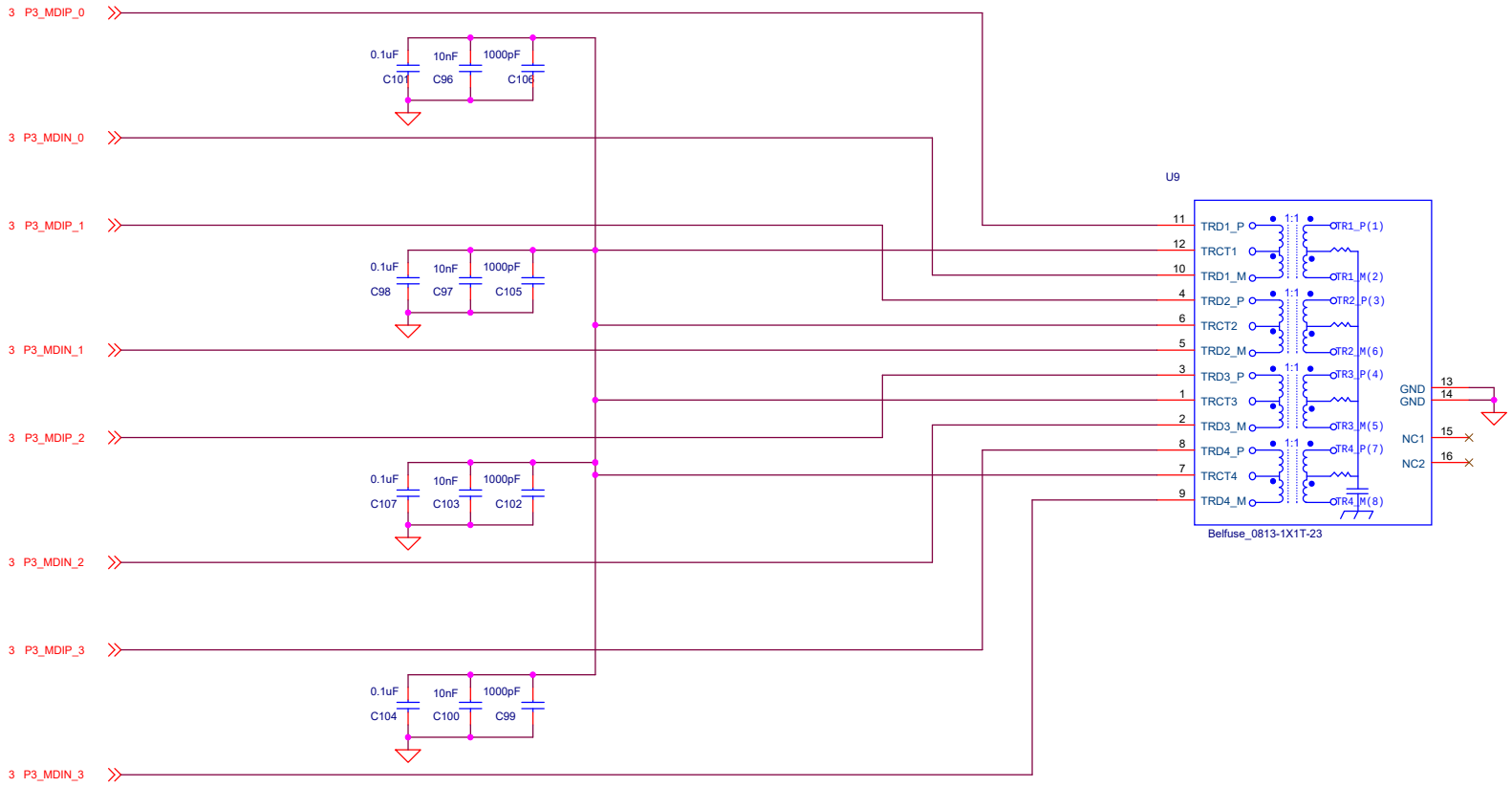

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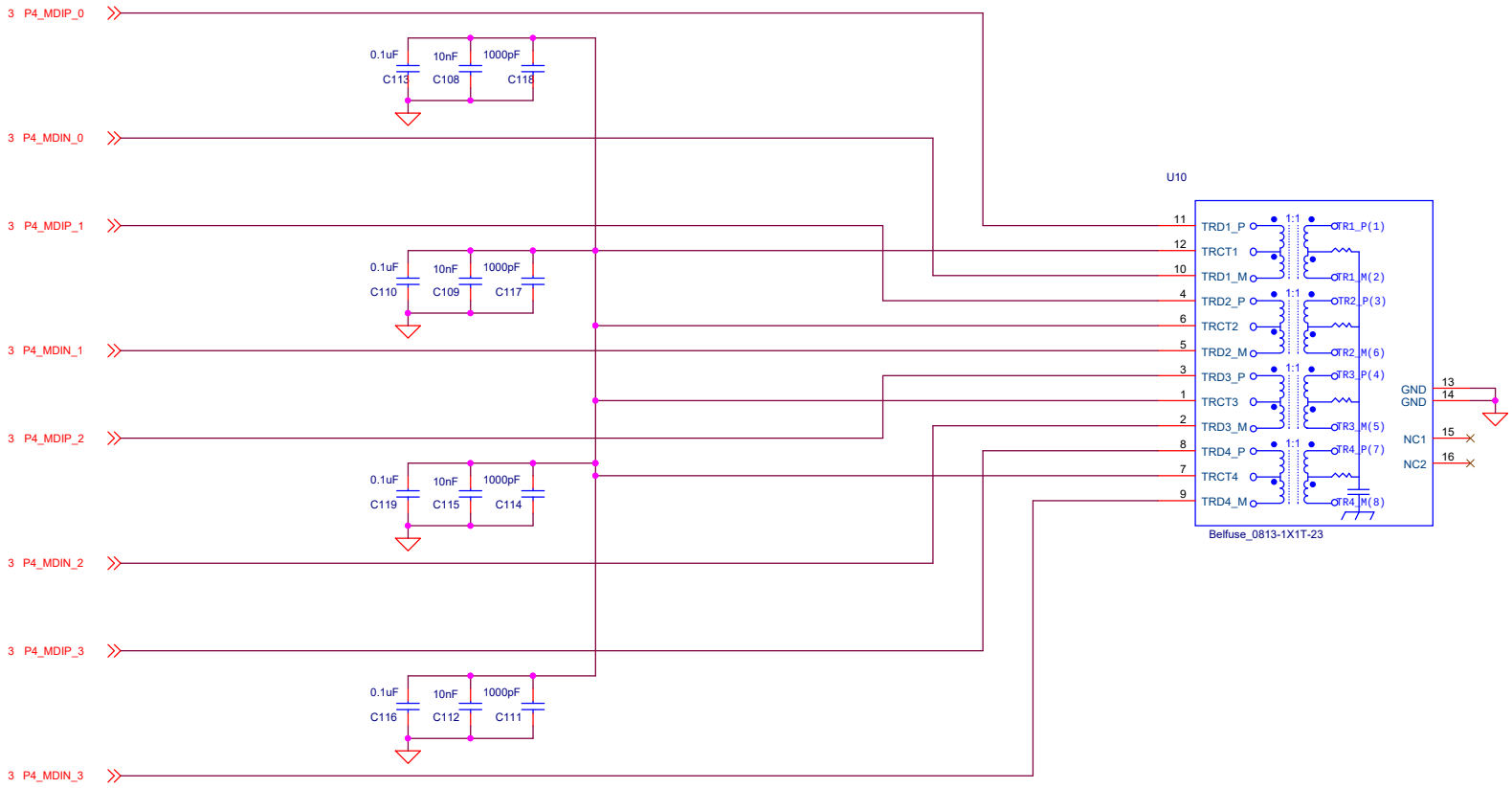

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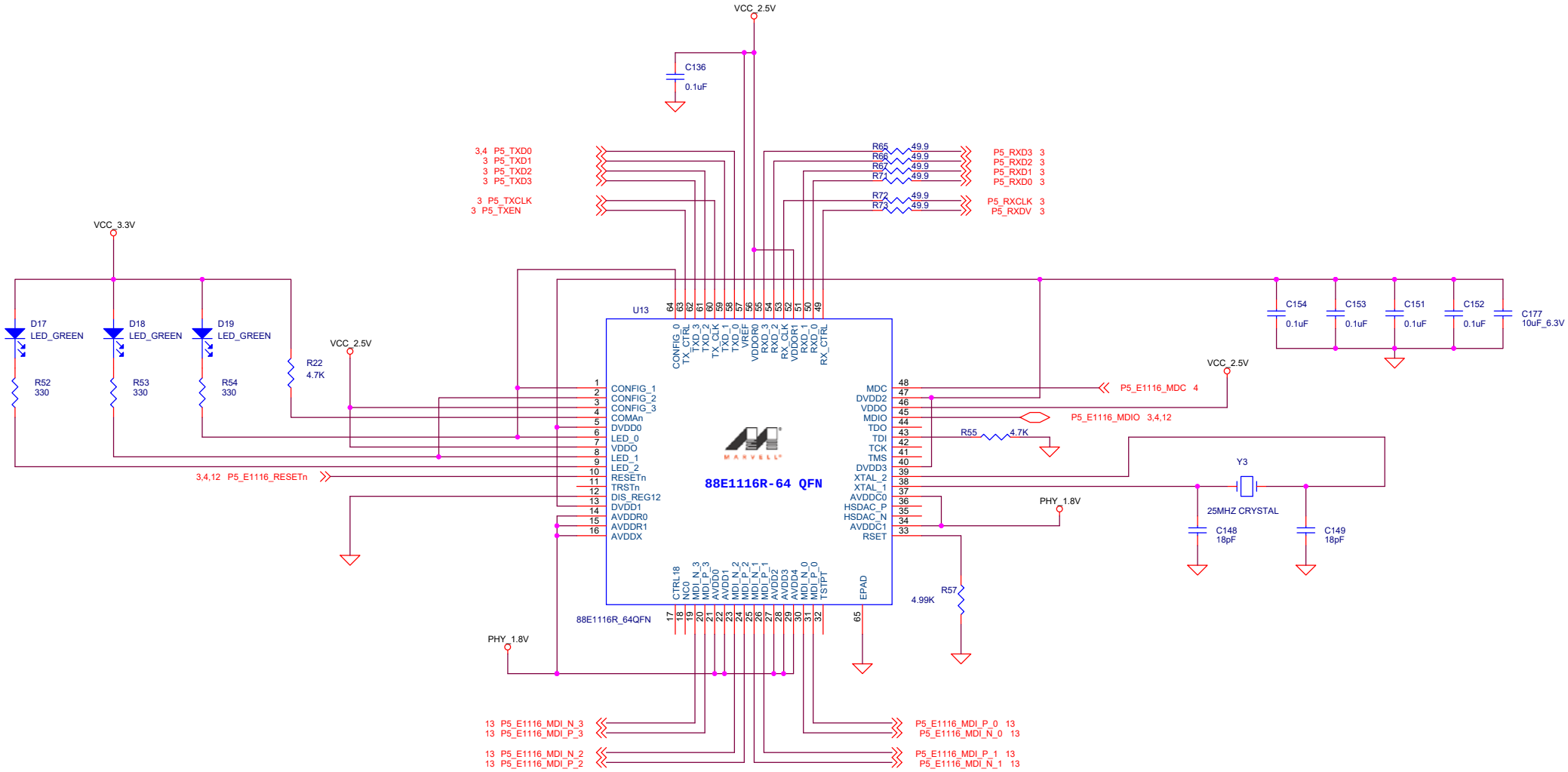
P5 RGMIIINTERFACE WITH E1116



MARVELL

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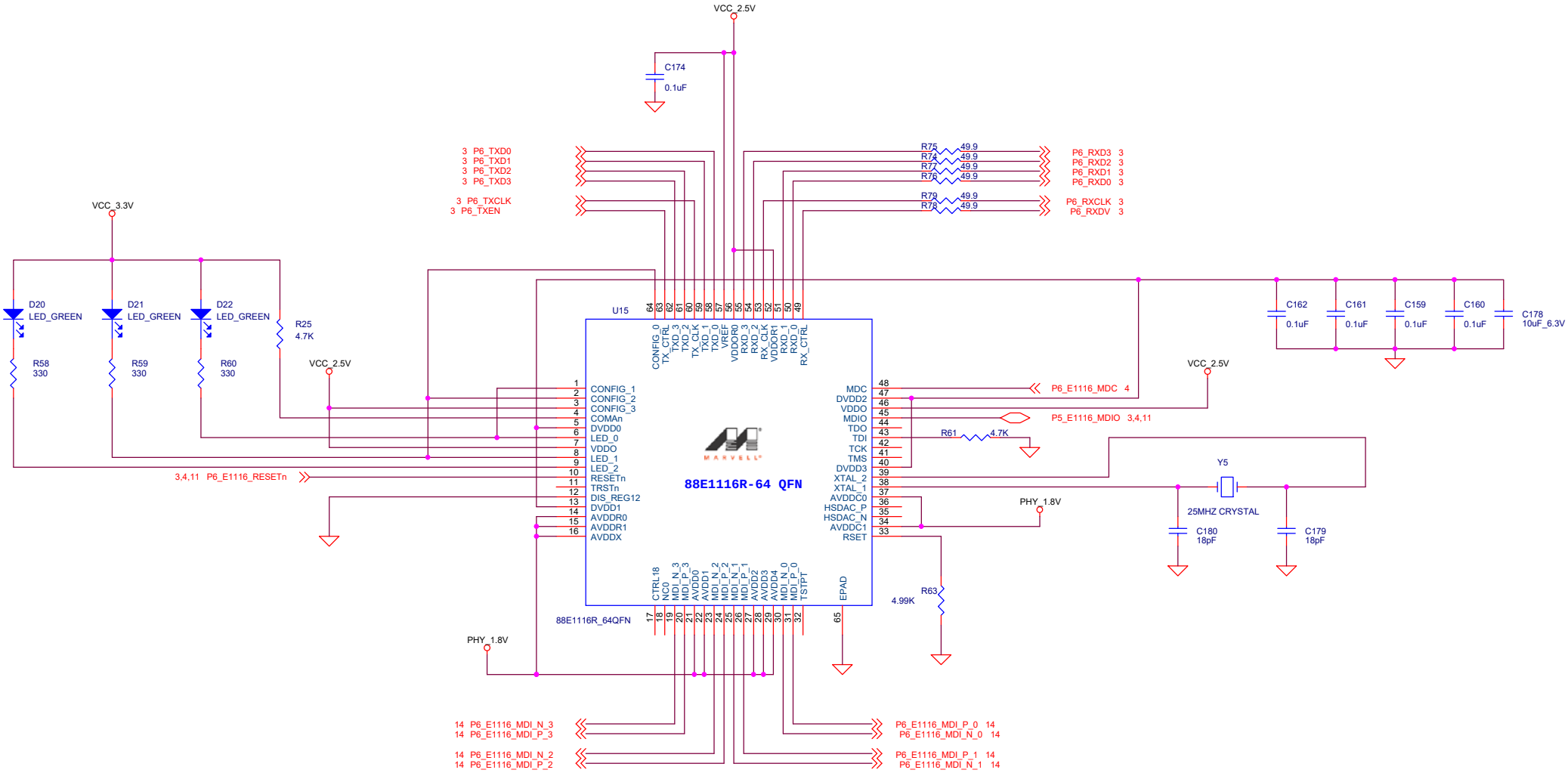
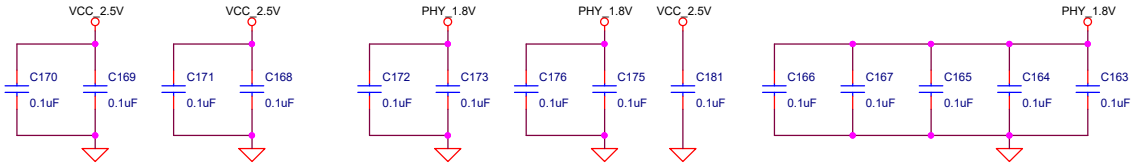
P6 RGMII/GMII INTERFACE WITH E1116





MARVELL

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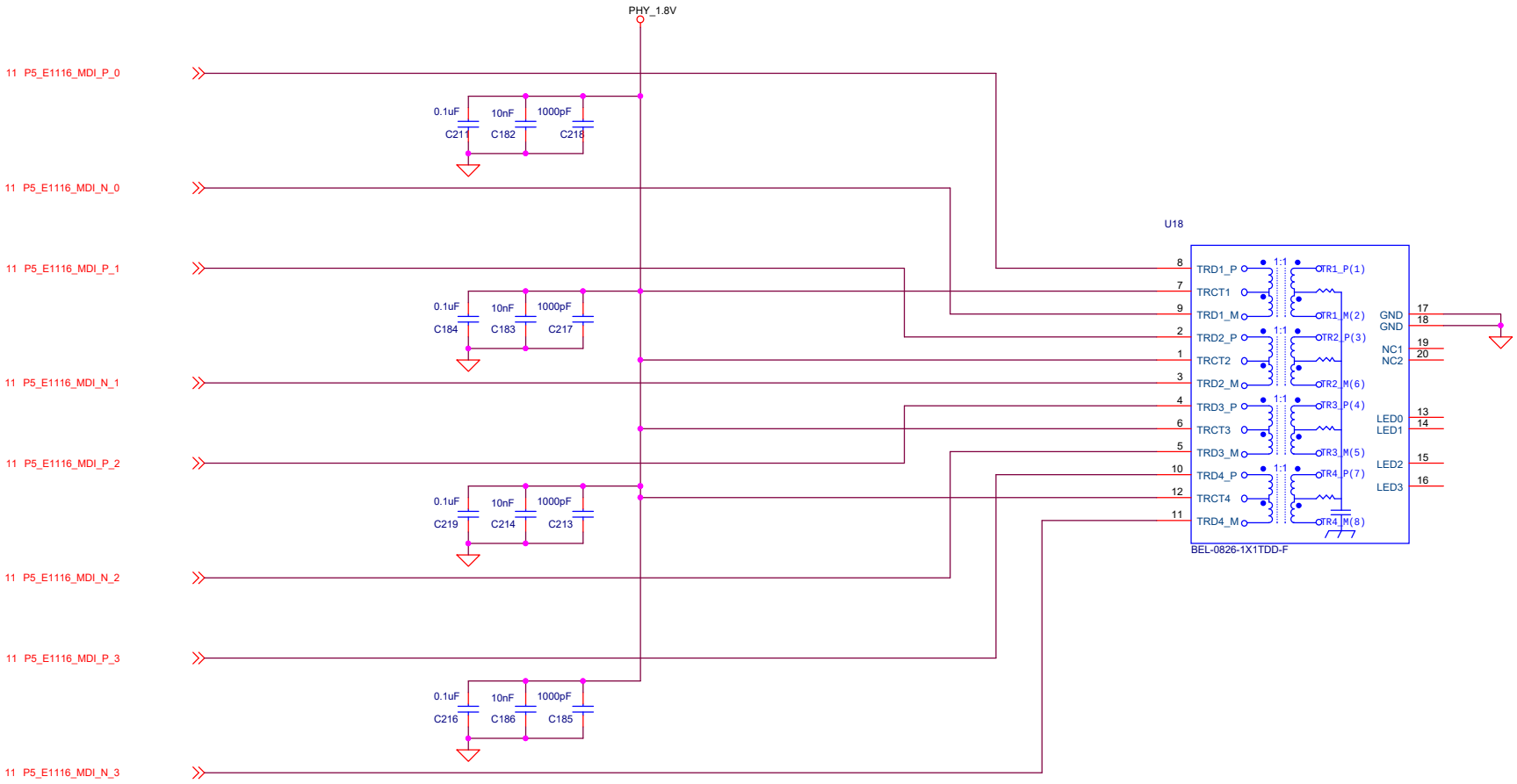
P5 E1116 MDI I/F



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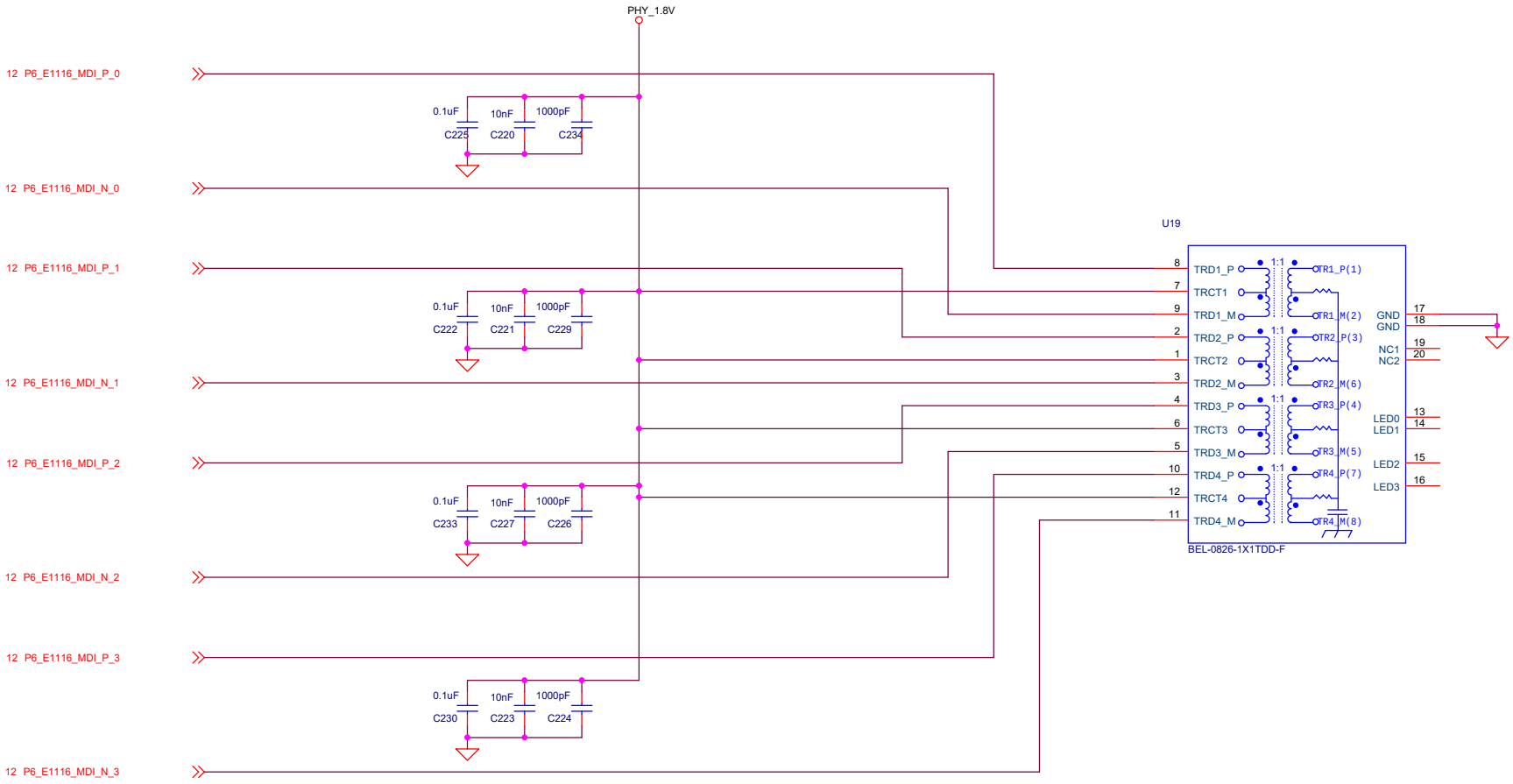
P6 E1116 MDI I/F



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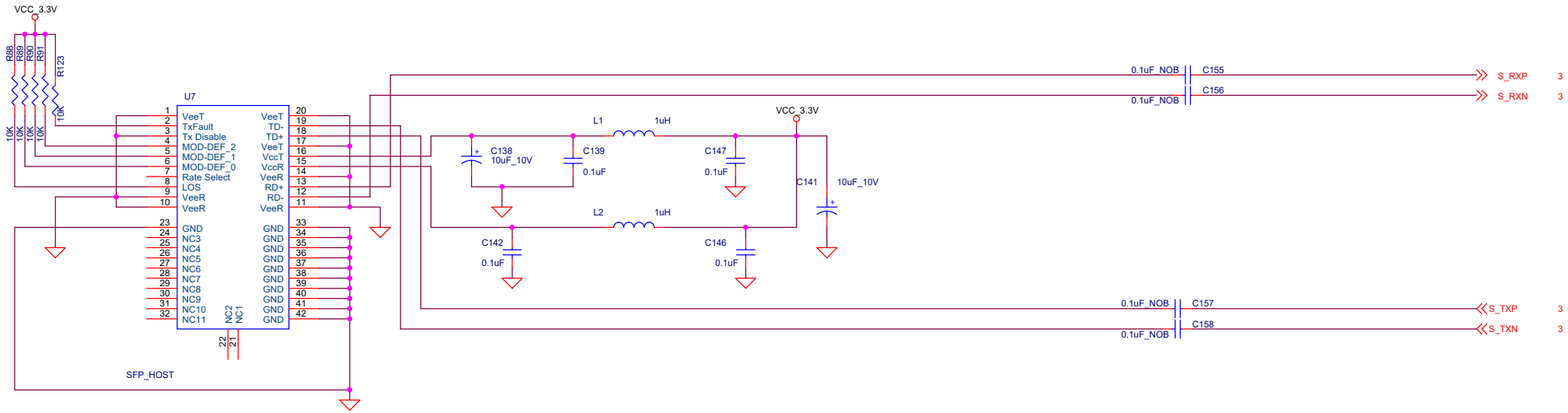
1000BASE-X



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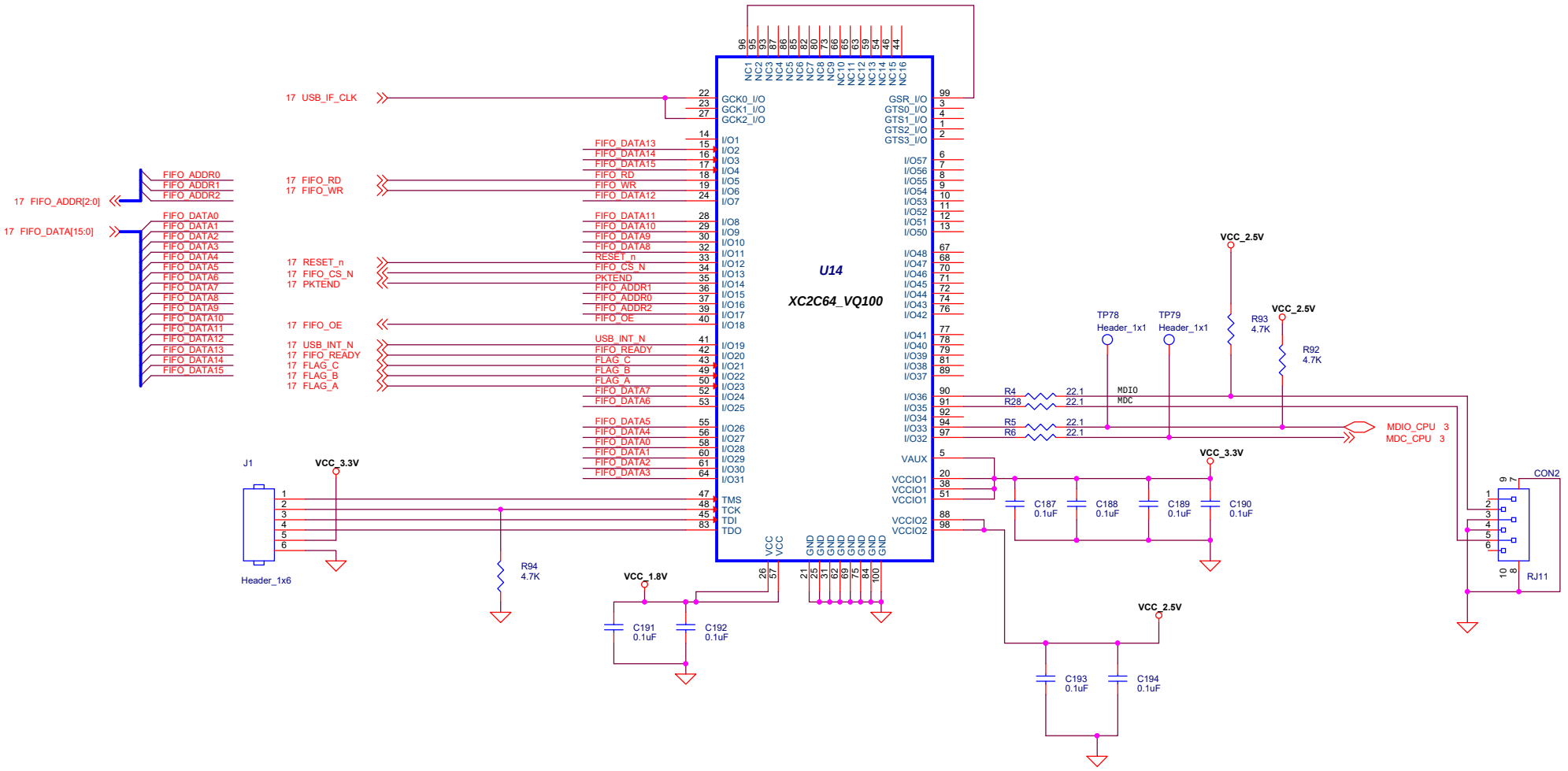
SMI CPLD CONTROLLER



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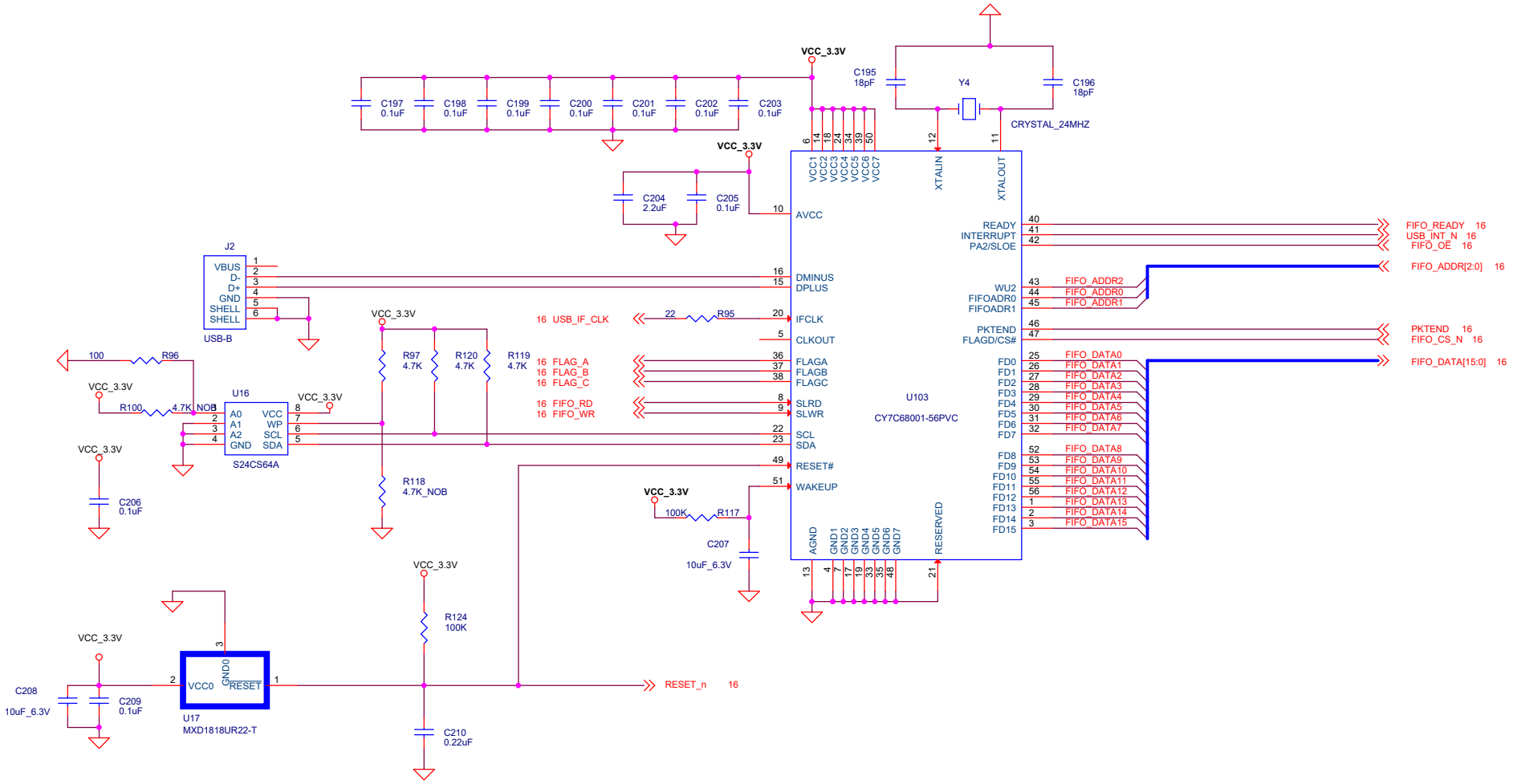
SMI USB CONTROLLER



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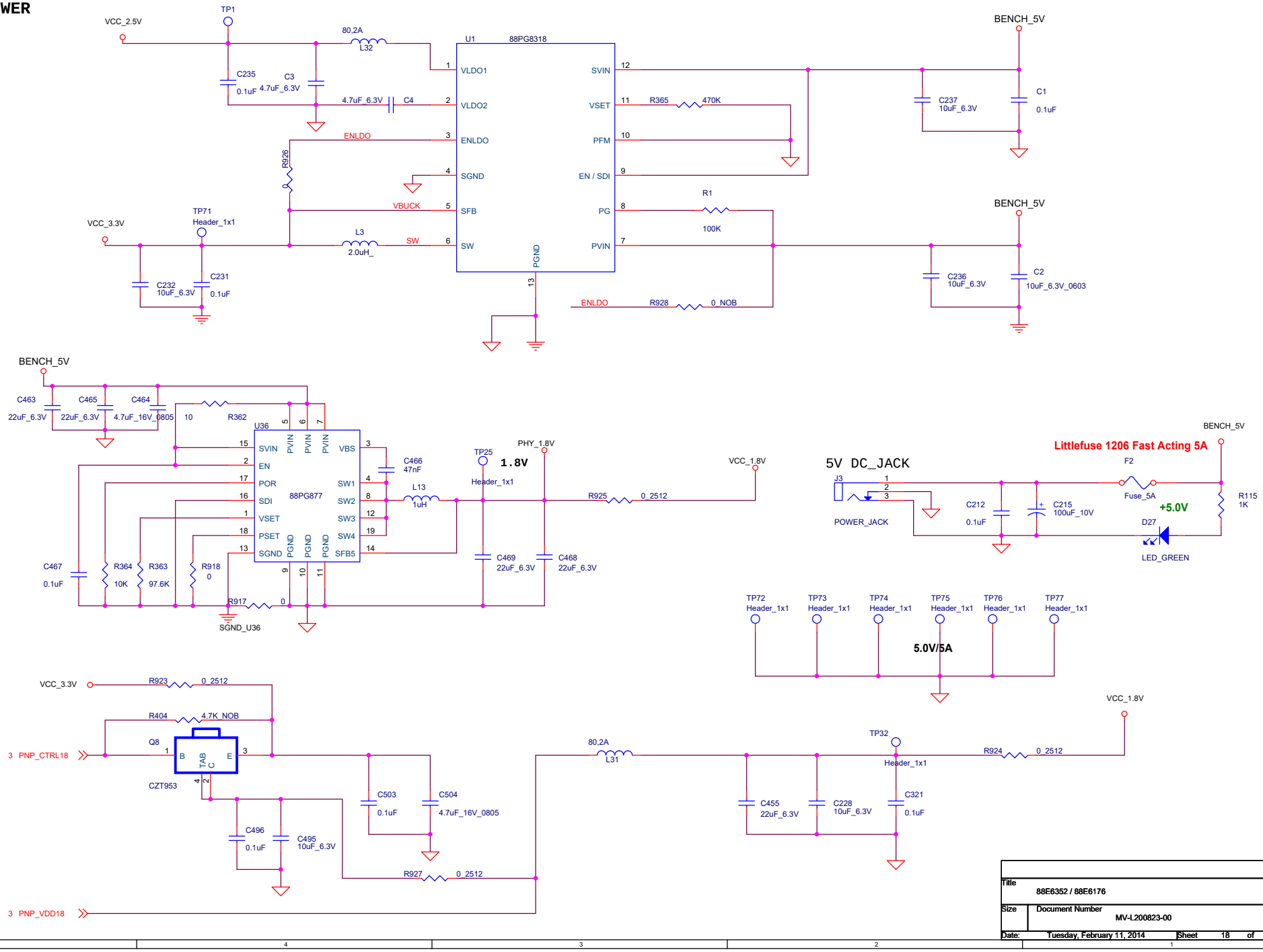
SWITCH & PHY POWER SUPPLY



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