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Regular Arrays of Copper Wires Formed by Template-Assisted Electrodeposition**

By Mingzhe Zhang, Steven Lenhart, Mu Wang,*
Lifeng Chi,* Nan Lu, Harald Fuchs, and Naiben Ming

Electrodeposition—or electroplating—has been intensively investigated in recent decades, as it is an important method for fabricating metallic thin films.^[1] In recent years, much effort has been devoted to patterned electrodeposition,^[2–5] which is expected to be a promising technique for fabricating micro-/nanostructures in a “bottom-up” fashion, and may have applications in future generations of microelectronics,^[6] miniaturized sensors,^[7] and microelectromechanical systems.^[8,9] There are generally two ways to achieve patterned electrodeposition. In one scenario, the pattern is achieved via self-organization. For example, in an ultrathin electrolyte layer the electrodeposits may have a considerably low branching rate^[4] and the branches may self-organize into regular arrays of metallic wires.^[10] However, achieving a regular array over a large area by self-organization remains a technical challenge. An alternative method is template-assisted electrodeposition.^[11] For example, with the help of ordered channels in anodized aluminum oxide, arrays of metallic nanowires can be fabricated,^[12] however a large-area ordered template of anodized aluminum oxide is extremely difficult to prepare.^[12] Therefore, the essential problem for patterned electrodeposition is to find an easy, cheap, and high-throughput way to generate ordered templates. Recently, it has been found that when monolayers are transferred onto a solid substrate, a regular striped pattern can be formed due to the wetting instabilities of the Langmuir film under low surface pressure.^[13] In this way, a heterogeneously structured surface with periodic re-

* Prof. M. Wang, Dr. M. Zhang, Prof. N. B. Ming
National Laboratory of Solid State Microstructures
Nanjing University
Nanjing 210 093 (P.R. China)
E-mail: muwang@nju.edu.cn

* Dr. L. Chi, Dr. M. Zhang, S. Lenhart, Dr. N. Lu, Prof. H. Fuchs
Physikalisches Institut, Westfälische Wilhelms-Universität
D-48 149 Münster (Germany)
E-mail: chi@uni-muenster.de

Prof. M. Wang
International Center of Quantum Structures, Institute of Physics
Chinese Academy of Sciences
Beijing 100 080 (P.R. China)
Dr. L. Chi, S. Lenhart, Dr. N. Lu, Prof. H. Fuchs
Center for Nanotechnology (CeNTech)
Gievenbecker Weg 11, D-48 149 Münster (Germany)

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gions of alternating surface free energies can be achieved. In this communication, we report the electrodeposition of regular arrays of submicrometer-wide copper wires over a silicon substrate, with the help of a patterned Langmuir–Blodgett (LB) film. We show that the width of the wires is homogeneous, and both the width and the separation of the wires can be tuned by the template, across a range of 100 nm to more than 1 μm .

The substrate for electrodeposition was prepared in the following way. The silicon surface was first cleaned and oxidized with O_2 plasma, and then covered with monolayer-thick regular stripes of *L*- α -dipalmitoylphosphatidylcholine (DPPC) by the previously reported method.^[13–15] In this way, alternating stripes of DPPC and silicon oxide were generated, as shown in Figures 1a,b. We have found that the widths of the DPPC stripes and the silicon dioxide channels depend on the experi-

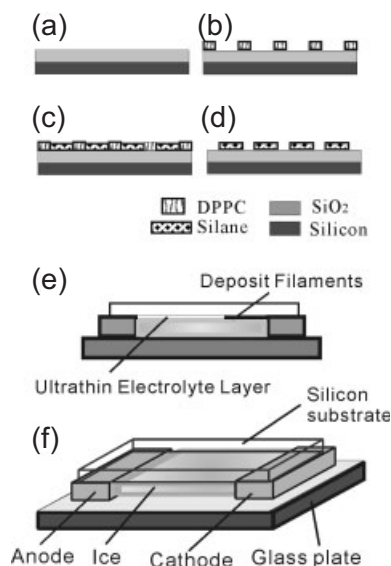


Figure 1. a–d) Schematic diagrams showing the procedure used to prepare the patterned substrate for electrodeposition. a) The surface of a silicon wafer is first oxidized. b) A periodic line pattern of DPPC is introduced on the substrate surface by pulling the LB film. c) Aminosilane molecules are self-assembled into the silicon dioxide channels. d) The DPPC stripes are removed; the aminosilane stripes remain. Copper crystallites will be nucleated on the aminosilane stripes. e,f) The side view and stereo view of the cell for electrodeposition. The surface with aminosilane contacts the electrolyte, on which the copper filaments will develop.

mental control parameters. The periodicity of the pattern was determined by the transfer speed, whereas the ratio of the width of the stripe to the width of the channel depended on the surface density of the Langmuir film during the transfer. Thereafter, the DPPC stripes were used as a mask, and 3-aminopropyltrimethoxysilane was silicized in the silicon dioxide channels (Fig. 1c). Ultrasonication and rinsing with chloroform removed the residual silane and DPPC stripes, thus achieving a template with alternating stripes of amine groups and silicon dioxide.^[16] The regular amino stripes on the substrate were used as a template for depositing copper by the

unique method of ultrathin electrochemical deposition.^[4,10] The setup for electrodeposition is schematically shown in Figures 1e,f. In the electrodeposition experiment, the copper filaments developed from the cathode, following the aminosilane tracks on the substrate and growing towards the anode, as indicated in Figure 1e.

In our experiment, we applied a constant voltage across the electrodes. Figure 2a shows the copper wires generated at 2.0 V viewed by scanning electron microscopy (SEM). The wires were robustly deposited on the substrate. They are

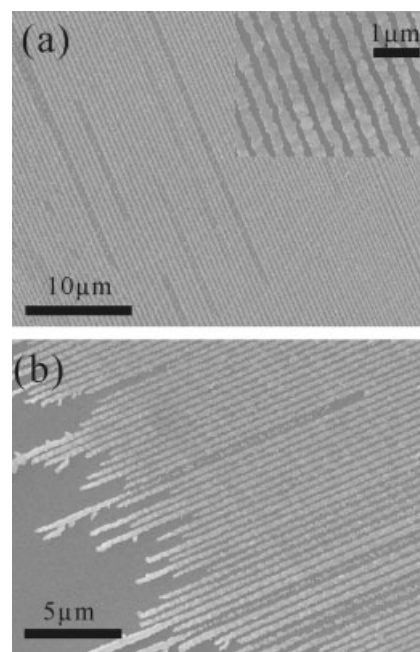


Figure 2. Scanning electron micrographs showing the copper wire arrays deposited on the patterned substrate. a) The copper wires are straight and homogeneous in width. b) The very front tips of the copper wires grown on the patterned substrate. The broadening of the tip is due to the residual growth of the copper crystallites when the power supply for electrodeposition is switched off.

straight, equally spaced, and homogeneous in width. The height of the copper wires, according to our atomic force microscopy (AFM) measurements, is of the order of 50 nm when the control parameters were set as $C = 0.07$ M, where C is the concentration of CuSO_4 , $V = 2.0$ V, $\text{pH} 3.4$, and $T = -4.2^\circ\text{C}$. Our experiments show that the height of the wires depended on the pH and the concentration of the electrolyte. At lower concentrations and lower pH, the height of the wires was lower. The largest area with strictly regular copper wire arrays that we achieved was about $200\ \mu\text{m} \times 200\ \mu\text{m}$, which could be improved by increasing the homogeneity of the ultrathin film of electrolyte. Figure 2b shows the very tips of the copper filaments grown on the substrate, which follow the templates on the substrate. Since the template structure is pre-designed and controllable, for each substrate we know the width of the stripes of aminosilane and width of the silicon oxide exactly. By checking the width of the copper filaments and the separa-

tions between the copper filaments, we conclude that the copper covers the region terminated with aminosilane.

Figure 3 shows copper wires with different widths and inter-wire separations, which were achieved by using templates with different structures. One may also see in Figure 3 that the

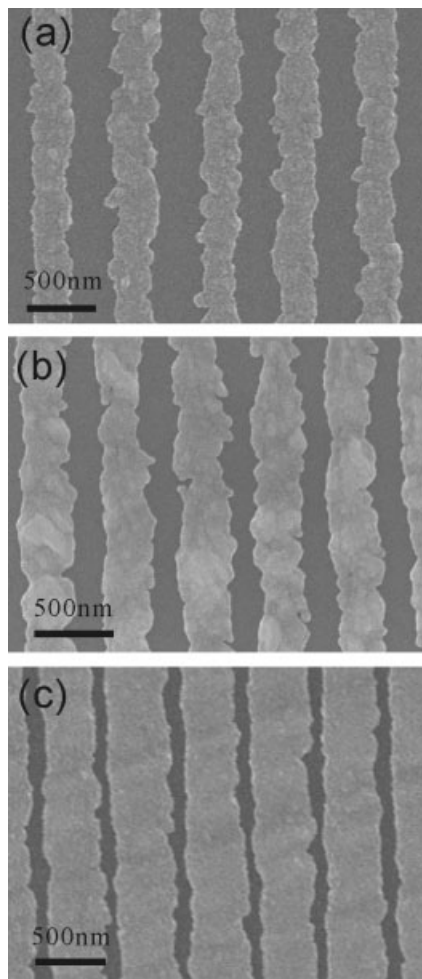


Figure 3. The ratio of the width of the copper wires to the width of the separation between neighboring wires was determined by the patterns on the template. The widths of the amino stripes and the silicon dioxide channels on the template are about: a) 200 nm and 200 nm; b) 300 nm and 200 nm; and c) 400 nm and 100 nm, respectively. Hence, the copper wires in these cases are about 200 nm, 300 nm, and 400 nm wide, accordingly. These micrographs also indicate that the copper wires are polycrystalline.

wires consist of small crystallites. The polycrystalline feature of the wires was confirmed by transmission electron microscopy (TEM). Figure 4a displays the diffraction contrast micrograph of the wire, where the separated grains can be easily identified. As indicated in the diffraction pattern, in addition to the dominant, strong diffractions of copper, some faint dots (rings) corresponding to the diffraction of Cu_2O can be recognized. Since both copper and cuprous oxide are polycrystalline, and the orientations of the crystallites are random, we may compare the concentrations of the crystallites of copper

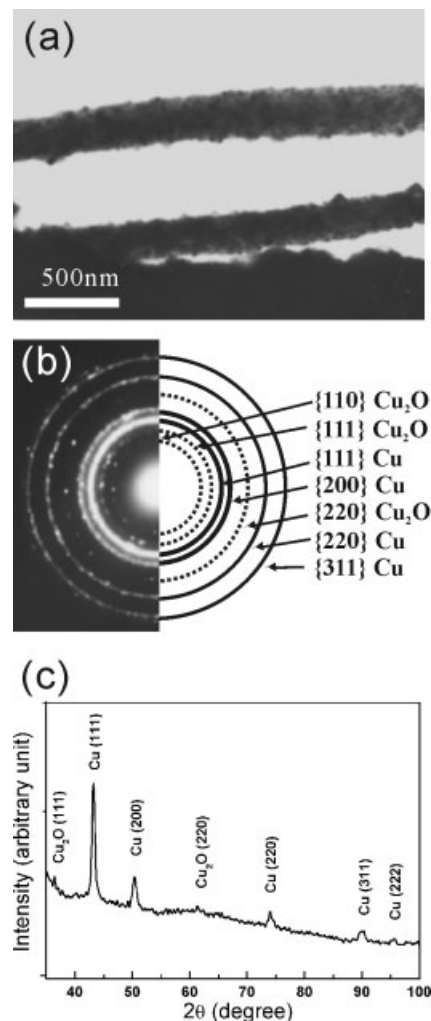


Figure 4. a) The transmission electron micrograph of the copper wires (the dark filaments). Some individual crystallites can be identified in the copper wire. b) The electron diffraction of the copper wire, which confirms that the wire is polycrystalline. The diffraction rings can be indexed. In addition to the diffraction of copper crystallites, faint diffraction spots/rings originating from Cu_2O can be identified. c) The X-ray diffraction pattern of the electrodeposits.

and the crystallites of cuprous oxide by comparing the intensity of the diffraction rings.^[4,10] According to the standard diffraction database, the intensities of (200) of copper and (111) of cuprous oxide should be similar if their amount is the same. Yet Figure 4b indicates that the ring corresponding to (111) of cuprous oxide is much fainter than that of (200) of copper. We therefore conclude that the concentration of Cu_2O crystallite in the wire is very low. Figure 4c shows the X-ray diffraction pattern of the electrodeposits. Since the electrodeposits are very thin, the overall diffraction signal is weak. Yet, from Figure 4c, one may still see that the diffraction from Cu_2O is much weaker than that from copper, indicating the low concentration of Cu_2O crystallites in the electrodeposits.

Two factors are essential for the formation of the arrays of copper wire on the substrate. One is that the copper crystallites should be nucleated on the substrate and form a robust

metal deposit, instead of generating an aggregate floating in the aqueous solutions;^[17–19] the second factor is that the electrodeposits should follow the patterns on the substrate. The nucleation barrier for copper electrocrystallization is the key to fulfilling these requirements. In electrocrystallization, the nucleus should be electrically connected to the cathode. As we demonstrated previously, the concave corner of the copper deposit and the substrate is the most favorable site for nucleation when the thickness of the electrolyte layer becomes sufficiently thin.^[20] Now we consider nucleation of the copper crystallite on the substrate on Sites 1,2, as shown in Figure 5. On Site 1, the nucleus contacts the grown copper wire and the aminosilane stripe. On Site 2, the nucleus contacts the copper

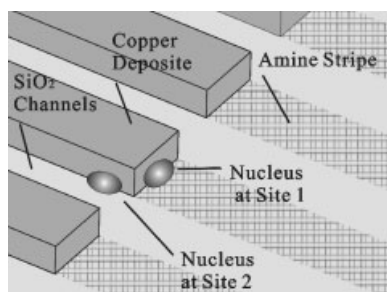


Figure 5. A schematic diagram showing the possible sites of nucleation in electrodeposition. On Site 1, the nucleus contacts the copper wire and the aminosilane stripe, whereas on Site 2 the nucleus contacts the copper wire and the silicon dioxide. The nucleation barriers on these two sites are different.

wire and the silicon dioxide substrate. It is known that in aqueous solution the interfacial energy of amino-terminated surfaces (stripes) is about 2.5 N m^{-1} ^[21] and that of silicon dioxide (channels) is of the order of 1.1 N m^{-1} .^[22] Naturally, one may expect that the decrease in the free energy by forming a nucleus on Site 1, ΔG_1^* , is larger than that on Site 2, ΔG_2^* . This means that formation of copper crystallites along the stripes of aminosilane is thermodynamically favored, and thus the wires, as shown in Figure 2, can be formed.

The key challenges in producing desired nanostructures over a large application area are the simplicity, low cost, and high throughput of the fabrication method. The template-assisted electrodeposition reported in this communication demonstrates that we have found a promising way of generating arrays of straight copper wires on a solid substrate. The width of the copper wires, and the separation between them, can be controlled by the template. We need to emphasize that this method is not limited to copper electrodeposition. As a matter of fact, by selecting proper organic functionalized molecules, one may generate a patterned substrate with a specific distribution of surface free energy, on which metal crystallites will be selectively deposited. Therefore, this method can be applied to the fabrication of micro- or nanostructures of other metals. We expect that the direct and alternating electric properties of these metallic structures may have potential applications in microelectronics and optoelectronics.

Experimental

Langmuir–Blodgett (LB) films were prepared with commercial Langmuir–Blodgett film balances (KSV 3000 and NIMA 6100). The trough was filled with ultrapure water (Millipore, resistivity $18.2 \text{ M}\Omega \text{ cm}$). 1- α -Dipalmitoylphosphatidylcholine (DPPC) (Fluka) was dissolved in chloroform (HPLC, Baker) and its concentration was 2.0 mM . Silicon substrates (WaterNet Co., orientation [100]) were cleaned and oxidized in an O_2 plasma (Templa System 100-E plasma system) [16]. The oxidized silicon surface was hydrophilic. The DPPC monolayer was kept in a low-pressure state ($2.0\text{--}3.0 \text{ mN m}^{-1}$) at a constant temperature (25°C). The monolayer was transferred onto the silicon surface by dipping the cleaned substrate into the trough and pulling it at a rate of $20\text{--}60 \text{ mm min}^{-1}$. In this way, regular line patterns with an adjustable periodicity between $300\text{--}1000 \text{ nm}$ and a stripe/channel width ratio between $1\text{--}10$ could be reproducibly obtained. The periodicity could be controlled by the transfer speed, whereas the stripe/channel width ratio was determined by the surface pressure. Chemically active structures with $-\text{NH}_2$ termination were obtained by immersing the DPPC-striped substrate into a 1 mM solution of 3-aminopropyltrimethoxysilane (APTMS, Aldrich) in 1-phenyloctane (Aldrich) for 15 min . Thereafter the polymeric residuals and DPPC stripes were removed by ultrasonication in chloroform and the silicon was cut into $20 \times 20 \text{ mm}^2$ pieces. Meanwhile, a two-dimensional template with periodic stripes of aminosilane and silicon dioxide was ready for electrocrystallization. Figures 1a–d schematically illustrate the basic strategy for fabricating periodically functionalized surfaces. The experimental details are similar to those reported previously [16].

The electrodeposition was carried out in a cell with two parallel, straight electrodes made of copper foil slices (99.9%) $40 \mu\text{m}$ thick. The electrodes were sandwiched between two rigid boundaries, one of which was a silicon wafer with a functionalized surface, and the other was a normal glass microscope slice. The separation of the electrodes was 8 mm . The electrolyte solution was prepared by analytical reagent CuSO_4 (Fluka) and Millipore ultrapure water. The concentration of CuSO_4 electrolyte was 0.07 M ($\text{pH } 3.4$). A schematic diagram of the experimental setup is shown in Figures 1e,f. To generate an ultrathin electrolyte layer for electrochemical deposition, we solidified the CuSO_4 solution by decreasing the temperature, using a Peltier element beneath the electrodeposition cell and a thermostatted circulator. Solidification started from the bottom glass plate. Meanwhile, CuSO_4 was partially expelled from the solid (this effect is known as the partitioning effect in crystallization [23]). As a result, the concentration of CuSO_4 increased in front of the ice–electrolyte interface. A very low solidification rate had to be used in order to prevent cellular interfacial morphology. On the other hand, it is known that the temperature at which an electrolyte solidifies (melting point/solidification point) depends on the concentration of electrolyte. For CuSO_4 , the solidification temperature decreases as the salt concentration is increased. Therefore, when equilibrium was reached at a set temperature (-4.5°C , for example), there existed an ultrathin layer of concentrated CuSO_4 electrolyte between the electrolyte ice and the template substrate. In our experiments, electrodeposition was carried out in this ultrathin layer, where the CuSO_4 concentration was expected not to exceed the saturated concentration at the setting temperature. To achieve a flat, homogeneous electrolyte–solid interface, great care should be taken at the beginning of the solidification to keep only one, or just a few, ice nuclei in the system. Several melting–solidification cycles were repeated to fulfill this requirement. The electrodeposits were observed with a field-emission SEM (LEO 1530 VP) and further analyzed with an atomic force microscope (Nanoscope IIIa) and a transmission electron microscope (FEGCM 2000).

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Microcontact Printing of CdS/Dendrimer Nanocomposite Patterns on Silicon Wafers**

By Xiao C. Wu, Alexander M. Bittner,* and Klaus Kern

Most photonics and electronics applications will eventually require parallel structuring methods for the control of the spatial positioning of functional materials, for example, multicolor LEDs, color pixels for field-emission displays, or multi-

channel chemical sensors. μ CP is an interesting alternative to standard patterning techniques (such as lithography) due to its speed, simplicity, cheapness, and capability for high spatial resolution.^[1] Patterning of functional materials via μ CP is mainly based on printing passivating self-assembled monolayers (SAMs)^[2] rather than the functional materials themselves. The advantage is that the interaction mechanisms between substrate, stamp, and ink (solution or suspension to cover the stamp) are well studied.^[3] However, progress has recently been made in direct printing of various functional materials.^[4,5] In order to achieve this a compatible stamp–material–surface system is needed. Successful printing often requires detailed studies of various printing conditions such as ink species (functional material), concentration, printing time, and surface properties of both stamp and substrate.

Nanoparticles might play an important role in future technologies such as photovoltaics, switches, phosphors, LEDs, electronic data-storage systems, and sensors.^[6] Parallel schemes for positioning of these nanoparticles in fixed arrays are the basis of such potential photonic and electronic devices.^[7] However, the direct patterning of these nanoparticles from solution with standard patterning techniques, such as laser ablation of the material and deposition through a shadow mask, are cumbersome, since the nanoparticles are often protected by organic surfactants (stabilizers) that tend to desorb at elevated temperature. Because the surfactants are usually necessary to stabilize the particles and to improve their functionality, any patterning approach must be carried out at low temperatures and in a mild environment.

Dendrimers are highly branched, spherically shaped molecules^[8] (when the number of generations (G) is greater than four—here we employ generation-eight dendrimers). They are composed of core, repetitive unit, and terminal groups. The repetitive unit, here $-N-(CH_2CH_2-CONHCH_2CH_2-)_2$, and the terminal group, here amine, can exhibit different functionalities according to requirements. Dendrimers can be used as nanoreactors for the synthesis of various nanoparticles, such as metals, semiconductors, and magnetic oxides.^[9–12] Based on dendrimer architecture, terminal groups, the loading factor of metal ions in dendrimers, and ion–ion interactions, nanoparticles can grow inside a dendrimer (internal type),^[9] outside a dendrimer (external type), or in both modes.^[10]

The big advantage of printing such composites is that—through the terminal groups—dendrimers can form layers on various surfaces via electrostatic forces, hydrogen bonds, van der Waals interactions, or metal–ligand interactions.^[13,14] We believe that organic dendrimers will become a promising host material for the synthesis of nanoparticles and for their subsequent controlled placement on surfaces. Here, we combine the functionality of dendrimers as nanoreactors with their facile adsorption on surfaces. We demonstrate direct printing of luminescent CdS/dendrimer nanocomposites on hydroxyl-terminated silicon wafers.

We found that dendrimers can easily be transferred from hydrophilic stamps to the hydroxyl-terminated substrate.^[5] Figure 1a shows polyamidoamine (PAMAM) dendrimer (gen-

[*] Dr. A. M. Bittner, Dr. X. C. Wu, Prof. K. Kern
Max-Planck-Institut für Festkörperforschung,
Heisenbergstr. 1, D-70569 Stuttgart (Germany)
E-mail: a.bittner@fkf.mpg.de

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