###### Add syncE and PTP input references to frequency and phase protection groups

***Objective:***

The objective of this test case is to add syncE and PTP input references to frequency and phase protection groups. Once inputs are added, frequency and phase should lock to the input references. Note: Oper State will transition from Acquiring Lock to Locked (if successful) and this could take up to 5 minutes.

***Procedure:***

* Add syncE input reference to a frequency protection group:

sync protection-groups frequency-protection-group FreqProtGrp synce-input-reference SyncE\_Input\_Port\_34

* Add PTP input reference to a phase protection group:

sync protection-groups phase-protection-group PhaseProtGrp ptp- input-reference PTP\_Input\_Port34

* You should see something like the following from VALCli:

5162\_0003> show sync frequency protection-groups

+ SYNC FREQUENCY PROTECTION GROUPS

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| Name | Threshold QL | Oper State | Selected Ref | Forced Selection | Ref Switch Count

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| FreqProtGrp | QL-PRS | Locked | SyncE\_Input\_Port\_34 | False | 1

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5162\_0003> show sync phase protection-groups

+ SYNC PHASE PROTECTION GROUPS

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| Name | Threshold Clock Class | Oper State | Selected Ref | Forced Selection | Ref Switch Count |

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| PhaseProtGrp | 6 | Locked | PTP\_Input\_Port34 | False | 1

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Test Case Results:

Passed: Yes No Verified by Date/Time Comments