```
source("synthesis_maximin.R")
# source("synthesis.R")
```

Parameters and initial setup

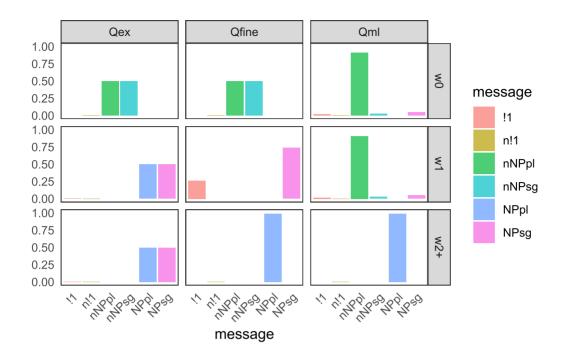
```
lambda <- 5 # Rationality parameter</pre>
n < -1
cost orig <- costp()</pre>
cost_neg_cheap \leftarrow costp(nNPsg = 0.1, nNPpl = 0.1, `n!1` = 2.6)
cost neg exa cheap <- costp(nNPsg = 0.1, nNPpl = 0.1, `!1` = 0.9, `n!1` = 1,
+2 = 0.9, n+2 = 1)
# cost <- cost neg cheap
cost <- cost neg exa cheap
# cost <- cost orig</pre>
P w flat <- P wp()
P_w_sg_odd <- P_wp(w1 = 1 / 10)
P_{w_pl_odd} \leftarrow P_{wp}(`w2+` = 1 / 10)
P_w_sg_odd_extreme \leftarrow P_wp(w1 = 1 / 1000)
P w pl odd extreme \leftarrow P wp('w2+' = 1 / 1000)
P_Q_flat <- P_Qp()</pre>
P Q Qml \leftarrow P Qp(Qml = 10)
P_Q_Qex \leftarrow P_Qp(Qex = 10)
P_Q_Qfine \leftarrow P_Qp(Qfine = 10)
P_i_flat <- P_ip()
P i penalize embedded <- P ip(ExhExh = 1, ExhLit = 1.1, LitLit = 1.21)
# P i penalize embedded <- P ip(ExhExh = 1, ExhLit = 1.4, LitLit = 2)
# P i penalize embedded <- P ip(ExhExh = 1, ExhLit = 3, LitLit = 9)</pre>
# P_i <- P_i_penalize_embedded</pre>
P_i <- P_i_flat
P i("iLitLitLitLit")
```

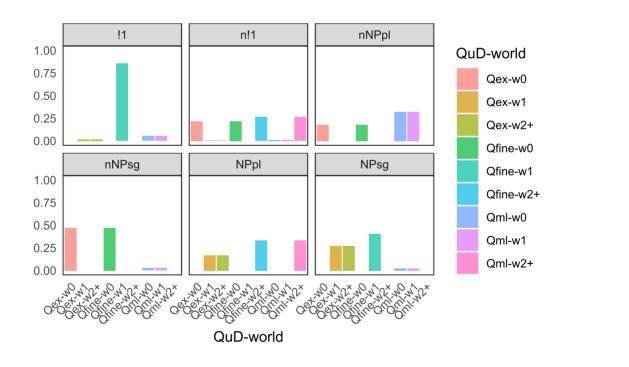
```
[1] 0.0625
```

Flat world and QuD priors

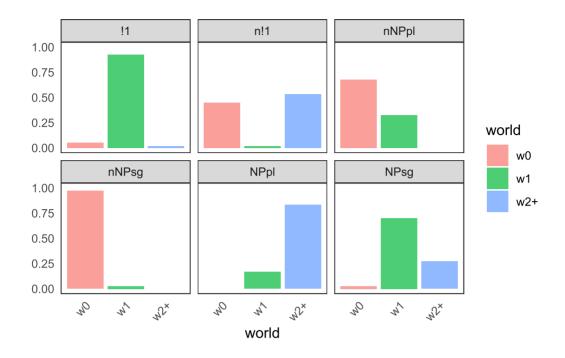
```
P_w <- P_w_flat
P_Q <- P_Q_flat

check_Sn(n)
```



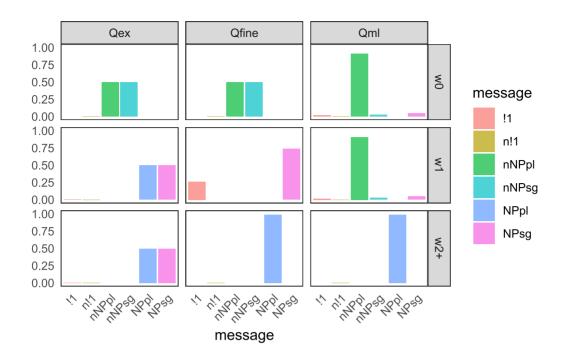


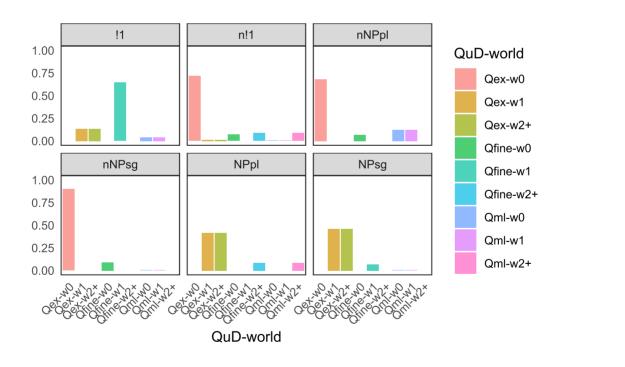
check_Ln_w(n)



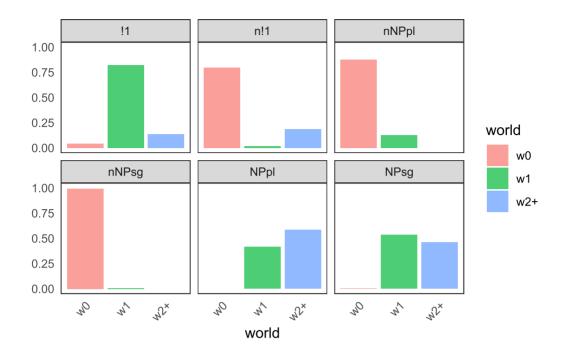
Flat world priors, Qex bias

```
P_w <- P_w_flat
P_Q <- P_Q_Qex
check_Sn(n)</pre>
```





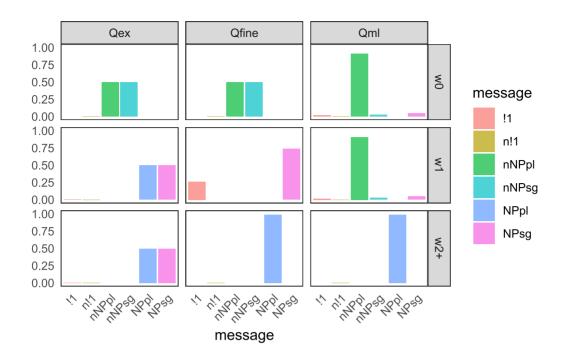
check_Ln_w(n)

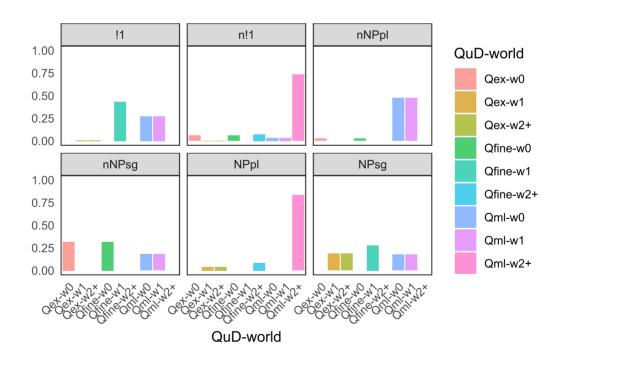


Flat world priors, Qml bias

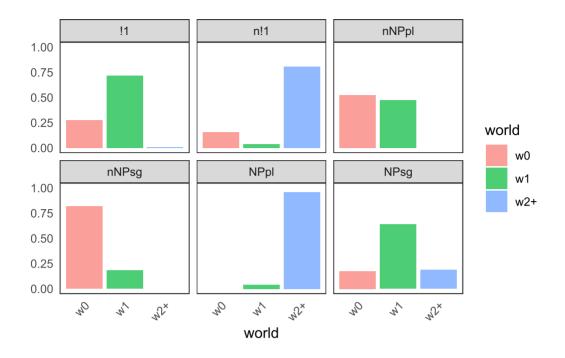
```
P_w <- P_w_flat
P_Q <- P_Q_Qml

check_Sn(n)
```



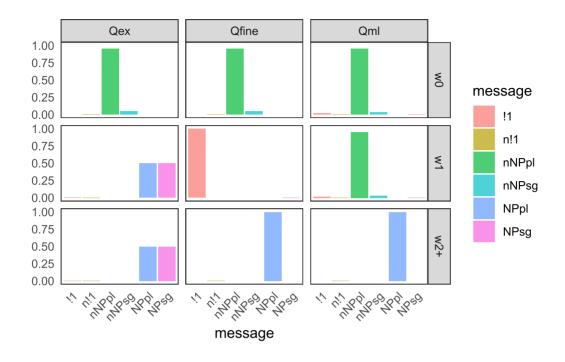


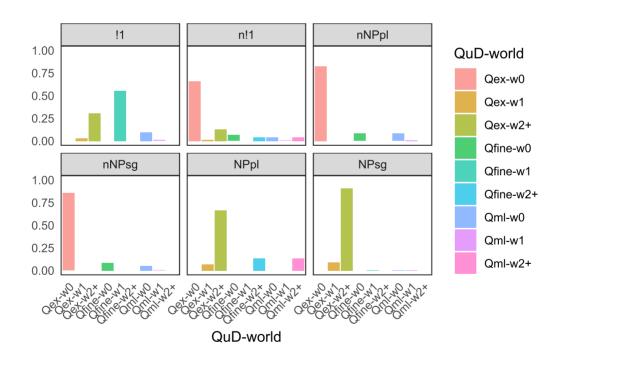
check_Ln_w(n)



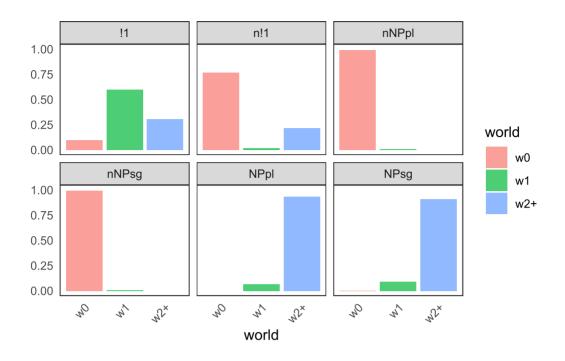
check_Sn_w(2)

nNPsg oddness (w1 biased against, Qex bias)





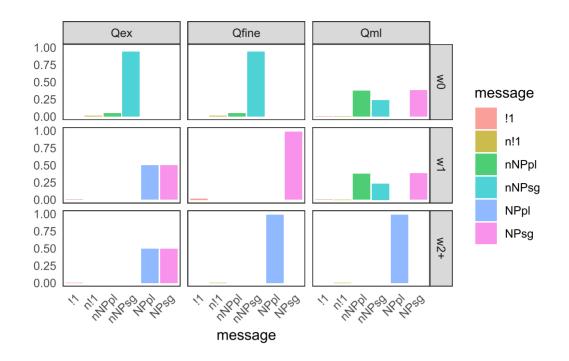
check_Ln_w(n)

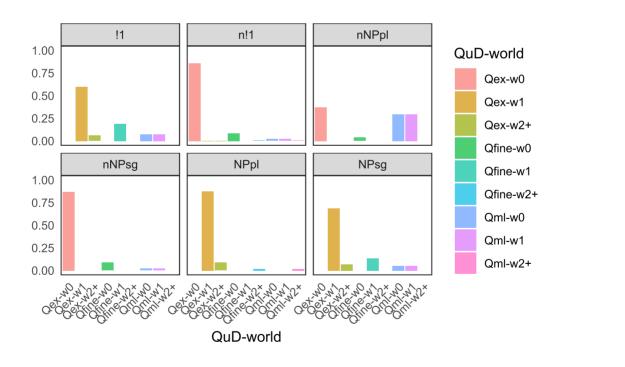


nNPpl oddness (w2+ biased againt, Qex bias)

check_Sn(n)

```
P_w <- P_w_pl_odd
P_Q <- P_Q_Qex
LO() %>% filter(QuD == "Qml" & world == "w0")
# A tibble: 96 \times 5
  world QuD
               message inter
                                       prob
  <chr> <chr> <chr>
                       <chr>
                                      <dbl>
         Qml
1 w0
               !1
                       iExhExhExhExh
                                          0
2 w0
         Qml
               !1
                       iExhExhExhLit
 3 w0
         Qml
              !1
                       iExhExhLitExh
4 w0
         Qml
              !1
                       iExhExhLitLit
                                          0
5 w0
         Qml
              !1
                       iExhLitExhExh
                                          0
         Qml
6 w0
              !1
                       iExhLitExhLit
7 w0
         Qml
              ! 1
                       iExhLitLitExh
                                          0
8 w0
                       iExhLitLitLit
                                          0
         Qml
              !1
9 w0
         Oml
                       iLitExhExhExh
10 w0
                       iLitExhExhLit
                                          0
         Qml
               !1
# i 86 more rows
```





check_Ln_w(n)

