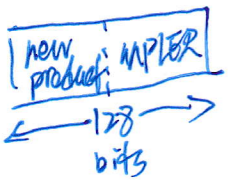
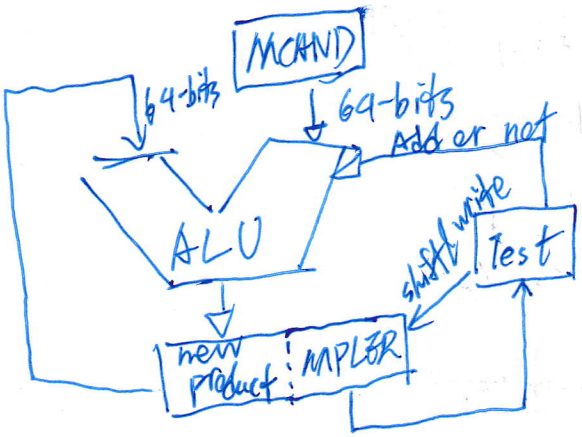
HW6. Q2

64-bits adder





64 runs

1 0000 ------ 0000 64 bits

× 1000 ------ 0101 64 bits

↓↓

1000 ------ 0000

× 1000 ------ 010 → 63-bits

+ 1000 ------ 0000 64-bits

↓↓

~~+ 000~~

~~+ 0000~~

1000 ------ 0000

× 1000 ------ 01 → 62-bits → shift right

1000 ------ 0000

shift left ← + 0000 ------ 0000 → 65-bits

⋮

Sum should be 128 bits