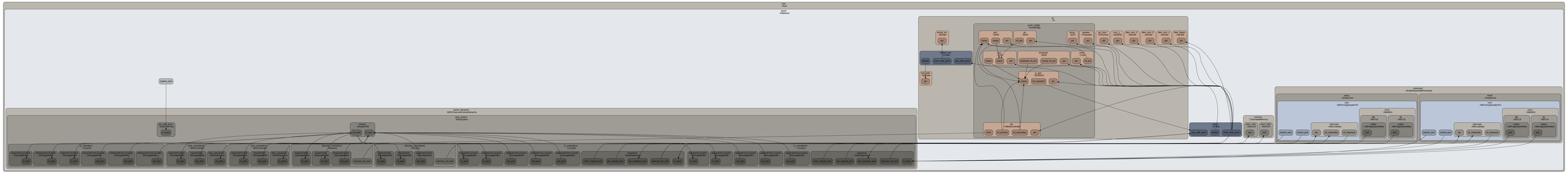

root
: Root

board
: X86Board

processor
: SimpleSwitchableProcessor

start0

: SimpleCore

core

: X86TimingSimpleCPU

mmu

: X86MMU

itb
: X86TLB

walker
: X86PagetableWalker

dtb
: X86TLB

walker
: X86PagetableWalker

interrupts
: X86LocalApic

start1

: SimpleCore

core

: X86TimingSimpleCPU

mmu

: X86MMU

itb
: X86TLB

walker
: X86PagetableWalker

dtb
: X86TLB

walker
: X86PagetableWalker

interrupts
: X86LocalApic

memory
: ChanneledMemory

mem_ctrl0
: MemCtrl

mem_ctrl1
: MemCtrl

cache_hierarchy
: MESITwoLevelCacheHierarchy

ruby_system
: RubySystem

network
: SimplePt2Pt

l1_controllers0
: L1Cache

requestFromL1Cache
: MessageBuffer

responseFromL1Cache
: MessageBuffer

unblockFromL1Cache
: MessageBuffer

requestToL1Cache
: MessageBuffer

responseToL1Cache
: MessageBuffer

sequencer
: RubySequencer

l1_controllers1
: L1Cache

requestFromL1Cache
: MessageBuffer

responseFromL1Cache
: MessageBuffer

unblockFromL1Cache
: MessageBuffer

requestToL1Cache
: MessageBuffer

responseToL1Cache
: MessageBuffer

sequencer
: RubySequencer

l2_controllers
: L2Cache

DirRequestFromL2Cache
: MessageBuffer

L1RequestFromL2Cache
: MessageBuffer

responseFromL2Cache
: MessageBuffer

unblockToL2Cache
: MessageBuffer

L1RequestToL2Cache
: MessageBuffer

responseToL2Cache
: MessageBuffer

directory_controllers0
: Directory

requestToDir
: MessageBuffer

responseToDir
: MessageBuffer

responseFromDir
: MessageBuffer

directory_controllers1
: Directory

requestToDir
: MessageBuffer

responseToDir
: MessageBuffer

responseFromDir
: MessageBuffer

dma_controllers0
: DMAController

responseFromDir
: MessageBuffer

requestToDir
: MessageBuffer

dma_sequencer
: DMASequencer

dma_controllers1
: DMAController

responseFromDir
: MessageBuffer

requestToDir
: MessageBuffer

dma_sequencer
: DMASequencer

sys_port_proxy
: RubyPortProxy

pc
: Pc

south_bridge
: SouthBridge

pic1
: I8259

pic2
: I8259

cmos

: Cmos

dma1
: I8237

keyboard
: I8042

pit
: I8254

speaker
: PcSpeaker

io_apic
: I82094AA

ide
: X86IdeController

pci_host
: PcPciHost

com_1
: Uart8250

fake_com_2
: IsaFake

fake_com_3
: IsaFake

fake_com_4
: IsaFake

fake_floppy
: IsaFake

default_bus
: IOXBar

empty_isa
: IsaFake

bad_addr
: BadAddr

iobus
: IOXBar

system_port

in_ports icache_port

in_ports

dcache_port int_responder int_requestor port port

interrupt_out_port

icache_port pio

in_ports

dcache_port int_responder int_requestor port port

interrupt_out_port

port pio

memory_out_port

port

memory_out_port

in_port

out_port out_port out_port out_port out_port out_port out_port out_port out_port out_port out_port out_port out_port

out_port

in_port in_port in_port in_port in_ports in_port in_ports in_port in_port in_port in_port in_port in_port mem_request_port pio_request_port pio_response_port in_port in_port mem_request_port pio_request_port pio_response_port

output

inputs

inputs pio

mem_side_ports

inputs output

int_pin

int_primary int_secondary

pio

pio

pio

keyboard_int_pin mouse_int_pin pio

int_pin pio pio

int_requestor

cpu_side_ports

pio

dma pio

pio pio pio pio pio pio

cpu_side_ports

default

default mem_side_ports

pio

pio